GENERAL PURPOSE PLASTIC SILICON RECTIFIER

BY133

DO-41P
Axial Plastic Package

ABSOLUTE MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS
(Ratings at T_a = 25°C unless specified otherwise, single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%)

Description	Symbols	BY133	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	1300	V
Maximum RMS Voltage	V_{RMS}	910	V
Maximum DC Blocking Voltage	V_{DC}	1300	V
Maximum Average Forward Rectified Current 0.375" (9.5mm) Lead Length at T_A=75ºC	$I_{(AV)}$	1.0	A
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	30	A
Maximum Forward Voltage at 1.0A and 25ºC	V_F	1.1	V
Maximum Reverse Current at Rated DC Blocking Voltage T_A=25°C T_A=100ºC	I_R	5.0 200	µA
Typical Junction Capacitance (Note 1)	C_j	15	pF
Typical Thermal Resistance (Note 2)	$R_{\Theta JA}$	50	ºC/W
Operating and Storage Temperature Range	Tstg	-55 to +150	ºC

NOTES:

1- Measured at 1 MH_Z and applied reverse voltage of 4.0 $V_{DC.}$
2- Thermal resistance from Junction to Ambient 0.375" (9.5mm) lead length PCB Mounted

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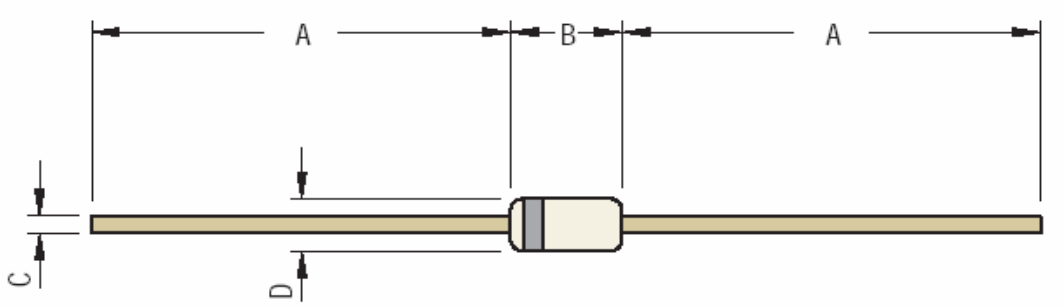
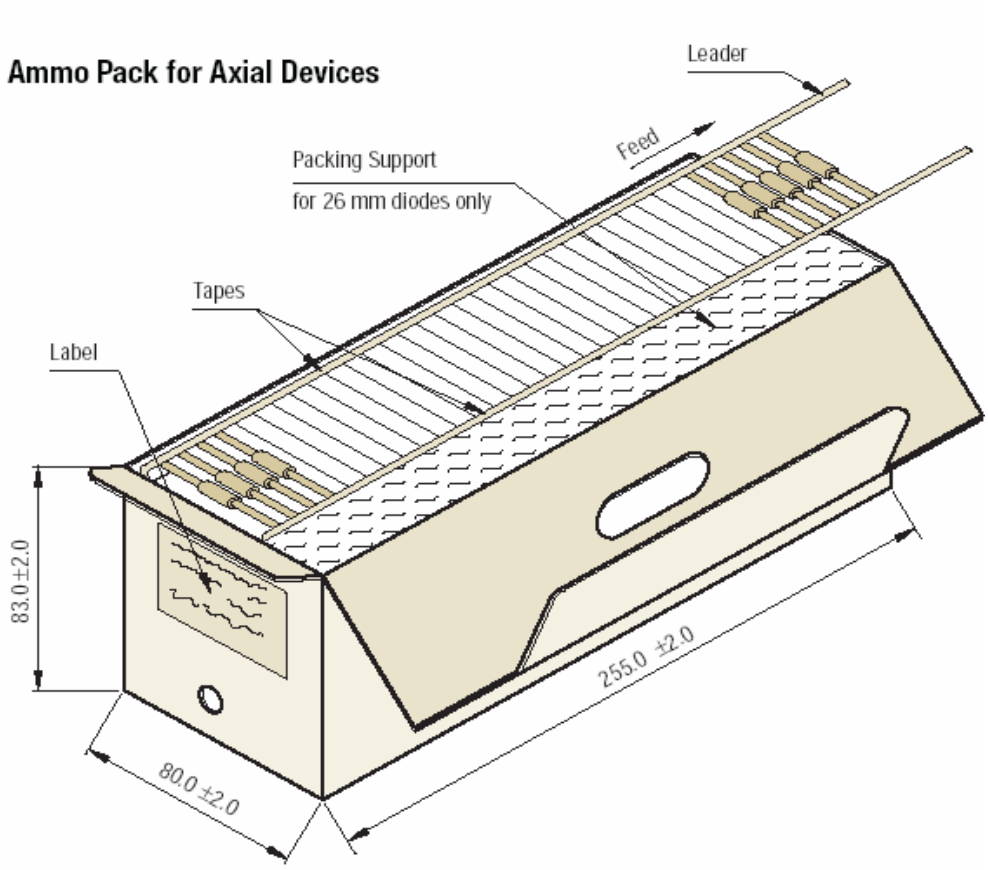


DIM	Min	Max
A	25.40	
B	4.20	5.20
C	0.70	0.90
D	2.00	2.70

All Dimensions are in mm

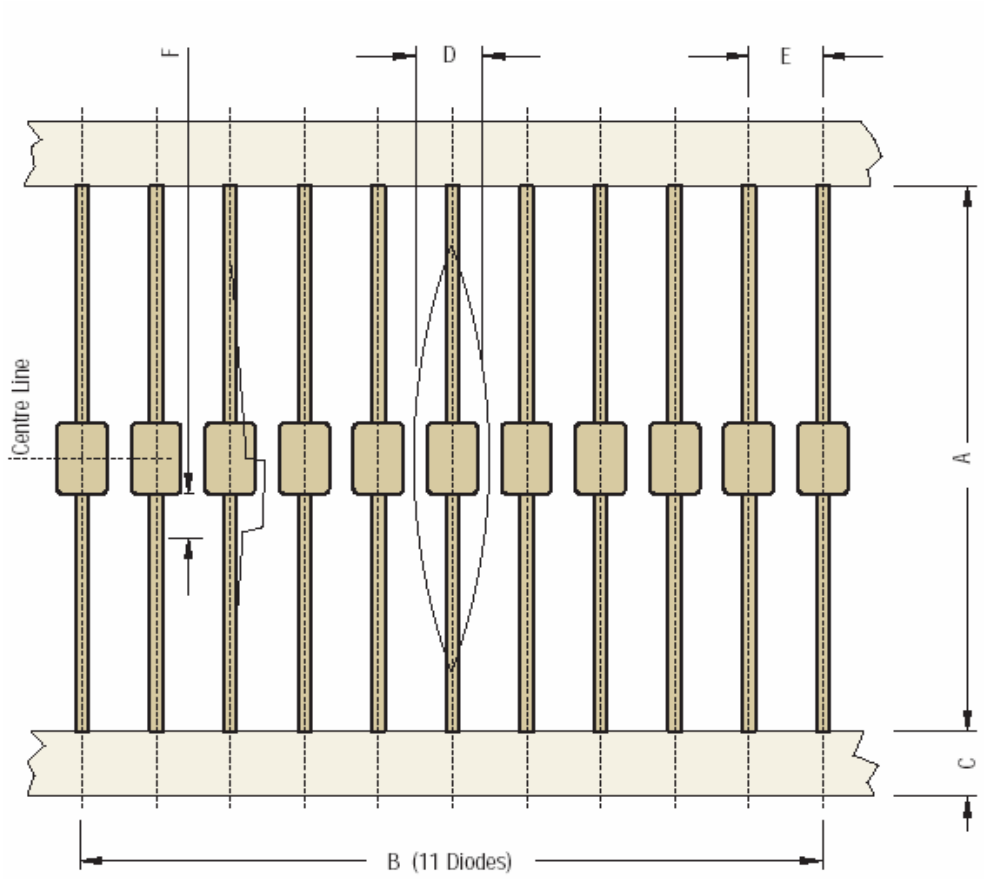


All Dimensions are in mm

Packaging Information

Package/	Packaging Type	Std. Packing	Inner Carton			Outer Carton		
Case Type		Qty	Qty	Size L x W x H	Gross Weight	Qty	Size L x W x H	Gross Weight
				(cm)	(Kg)		(cm)	(Kg)
DO-41P	T&A	5,000	5K	27 x 8 x 14	1.96	45K	46 x 35 x 25	17.5

T & A: Tape and Ammo Pack



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DO-41P 52 mm Tape		
DIM	**Min**	**Max**
A	50.0	54.0
B	95.0	105.0
C	5.60	6.50
D		1.5R
E	9.50	10.50
F		1.25

All Dimensions are in mm

TAPE SPECIFICATIONS

1. 300 mm (Min) leader tape on every roll.
2. No. of empty places allowed 0.25% without consecutive empty places.
3. Ends of leads shall normally not protrude beyond the tapes.
4. Components shall be held sufficiently in the tape or tapes so that they can not come free in normal handling.

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Customer Notes

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