



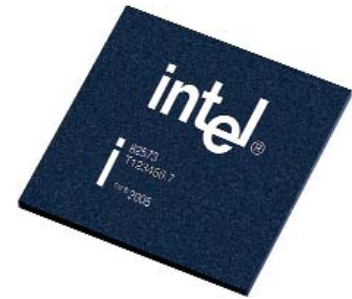
# Intel® 82573E/V/L Gigabit Ethernet Controllers

## High-performance Gigabit Network Connectivity with Intel® Active Management Technology

- High-performing, PCI Express\*-based 10/100/1000 Ethernet connection
- Supports Intel® Active Management Technology<sup>4</sup> (Intel® AMT) to help IT discover, heal, and protect their computing assets (Intel® 82573E Gigabit Ethernet Controller)
- Simple installation and maintenance with Intel® SingleDriver™ technology
- Single-component design for compact LAN on motherboard (LOM) implementation with reduced bill of materials (BOM)
- Available with Intel® lead-free<sup>1</sup> technology

The Intel® 82573E/V/L Gigabit Ethernet Controllers are compact single-port integrated 10/100/1000 Mbps copper network-connection components for desktop, workstation, and value-server designs with critical space constraints. The 82573E supports Intel AMT, which allows IT to remotely discover, heal, and protect their computing assets. The 82573V supports ASF 2.0 and Advanced Pass Through modes for legacy manageability applications. The 82573L offers lower power consumption for non-managed platforms.

- **Intel Active Management Technology:** The Intel 82573E controller is a key platform ingredient for enabling Intel AMT by providing always-available network connectivity and an integrated management engine. Intel AMT allows IT to discover, heal, and protect all the computing assets on their corporate network. With Intel AMT, IT managers can discover all their hardware and software computing assets. Because asset information is stored in non-volatile memory, IT can read it even if the PC is off or the operating system (OS) is inoperable. IT managers can heal



systems remotely through Intel AMT's out-of-band management capabilities, which allow remote diagnosis and repair of PCs after software, OS, or hardware failures. Intel AMT stores software and virus-protection version information that is consistent and up-to-date across the enterprise, helping protect systems against malicious software attacks. The 82573E is recommended for use on the Intel® Professional Business Platform based on the Intel® 945 Express Chipset family, which supports Intel AMT.

- **Small-footprint Gigabit Ethernet implementation with integrated PCI Express (PCIe\*) support:** All three controllers allow original equipment manufacturers (OEMs) to implement Gigabit Ethernet on their platforms in a compact board area with reduced BOM. Each has fully integrated Gigabit Ethernet Media Access Control (MAC) and Physical-Layer (PHY) functions, and each provides a standard IEEE 802.3\* Ethernet interface for 1000BASE-T, 100BASE-TX, and 10BASE-T applications. The controller's host bus utilizes the PCIe architecture (Revision 1.0a) designed for high-performance and low-memory latency. The Intel 82573E/V/L

controllers offer reduced system BOM opportunities by providing integrated voltage regulators and the ability to share the system BIOS Flash instead of requiring a separate EEPROM for local area network (LAN) configuration data.

- **Performance-enhancing features:** Each of the Intel 82573E/V/L controllers includes advanced interrupt-handling features and uses efficient ring-buffer descriptor data structures with on-chip caching for up to 64 packet descriptors per queue. Other performance-enhancing features include offloading tasks from the host via Transmission Control Protocol/User Datagram Protocol/Internet Protocol (TCP/UDP/IP) checksum calculations and TCP segmentation.

- **Simplified installation and maintenance with Intel® SingleDriver™ technology:** These controllers are supported by Intel SingleDriver technology, which allows one driver to work for all Intel® PRO/1000 Gigabit Ethernet connections. Intel SingleDriver technology simplifies network connection installation, management, and maintenance.
- **Available with lead-free<sup>1</sup> technology:** These Intel® controllers are packaged in a lead-free, 15x15 mm, 196-ball grid array for space-saving installation. Built on Intel lead-free technology, these controllers conform to the European Union’s Restrictions on the use of Hazardous Substances (RoHS)<sup>2</sup> and Japan’s White Goods Recycling Act.

### Controller Overview

| Controller | Distinguishing Feature               | Order Code |
|------------|--------------------------------------|------------|
| 82573E     | Intel® AMT <sup>A</sup>              | RC82573E   |
| 82573E     | Lead-free <sup>1</sup> and Intel AMT | PC82573E   |
| 82573V     | ASF 2.0 and APT                      | RC82573V   |
| 82573V     | Lead-free, ASF 2.0, and APT          | PC82573V   |
| 82573L     | Low power                            | RC82573L   |
| 82573L     | Lead-free and low power              | PC82573L   |

### Specifications: Intel® 82573E/V/L Gigabit Ethernet Controllers

| Electrical  |   |
|---|---|
| Power supply  | <ul style="list-style-type: none"> <li>• 3.3 V, 2.5 V, 1.2 V</li> </ul>   |
| Typical targeted power dissipation (82573E and 82573V only) | <ul style="list-style-type: none"> <li>• 1370 mW at 1000 Mbps active</li> <li>• 535 mW at 100 Mbps active</li> <li>• 592 mW at 10 Mbps active</li> <li>• 177 mW at no link and no wake</li> </ul> |
| Typical targeted power dissipation (82573L only)            | <ul style="list-style-type: none"> <li>• 1217 mW at 1000 Mbps active</li> <li>• 483 mW at 100 Mbps active</li> <li>• 504 mW at 10 Mbps active</li> <li>• 53 mW at no link and no wake</li> </ul>  |
| Packaging   |   |
| Operating temperature                                       | <ul style="list-style-type: none"> <li>• 0° C to 70° C (maximum); simplified thermal design</li> </ul>  |
| Storage temperature   | <ul style="list-style-type: none"> <li>• -40° C to 125° C</li> </ul>  |
| Package type  | <ul style="list-style-type: none"> <li>• Lead-free<sup>1</sup> 196-pin TF-BGA, 1.0 mm ball pitch, 15x15 mm (simplifies LOM board designs)</li> </ul>  |

## Features

## Benefits

### PCI Express\* Features

|   |  |
|---|--|
| X1 PCIe* interface on Intel® I/O Controller Hub 7 (ICH7) or Memory Control Hub (Intel® 945/955 chipset) devices | <ul style="list-style-type: none"> <li>• Bus sharing not required</li> <li>• Low-latency path to memory</li> </ul>   |
| 2 Gbps peak bandwidth per direction   | <ul style="list-style-type: none"> <li>• Supports Gigabit Ethernet at wire speed</li> </ul>  |
| PCI Express Rev 1.0a specification  | <ul style="list-style-type: none"> <li>• Standards interoperability</li> <li>• Provides PCIe power-management capabilities for PC and embedded applications</li> </ul> |
| High bandwidth density per pin  | <ul style="list-style-type: none"> <li>• Less-congested board routing</li> </ul>   |

### Gigabit MAC/PHY Advanced Features

|   |   |
|---|---|
| Wide, pipelined internal data path architecture   | <ul style="list-style-type: none"> <li>• Low-latency data handling</li> <li>• Superior direct memory access (DMA) transfer rate performance</li> </ul>      |
| Optimized transmit (Tx) and receive (Rx) queues   | <ul style="list-style-type: none"> <li>• Network packet handling without waiting or buffer overflow</li> </ul>  |
| 32 KB configurable Rx and Tx first-in/first-out (FIFO)  | <ul style="list-style-type: none"> <li>• FIFO size adjustable to application</li> </ul>   |
| IEEE 802.3x*-compliant flow-control support with software controllable pause times and threshold values | <ul style="list-style-type: none"> <li>• Reduce frame loss from Rx FIFO overruns</li> <li>• Hardware or software control over Tx of pause frames</li> </ul> |
| Programmable host memory Rx buffers (256 B-16 KB)   | <ul style="list-style-type: none"> <li>• Efficient use of system memory and PCIe bus</li> </ul>   |
| Descriptor ring management hardware for Tx and Rx   | <ul style="list-style-type: none"> <li>• Simple software programming model</li> </ul>   |
| Mechanism for reducing interrupts from Tx/Rx operations   | <ul style="list-style-type: none"> <li>• Maximizes system performance and throughput</li> </ul>   |
| Integrated PHY for 10/100/1000 Mbps (full- and half-duplex)   | <ul style="list-style-type: none"> <li>• Smaller footprint, lower power dissipation compared to multichip MAC and PHY solutions</li> </ul>                  |
| IEEE 802.3ab* auto-negotiation support  | <ul style="list-style-type: none"> <li>• Automatic link configuration for speed, duplex, flow control</li> </ul>  |
| IEEE 802.3ab PHY compliance and compatibility   | <ul style="list-style-type: none"> <li>• Robust operation over installed base of Category-5 twisted-pair cabling</li> </ul>                                 |

### Host Offloading Features

|  |   |
|--|---|
| Tx/Rx IP, TCP, and UDP checksum offloading   | <ul style="list-style-type: none"> <li>• Lower CPU utilization</li> </ul>   |
| Tx TCP segmentation  | <ul style="list-style-type: none"> <li>• Increased throughput and lower CPU utilization</li> <li>• Compatible with large send offload (in Microsoft Windows* 2000)</li> </ul>   |
| IPv6 offloading  | <ul style="list-style-type: none"> <li>• Checksum and segmentation capability extended to new standard packet type</li> </ul>   |
| Advanced packet filtering  | <ul style="list-style-type: none"> <li>• 16 exact-matched packets (unicast or multicast)</li> <li>• 4096-bit hash filter for multicast frames</li> <li>• Promiscuous (unicast and multicast) transfer mode support</li> <li>• Optional filtering of invalid frames</li> </ul> |
| IEEE 802.1q* Virtual Local Area Network (VLAN) support with VLAN tag insertion, stripping, and packet filtering for up to 4096 VLAN tags | <ul style="list-style-type: none"> <li>• Ability to create multiple VLAN segments</li> </ul>  |
| Descriptor ring management hardware for Tx and Rx  | <ul style="list-style-type: none"> <li>• Optimized fetch and write-back mechanisms for efficient system memory and PCIe bandwidth usage</li> </ul>  |

### Manageability Features

|  |   |
|--|---|
| Intel® Active Management Technology <sup>4</sup> (82573E only)       | <ul style="list-style-type: none"> <li>• Out of Band (OOB) access allows remote management of PCs regardless of system power or OS state</li> <li>• Remote troubleshooting and recovery significantly reduces desk-side visits and increases IT efficiency</li> <li>• Proactive alerting and event logging decreases downtime and minimizes time to repair</li> <li>• Third-party non-volatile storage prevents users from removing critical inventory, remote control, or virus protection agents</li> <li>• Remote HW and SW asset tracking eliminates time-consuming manual inventory tracking and reduces asset accounting costs</li> </ul> |
| Intel® SingleDriver™ technology                                      | <ul style="list-style-type: none"> <li>• One driver supports all Intel® PRO/1000 connections, simplifying installation and maintenance</li> </ul>   |
| Alerting Standards Format 2.0 (82573V only)                          | <ul style="list-style-type: none"> <li>• Standard alerting capability to notify IT of system events</li> </ul>  |
| Advanced Pass Through (82573V only)                                  | <ul style="list-style-type: none"> <li>• Filtering and redirection for external server Board Management Controller (BMC) data</li> </ul>  |
| Boot ROM Preboot eXecution Environment (PXE) Flash interface support | <ul style="list-style-type: none"> <li>• Support for PXE ROMs integrated into the system BIOS</li> </ul>  |
| SDG 3.0, WiF 3.0 and PC2001 compliant                                | <ul style="list-style-type: none"> <li>• Remote network management through Desktop Management Interface (DMI) 2.0 and SNMP</li> </ul>   |
| Wake on LAN support  | <ul style="list-style-type: none"> <li>• Packet recognition and wake-up for LOM applications without software configuration</li> </ul>  |

### Additional Device Features Features

|  |   |
|--|---|
| Three LED outputs  | <ul style="list-style-type: none"> <li>• Provides the ability to display the various link, speed, and activity states of the LAN</li> </ul>   |
| Programmable LED functionality   | <ul style="list-style-type: none"> <li>• Software definable function (speed, link, activity) and blinking allow flexible LED signaling implementations</li> </ul>                                       |
| Internal phase-locked loop (PLL) for clock generation can use 25-MHz crystal | <ul style="list-style-type: none"> <li>• Lower component count and reduced system cost</li> </ul>   |
| JTAG (IEEE 1149.1*) Test Access Port built in silicon                        | <ul style="list-style-type: none"> <li>• Simplified testing using boundary scan</li> </ul>  |
| On-chip power control circuitry  | <ul style="list-style-type: none"> <li>• Reduced number of on-board power supply regulators</li> <li>• Simplified power supply design for less power-critical applications</li> </ul>                   |
| Loop-back capabilities   | <ul style="list-style-type: none"> <li>• Allows OEMs to quickly check silicon integrity on their manufacturing line</li> </ul>  |
| SPI Flash or EEPROM support  | <ul style="list-style-type: none"> <li>• Flexibility for external EEPROM or SPI Flash device to store LAN configuration data. SPI Flash may be shared with system BIOS for reduced BOM cost.</li> </ul> |
| Optional on-die voltage regulator  | <ul style="list-style-type: none"> <li>• Flexibility for an internal 2.5 V regulator for reduced BOM cost</li> </ul>  |

## For more information, contact your Intel sales representative.

<sup>1</sup>Lead has not been intentionally added, but lead may still exist as an impurity below 1000 ppm.

<sup>2</sup>Lead and other materials banned in the RoHS Directive are either (1) below all applicable substance thresholds as proposed by the EU or (2) an approved/pending exemption applies.

<sup>4</sup>Intel® Active Management Technology requires a system with an Intel® 945G Express Chipset, Intel® PRO/1000 PM network connection, and appropriate third-party software.

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