INJ0003AX SERIES

・PRELIMINARY

Notice: This is not a final specification
Some parametric are subject to change.

High speed switching
Silicon P-channel MOSFET

DESCRIPTION

INJ0003AX is a Silicon P-channel MOSFET.
This product is most suitable for low voltage use such as portable machinery , because of low voltage drive and low on resistance.

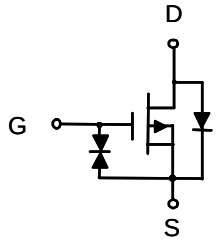
FEATURE

・Input impedance is high, and not necessary to consider a drive electric current.
・Vth is low, and drive by low voltage is possible. Vth=−0.6～−1.2V
・Low on Resistance. Ron=2Ω(TYP)
・High speed switching.
・Small package for easy mounting.

APPLICATION

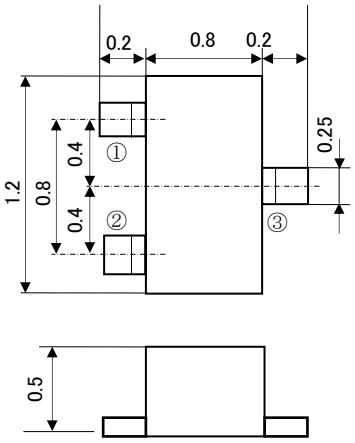
high speed switching , Analog switching

EQUIVALENT CIRCUIT



OUTLINE DRAWING Unit:mm

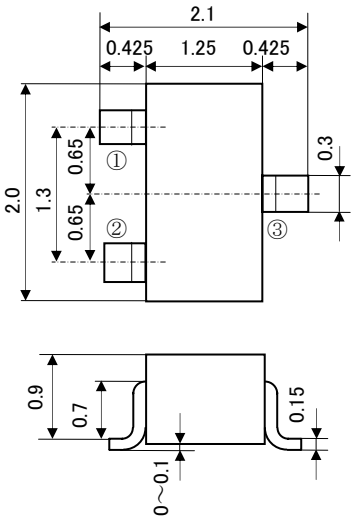
INJ0003AT2



JEITA, JEDEC:—
ISAHAYA:T-USM

TERMINAL CONNECTOR
①:GATE
②:SOURCE
③:DRAIN

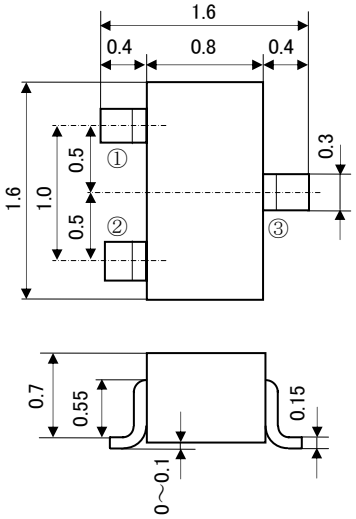
INJ0003AM1



JEITA:SC-70
JEDEC:—

TERMINAL CONNECTOR
①:GATE
②:SOURCE
③:DRAIN

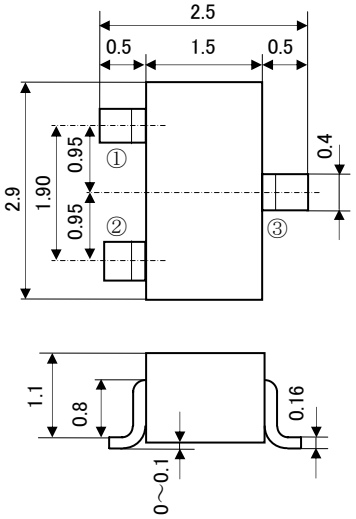
INJ0003AU1



JEITA:SC-75A
JEDEC:—

TERMINAL CONNECTOR
①:GATE
②:SOURCE
③:DRAIN

INJ0003AC1



JEITA:SC-59
JEDEC:Similar to TO-236

T TERMINAL CONNECTOR
①:GATE
②:SOURCE
③:DRAIN

INJ0003AX SERIES

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High speed switching
Silicon P-channel MOSFET

MAXIMUM RATING(Ta＝25℃)

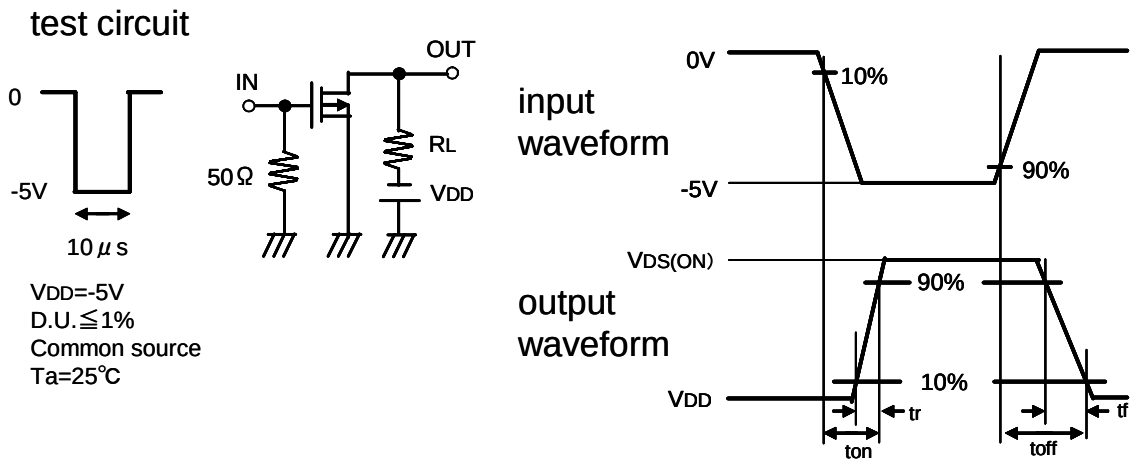
SYMBOL	PARAMETER	RATING				UNIT
		INJ0003AT2	INJ0003AU1	INJ0003AM1	INJ0003AC1	
V_{DSS}	Drain-source voltage	−20				V
V_{GSS}	Gate-source voltage	±8				V
I_D	Drain current	−200				mA
P_D	Total power dissipation (Ta=25℃)	125(※)	150	200		mW
Tch	Channel temperature	+125	+150			℃
Tstg	Range of Storage temperature	−55～+125	−55～+150			℃

ELECTRICAL CHARACTERISTICS(Ta＝25℃)

※package mounted on 9mm×19mm×1mm glass-epoxy substrate.

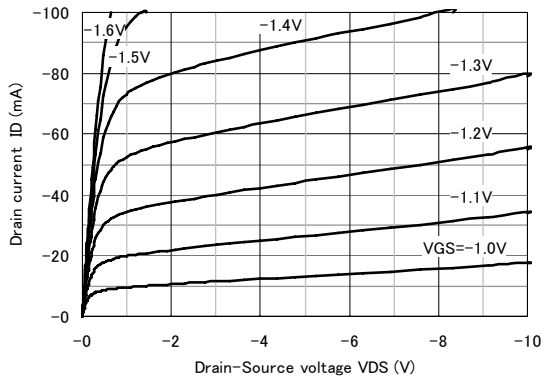
SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
			MIN	TYP	MAX	
$V_{(BR)DSS}$	Drain-source breakdown voltage	I_D=−100μA, V_{GS}=0V	−20	−	−	V
I_{GSS}	Gate-source leak current	V_{GS}=±5V, V_{DS}=0V	−	−	±0.5	μA
I_{DSS}	Zero gate voltage drain current	V_{DS}=−20V ,V_{GS}=0V	−	−	−50	μA
V_{th}	Gate threshold voltage	I_D=−250μA, V_{DS}= V_{GS}	−0.6	−	−1.2	V
$\|Y_{fs}\|$	Forward transfer admittance	V_{DS}=−10V, I_D=−0.1A	−	280	−	mS
$R_{DS(ON)}$	Static drain-source on-state resistance	I_D=−100mA, V_{GS}=−4.0V	−	2	−	Ω
C_{iss}	Input capacitance	V_{DS}=−10V, V_{GS}=0V,f=1MHz	−	37	−	pF
C_{oss}	Output capacitance	V_{DS}=−10V, V_{GS}=0V,f=1MHz	−	12	−	pF
t_{ON}	Switching time	V_{DD}=−5V , I_D=−10mA V_{GS}=0～−5V	−	16	−	ns
t_{OFF}			−	110	−	

Switching time test condition

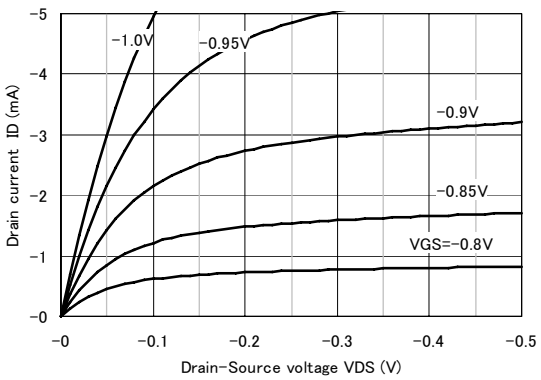


TYPICAL CHARACTERISTICS

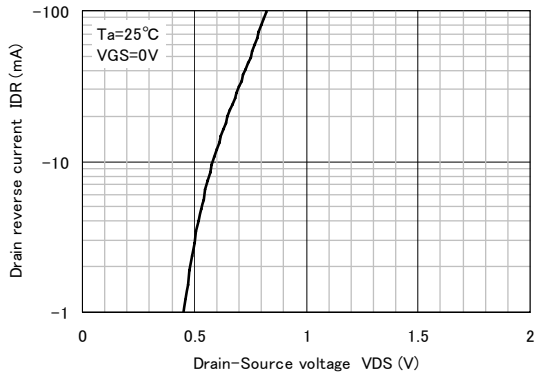
ID -VDS



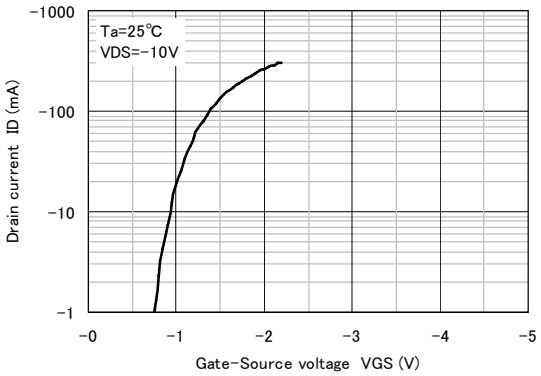
ID -VDS(Low voltage region)



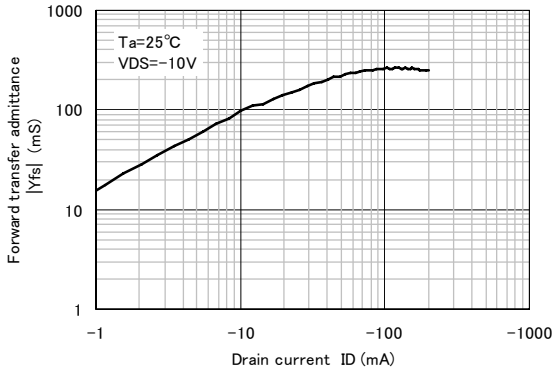
IDR -VDS



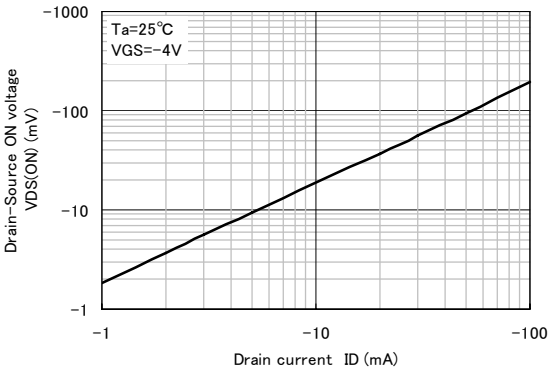
ID -VGS



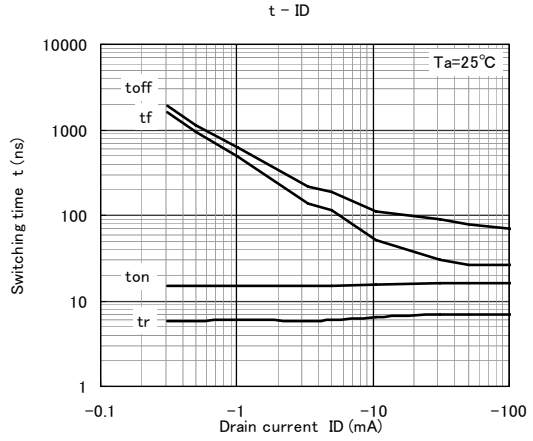
|Yfs| - ID



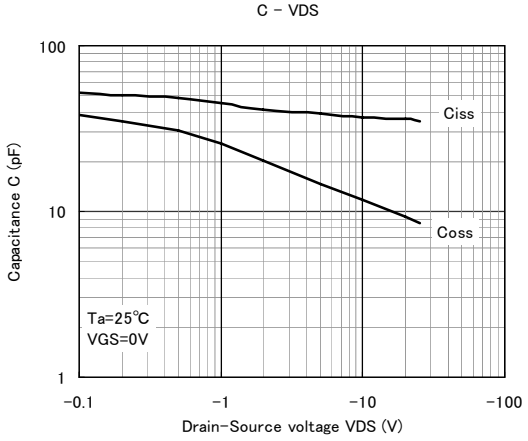
VDS(ON) -ID



t - ID



C - VDS



ISAHAYA ELECTRONICS CORPORATION

Marketing division, Marketing planning department

6-41 Tsukuba, Isahaya, Nagasaki, 854-0065 Japan

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Apr.2007