

# LMT035DNAFWU-1

# LCD Module User Manual

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Date: 2013-03-04	Date:	Date:

Rev.	Descriptions	Release Date
0.1	Preliminary New release	2007-09-29
0.2	Typing correction on General Specification	2010-08-10
0.3	Update Cover Sheet and General Specification	2011-03-22
0.4	Add 7.3 Data Transaction Timing	2013-03-04
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7.3 **8.** 

9.

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# 1. APPLICATIONS

This Module is designed for potable DVD, GPS car TV & PMP(portable multimedia player) application which require high quality flat panel displays. It is also a good substitute for many outmoded CSTN module in the industrial application.

This product is composed of a TFT-LCD panel, driver ICs, FPC and LED backlight unit.

# 2. General Specification

Signal Interface : Digital 24-bits RGB
Display Technology : a-Si TFT active matrix

Display Mode: TN Type Full Color / Transmissive / Normal White

Screen Size(Diagonal): 3.5"

Outline Dimension : 76.9 x 63.9 x 3.7 (mm)

(see attached drawing for details)

Active Area : 70.08 x 52.56 (mm)

Number of dots : 320 x 3 (RGB) x 240

Pixel Pitch : 0.219 x 0.219 (mm)

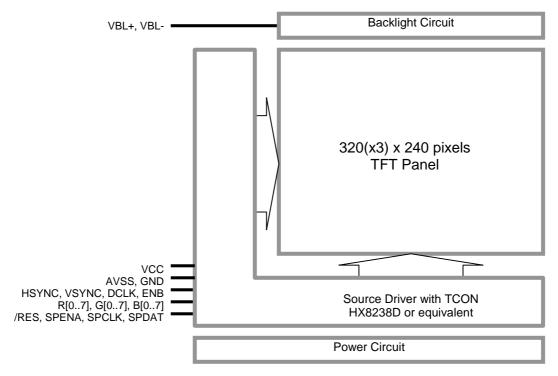
Pixel Configuration: RGB Stripe

Backlight: LED

Surface Treatment : Anti-Glare Treatment

Viewing Direction : 12 o'clock Operating Temperature :  $-20 \sim +70^{\circ}$ C Storage Temperature :  $-30 \sim +80^{\circ}$ C

# 3. Block Diagram



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# 4. Terminal Function (Input Terminal)

1     VBL-     Power     Negative Backlight Power Supply       2     VBL-       3     VBL+     Positive Backlight Power Supply       4     VBL+       5     NC       :     :       7     NC       8     /RES     Input       9     SPENA     Input     Serial port Enable (*1)       10     SPCLK     Input     Serial port clock (*1)       11     SPDAT     Input     Serial port Data input (*1)       12     B0     Input     8bit Data for Blue       :     :     :       19     B7     8bit Data for Green       :     :     :       20     G0     Input     8bit Data for Red       :     :     :       27     G7       28     R0     Input     8bit Data for Red       :     :     :       35     R7       36     HSYNC     Input     Horizontal Sync Input       37     VSYNC     Input     Vertical Sync Input	
2         VBL-           3         VBL+           4         VBL+           5         NC           1         No Connection           1         NC           8         /RES         Input         Hardware Reset           9         SPENA         Input         Serial port Enable (*1)           10         SPCLK         Input         Serial port clock (*1)           11         SPDAT         Input         Serial port Data input (*1)           12         B0         Input         8bit Data for Blue           19         B7         8bit Data for Green           20         G0         Input         8bit Data for Red           27         G7         8bit Data for Red           28         R0         Input         8bit Data for Red           35         R7         Input         Horizontal Sync Input	
4         VBL+           5         NC           1         NC           8         /RES         Input         Hardware Reset           9         SPENA         Input         Serial port Enable (*1)           10         SPCLK         Input         Serial port clock (*1)           11         SPDAT         Input         Serial port Data input (*1)           12         B0         Input         8bit Data for Blue           19         B7         B7           20         G0         Input         8bit Data for Green           10         SPC         Input         8bit Data for Red           10         Input         8bit Data for Red         Input           10         Input         Bott Data for Red         Input	
5         NC         -         No Connection           7         NC         NC         8         /RES         Input         Hardware Reset           9         SPENA         Input         Serial port Enable (*1)           10         SPCLK         Input         Serial port clock (*1)           11         SPDAT         Input         Serial port Data input (*1)           12         B0         Input         8bit Data for Blue           :         :         :           19         B7         B7           20         G0         Input         8bit Data for Green           :         :         :           27         G7         B           28         R0         Input         8bit Data for Red           :         :         :           35         R7           36         HSYNC         Input         Horizontal Sync Input	
Imput	
8 /RES Input Hardware Reset 9 SPENA Input Serial port Enable (*1) 10 SPCLK Input Serial port clock (*1) 11 SPDAT Input Serial port Data input (*1) 12 B0 Input Serial port Data input (*1) 13 B7 20 G0 Input Serial port Data for Blue 1	
8 /RES Input Hardware Reset 9 SPENA Input Serial port Enable (*1) 10 SPCLK Input Serial port clock (*1) 11 SPDAT Input Serial port Data input (*1) 12 B0 Input Serial port Data input (*1) 13 B7 20 G0 Input Serial port Data for Blue 1	
9 SPENA Input Serial port Enable (*1) 10 SPCLK Input Serial port clock (*1) 11 SPDAT Input Serial port Data input (*1) 12 B0 Input Sbit Data for Blue : : :	
10 SPCLK Input Serial port clock (*1) 11 SPDAT Input Serial port Data input (*1) 12 B0 Input Sbit Data for Blue : : :	
11         SPDAT         Input         Serial port Data input (*1)           12         B0         Input         8bit Data for Blue           :         :         :           19         B7            20         G0         Input         8bit Data for Green           :         :            27         G7           28         R0         Input         8bit Data for Red           :         :            35         R7           36         HSYNC         Input         Horizontal Sync Input	
12         B0         Input         8bit Data for Blue           :         :         :           19         B7         .           20         G0         Input         8bit Data for Green           :         :         .           27         G7         .           28         R0         Input         8bit Data for Red           :         :         .           35         R7         .           36         HSYNC         Input         Horizontal Sync Input	
:       :         19       B7         20       G0         !       Input         27       G7         28       R0         !       Input         35       R7         36       HSYNC         Input       Horizontal Sync Input	
20       G0       Input       8bit Data for Green         :       :       :         27       G7       .         28       R0       Input       8bit Data for Red         :       :         35       R7         36       HSYNC       Input       Horizontal Sync Input	
20       G0       Input       8bit Data for Green         :       :       :         27       G7       .         28       R0       Input       8bit Data for Red         :       :         35       R7         36       HSYNC       Input       Horizontal Sync Input	
28 R0 Input 8bit Data for Red  : : :	
28 R0 Input Sbit Data for Red : : : 35 R7	
: : : : : : : : : : : : : : : : : : :	
36 HSYNC Input Horizontal Sync Input	
36 HSYNC Input Horizontal Sync Input	
37 VSYNC Input Vertical Sync Input	
38 DCLK Input Clock Signal for RGB data	
Latching at the rising edge	
39 NC No Connection	
40 NC	
41 VCC Power Digital Power Supply	
42 VCC	
43 NC - No connection	
<u>: : : : : : : : : : : : : : : : : : : </u>	
51 NC	
52 ENB Input Data Enable	
53 GND Power Power GND (0V)	
54 AVSS Power Power GND (0V)	,

#### Note:

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<sup>\*1</sup> The SPI terminal is for Gamma and internal configuration.

<sup>\*2</sup> For digital RGB input data format, both SYNC mode and DE+SYNC mode are supported. If ENB signal is fixed low, SYNC mode is used. Otherwise, DE+SYNC is used.

# 5. Absolute Maximum Ratings

GND, V<sub>SS</sub>=0V, T<sub>OP</sub>=25°C

Items	Symbol	Min.	Max.	Unit	Condition
Supply Voltage	$V_{CC}$	-0.3	+4.0	V	$V_{SS} = 0V$
for Source Driver					
Digital Input Voltage	V <sub>IN</sub>	-0.3	V <sub>DD</sub> +0.3	V	$V_{SS} = 0V$
Operating Temperature	T <sub>OP</sub>	-20	70	°C	No Condensation
Storage Temperature	T <sub>ST</sub>	-30	80	°C	No Condensation

#### Note:

- \*1. This rating applies to all parts of the module. And should not be exceeded.
- \*2. Maximum wet-bulb temperature is 39. Condensation of dew must be avoided.
- \*3. The operating temperature only guarantees operation of the circuit. The contrast, response speed, and the other specification related to electro-optical display quality is determined at the room temperature, Ta=25.
- \*4. Ambient temperature when the backlight is lit (reference value)
- \*5 Any Stresses exceeding the Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

## 6. Electrical Characteristics

#### 6.1 DC Characteristics

GND,  $V_{SS}$ =0V,  $V_{CC}$ =3.3V,  $T_{OP}$ =25°C

Items	Symbol	MIN.	TYP.	MAX.	Unit
Digital Supply Voltage	$V_{CC}$	3.0	3.3	3.6	V
Digital Operating Current	I <sub>cc</sub>	-	7.5	-	mA
Signal Input Voltage High	$V_{SIG-H}$	0.8xV <sub>cc</sub>	-	V <sub>cc</sub>	V
Signal Input Voltage Low	$V_{SIG-L}$	0	-	$0.2xV_{CC}$	<b>V</b>
Frame Frequency	F <sub>FRAME</sub>	-	60	-	Hz
Dot Data Clock	D <sub>CLK</sub>	-	6.5	-	MHz

#### Note:

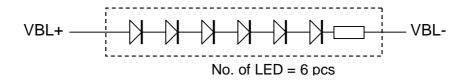
### 6.2 LED Backlight Circuit Characteristics

 $V_{BL}=0V$ ,  $If_{BL}=20mA$ ,  $T_{OP}=25^{\circ}C$ 

Items	Symbol	MIN.	TYP.	MAX.	Unit	Note
Forward Voltage	$Vf_{BLA}$	ı	20.0	-	V	
Forward Current	If <sub>BLA</sub>	-	20.0	22.0	mΑ	
Life Time	-	-	50000	-	hr	

Cautions:

Exceeding the recommended driving current could cause substantial damage to the backlight and shorten its lifetime.



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VCOMH& VCOML is for adjusting the color with gamma data

# 7. AC Characteristics

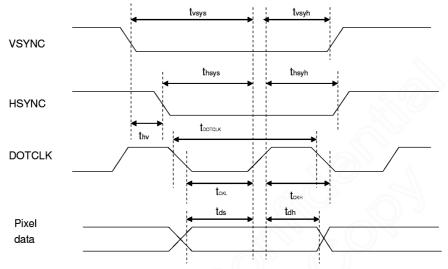
# 7.1 Pixel Timing

URL:

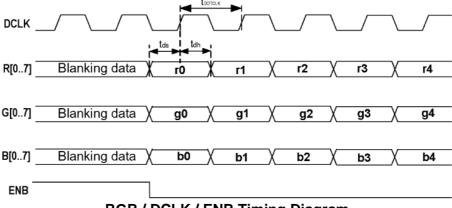
 $GND, V_{SS} = 0V, V_{CC} = 3.3V, T_{OP} = 25^{\circ}C$ 

		MI	MIN. TYP.		MAX.			
Item	Symbol	24bit	8bit	24bit	8bit	24bit	8bit	Unit
DOTCLK Freq	tdotclk	ı	1	6.5	19.5	10	30	MHz
DOTCLK Period	tdotclk	100	33.3	154	51.3	-	-	ns
Vertical Sync Setup Time	tvsys	20	10	-	-	-	-	ns
Vertical Sync Hold Time	tvsyh	20	10	-	-	-	-	ns
Horizontal Sync Setup Time	thsys	20	10	-	-	-	-	ns
Horizontal Sync Hold Time	thsyh	20	10	-	-	-	-	ns
Phase difference of Sync Signal(falling edge)	thv		-		-	24	40	tdotclk
DOTCLK Low Period	tclk	50	15	-	-	-	-	ns
DOTCLK High Period	tckh	50	15	-	-	-	-	ns
Data Setup Time	tsutdh	12	10	-	-	-	-	ns
Data Hold Time	thd	12	10	-	-	-	-	ns
Reset Pulse width	tres	1	0		•		•	ns

Note: For the details of the timing, please see the Driver IC data sheet.



**HSYNC / VSYNC / DCLK Timing Digram** 



**RGB / DCLK / ENB Timing Diagram** 

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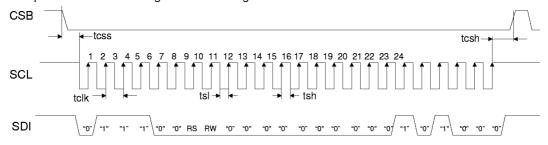


# 7.2 SPI Interface Timing

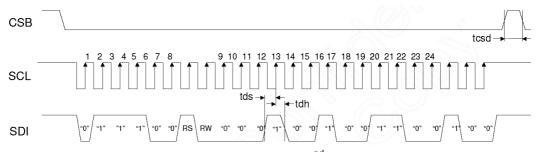
 $GND,V_{SS}=0V,V_{CC}=3.3V,T_{OP}=25^{\circ}C$ 

Item	Symbol	MIN.	TYP.	MAX.	Unit
Serial Clock Freq	fclk	-	-	20	MHz
Serial Clock Cycle Time	tclk	50	-	-	ns
Clock Low Width	tsl	25	-	-	ns
Clock High Width	tsh	25	-	-	ns
Chip Select Setup Time	tcss	0	-	-	ns
Chip Select Hold Time	tsch	10	-	-	ns
Chip Select High Delay Time	tscd	20	-	-	ns
Data Setup Time	tds	5	-	-	ns
Data Hold Time	tdh	10	-	-	ns

Note: the example below is transferring "1264h" into register "28h"



SPI Data Transfer (1st data)

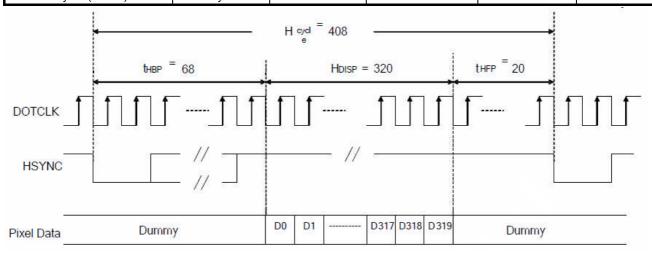


SPI Data Transfer (2<sup>nd</sup> data)

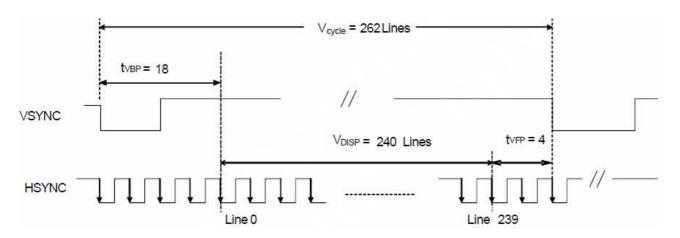


# 7.3 Data Transaction Timing

Characteristics	Symbol	MIN.	TYP.	MAX.	Unit
DOTCLK Frequency	fDOTCLK	-	6.5	10	MHz
DOTCLK Period	tDOTCLK	100	154	-	ns
Vertical Frequency (Refresh)	fV	-	60	75	Hz
Horizontal Back Porch	tHBP	-	68	-	tDOTCLK
Horizontal Front Porch	tHFP	-	20	-	tDOTCLK
Horizontal Display Area	HDISP	-	320	-	tDOTCLK
Horizontal Cycle	Hcycle	-	408	450	tDOTCLK
Vertical Back Porch	tVBP	-	18	-	Lines
Vertical Front Porch	tVFP	-	4	-	Lines
VS pulse width	tWV	-	4	-	Lines
Vertical Cycle (NTSC)	Vcycle	-	262	350	Lines



# **Horizontal Data Transaction Timing**



**Vertical Data Transaction Timing** 

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# 8. Optical Characteristics

 $I_f$ =20mA,  $V_L$ =19.2V, Ta=25C

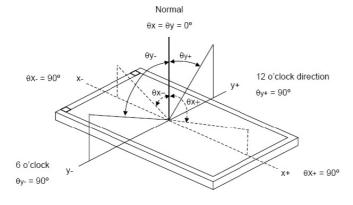
Item	Symbol	MIN.	TYP.	MAX.	UNIT	Note.
Contrast Ratio	CR	(250)	(300)	-		*1, θ=0
Luminance	$Y_L$	(200)	(250)	-	Cd/m <sup>2</sup>	θ=0
Uniformity	L <sub>U</sub>	(70)	(75)	ı	%	*4, θ=0
Response Time Rise	$T_R$	•	(10)	(20)	ms	*3, θ=0
Response Time Fall	$T_F$	•	(15)	(30)	ms	*3, θ=0
Viewing Angle	θΙ	(50)	(60)	ı	deg	*2
Viewing Angle	θr	(50)	(60)	ı	deg	*2
Viewing Angle	θu	(40)	(50)	ı	deg	*2
Viewing Angle	θd	(40)	(60)	ı	deg	*2
Color Chromaticity	Χ	•	0.30	ı	-	
(White)	Υ	•	0.32	ı	-	
Color Chromaticity	Χ	-	0.62	-	-	
(Red)	Υ	-	0.36	-	-	
Color Chromaticity	Х	-	0.33	-	-	
(Green)	Y	-	0.59	-	-	
Color Chromaticity	Х	-	0.14	-	-	
(Blue)	Y	-	0.09	-	-	

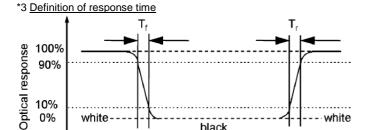
Note:

The contrast ratio could be calculate by the following expression:

Contrast Ratio (CR) = Luminanc with all pixels white / Luminance with all pixels black

### \*2 Definition of Viewing Angle

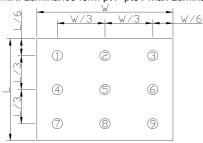




black

<u>Definition of Luminance Uniformity</u> Luminance uniformity (Lu)=

Min. Luminance form pt1~pt9 / Max Luminance form Pt1~pt9



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<sup>\*1.</sup> Definition of Contrast Ratio

# 9. Precautions of using LCD Modules

### Mounting

- Mounting must use holes arranged in four corners or four sides.
- The mounting structure so provide even force on to LCD module. Uneven force (ex. Twisted stress) should not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- It is suggested to attach a transparent protective plate to the surface in order to protect the polarizer. It should have sufficient strength in order to the resist external force.
- The housing should adopt radiation structure to satisfy the temperature specification.
- Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. Never rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics deteriorate the polarizer.)
- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer

### **Operating**

- The spike noise causes the mis-operation of circuits. It should be within the  $\pm 200 \text{mV}$  level (Over and under shoot voltage)
- Response time depends on the temperature. (In lower temperature, it becomes longer.)
- Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- When fixed patterns are displayed for a long time, remnant image is likely to occur.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference

#### **Electrostatic Discharge Control**

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### **Strong Light Exposure**

Strong light exposure causes degradation of polarizer and color filter.

#### Storage

When storing modules as spares for a long time, the following precautions are necessary.

- Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

#### **Protection Film**

- When the protection film is peeled off, static electricity is generated between the film and polarizer.
   This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt tore main on the polarizer. Please carefully peel off the protection film without rubbing it against the polarizer.
- When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

#### **Transportation**

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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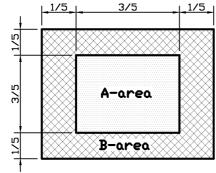
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# 10. Appendix A < Inspection items and criteria for appearance defect>

Items	Criteria								
Open Segment or Common	Not permitted								
Short	Not permitted	Not permitted							
Wrong Viewing Angle	Not permitted								
Decliners	Not permitted								
Contrast Ration Uneven	According to the limit	t specimen							
Crosstalk	According to the limit	According to the limit specimen							
White spots	X>1 pixel	A-area	Not permitted	Max 6 spots allowed					
		B-area	Max. 1 allowed						
	1/2 pixel <x≤1 pixel<="" td=""><td>A-area</td><td>Not permitted</td><td></td></x≤1>	A-area	Not permitted						
		B-area	Max. 2 allowed						
	X≤1/2 pixel	A-area	Max. 1 allowed						
		B-area	Max. 4 allowed						
Black Sport	X>1 pixel	A-area	Not permitted						
		B-area	Max. 2 allowed						
	X≤1/2 pixel	A-area	Max. 1 allowed						
		B-area	Max. 4 allowed						
Line Defect	Apparent vertical hor	rizontal line d	efects are not pern	nitted					

#### Note:

- On Pixel include 3 dots (RedDot + GreenDot + BlueDot) Definition of Panel "A-area" and "B-area"



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