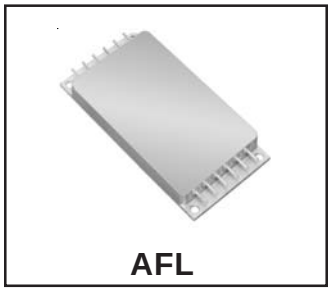
International
IOR Rectifier

PD - 94457A

AFL50XXS SERIES

50V Input, Single Output

ADVANCED ANALOG HIGH RELIABILITY HYBRID DC/DC CONVERTERS



AFL

Description

The AFL Series of DC/DC converters feature high power density with no derating over the full military temperature range. This series is offered as part of a complete family of converters providing single and dual output voltages and operating from nominal +28, +50, +120 or +270 volt inputs with output power ranging from 80 to 120 watts. For applications requiring higher output power, individual converters can be operated in parallel. The internal current sharing circuits assure equal current distribution among the paralleled converters. This series incorporates Advanced Analog's proprietary magnetic pulse feedback technology providing optimum dynamic line and load regulation response. This feedback system samples the output voltage at the pulse width modulator fixed clock frequency, nominally 550 KHz. Multiple converters can be synchronized to a system clock in the 500 KHz to 700 KHz range or to the synchronization output of one converter. Undervoltage lockout, primary and secondary referenced inhibit, soft-start and load fault protection are provided on all models.

These converters are hermetically packaged in two enclosure variations, utilizing copper core pins to minimize resistive DC losses. Three lead styles are available, each fabricated with Advanced Analog's rugged ceramic lead-to-package seal assuring long term hermeticity in the most harsh environments.

Manufactured in a facility fully qualified to MIL-PRF-38534, these converters are available in four screening grades to satisfy a wide range of requirements. The CH grade is fully compliant to the requirements of MIL-H-38534 for class H. The HB grade is fully processed and screened to the class H requirement, may not necessarily meet all of the other MIL-PRF-38534 requirements, e.g., element evaluation and Periodic Inspection (P.I.) not required. Both grades are tested to meet the complete group "A" test specification over the full military temperature range without output power deration. Two grades with more limited screening are also available for use in less demanding applications. Variations in electrical, mechanical and screening can be accommodated. Contact Advanced Analog for special requirements.

Features

- 30 To 80 Volt Input Range
- 3.3, 5, 8, 9 12, 15, 24 and 28 Volts Outputs Available
- High Power Density - up to 84 W / in^3
- Up To 120 Watt Output Power
- Parallel Operation with Stress and Current Sharing
- Low Profile (0.380") Seam Welded Package
- Ceramic Feedthru Copper Core Pins
- High Efficiency - to 85%
- Full Military Temperature Range
- Continuous Short Circuit and Overload Protection
- Remote Sensing Terminals
- Primary and Secondary Referenced Inhibit Functions
- Line Rejection > 40 dB - DC to 50KHz
- External Synchronization Port
- Fault Tolerant Design
- Dual Output Versions Available
- Standard Military Drawings Available

Specifications

ABSOLUTE MAXIMUM RATINGS		
Input Voltage	-0.5V to 100V	
Soldering Temperature	300°C for 10 seconds	
Case Temperature	Operating	-55°C to +125°C
	Storage	-65°C to +135°C

Static Characteristics $-55°C \leq T_{CASE} \leq +125°C$, $30V \leq V_{IN} \leq 80V$ unless otherwise specified.

Parameter	Group A Subgroups	Test Conditions	Min	Nom	Max	Unit
INPUT VOLTAGE		Note 6	30	50	80	V
OUTPUT VOLTAGE		V_{IN} = 50 Volts, 100% Load				
AFL5005S	1		4.95	5.00	5.05	V
AFL5008S	1		7.92	8.00	8.08	V
AFL5009S	1		8.91	9.00	9.09	V
AFL5012S	1		11.88	12.00	12.12	V
AFL5015S	1		14.85	15.00	15.15	V
AFL5028S	1		27.72	28.00	28.28	V
AFL5005S	2, 3		4.90		5.10	V
AFL5008S	2, 3		7.84		8.16	V
AFL5009S	2, 3		8.82		9.18	V
AFL5012S	2, 3		11.76		12.24	V
AFL5015S	2, 3		14.70		15.30	V
AFL5028S	2, 3		27.44		28.56	V
OUTPUT CURRENT		V_{IN} = 30, 50, 80 Volts - Note 6				
					16.0	A
AFL5005S					10.0	A
AFL5008S					10.0	A
AFL5009S					9.0	A
AFL5012S					8.0	A
AFL5015S					4.0	A
AFL5028S						
OUTPUT POWER		Note 6				
					80	W
AFL5005S					80	W
AFL5008S					90	W
AFL5009S					108	W
AFL5012S					120	W
AFL5015S					112	W
AFL5028S						
MAXIMUM CAPACITIVE LOAD		Note 1	10,000			µfd
OUTPUT VOLTAGE TEMPERATURE COEFFICIENT		V_{IN} = 50 Volts, 100% Load - Note 1, 6	-0.015		+0.015	%/°C
OUTPUT VOLTAGE REGULATION						
AFL5028S Line	1, 2, 3	No Load, 50% Load, 100% Load	-70.0		+70.0	mV
All Others Line	1, 2, 3	V_{IN} = 30, 50, 80 Volts	-20.0		+20.0	mV
Load	1, 2, 3		-1.0		+1.0	%
OUTPUT RIPPLE VOLTAGE		V_{IN} = 30, 50, 80 Volts, 100% Load,				
AFL5005S	1, 2, 3	BW = 10MHz			30	mV_{pp}
AFL5008S	1, 2, 3				40	mV_{pp}
AFL5009S	1, 2, 3				40	mV_{pp}
AFL5012S	1, 2, 3				45	mV_{pp}
AFL5015S	1, 2, 3				50	mV_{pp}
AFL5028S	1, 2, 3				100	mV_{pp}

For Notes to Specifications, refer to page 4

Static Characteristics (Continued)

Parameter	Group A Subgroups	Test Conditions	Min	Nom	Max	Unit
INPUT CURRENT		V_{IN} = 50 Volts				
No Load	1	I_{OUT} = 0			50	mA
	2, 3				60	mA
Inhibit 1	1, 2, 3	Pin 4 Shorted to Pin 2			5	mA
Inhibit 2	1, 2, 3	Pin 12 Shorted to Pin 8			5	mA
INPUT RIPPLE CURRENT		V_{IN} = 50 Volts, 100% Load, BW = 10MHz				
AFL5005S	1, 2, 3				60	mA_{pp}
AFL5008S	1, 2, 3				60	mA_{pp}
AFL5009S	1, 2, 3				60	mA_{pp}
AFL5012S	1, 2, 3				60	mA_{pp}
AFL5015S	1, 2, 3				60	mA_{pp}
AFL5028S	1, 2, 3				60	mA_{pp}
CURRENT LIMIT POINT		V_{OUT} = 90% V_{NOM} , V_{IN} = 50 Volts				
As a percentage of full rated load		Note 5				
	1		115		125	%
	2		105		115	%
	3		125		140	%
LOAD FAULT POWER DISSIPATION		V_{IN} = 50 Volts				
Overload or Short Circuit	1, 2, 3				32	W
EFFICIENCY		V_{IN} = 50 Volts, 100% Load				
AFL5005S	1, 2, 3		78	81		%
AFL5008S	1, 2, 3		79	82		%
AFL5009S	1, 2, 3		80	83		%
AFL5012S	1, 2, 3		81	84		%
AFL5015S	1, 2, 3		82	85		%
AFL5028S	1, 2, 3		82	84		%
ENABLE INPUTS (Inhibit Function)						
Converter Off	1, 2, 3	Logical Low on Pin 4 or Pin 12	-0.5		0.8	V
Sink Current		Note 1			100	µA
Converter On	1, 2, 3	Logical High on Pin 4 and Pin 12 - Note 9	2.0		50	V
Sink Current		Note 1			100	µA
SWITCHING FREQUENCY	1, 2, 3		500	550	600	KHz
SYNCHRONIZATION INPUT						
Frequency Range	1, 2, 3		500		700	KHz
Pulse Amplitude, Hi	1, 2, 3		2.0		10	V
Pulse Amplitude, Lo	1, 2, 3		-0.5		0.8	V
Pulse Rise Time		Note 1			100	nSec
Pulse Duty Cycle		Note 1	20		80	%
ISOLATION	1	Input to Output or Any Pin to Case (except Pin 3). Test @ 500VDC	100			MΩ
DEVICE WEIGHT		Slight Variations with Case Style		85		gms
MTBF		MIL-HDBK-217F, AIF @ T_C = 40°C	300			KHrs

For Notes to Specifications, refer to page 4

Dynamic Characteristics -55°C ≤ T_{CASE} ≤ +125°C, V_{IN}=50V unless otherwise specified.

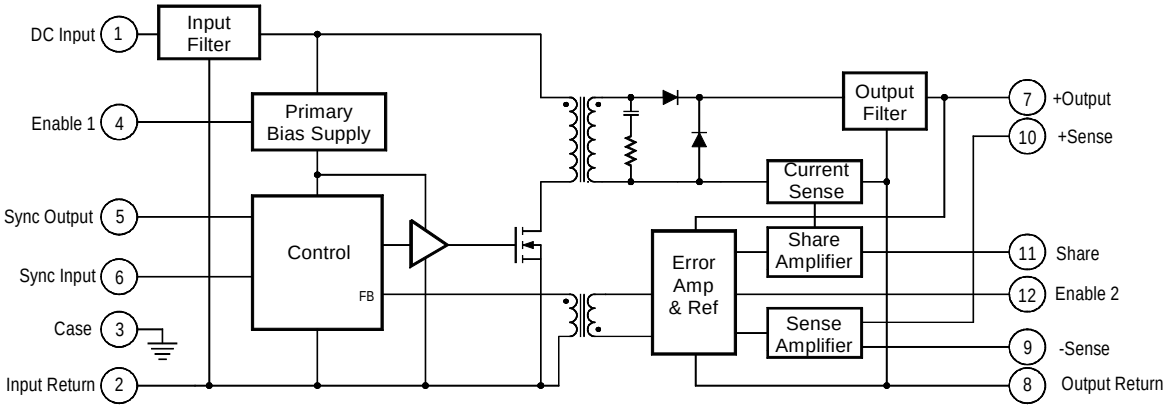
Parameter		Group A Subgroups	Test Conditions	Min	Nom	Max	Unit
LOAD TRANSIENT RESPONSE			Note 2, 8				
AFL5005S	Amplitude	4, 5, 6	Load Step 50% ⇔ 100%	-450		450	mV
	Recovery	4, 5, 6				200	µSec
	Amplitude	4, 5, 6	Load Step 10% ⇔ 50%	-450		450	mV
	Recovery	4, 5, 6				300	µSec
AFL5008S	Amplitude	4, 5, 6	Load Step 50% ⇔ 100%	-500		500	mV
	Recovery	4, 5, 6				200	µSec
	Amplitude	4, 5, 6	Load Step 10% ⇔ 50%	-500		500	mV
	Recovery	4, 5, 6				300	µSec
AFL5009S	Amplitude	4, 5, 6	Load Step 50% ⇔ 100%	-600		600	mV
	Recovery	4, 5, 6				200	µSec
	Amplitude	4, 5, 6	Load Step 10% ⇔ 50%	-600		600	mV
	Recovery	4, 5, 6				300	µSec
AFL5012S	Amplitude	4, 5, 6	Load Step 50% ⇔ 100%	-750		750	mV
	Recovery	4, 5, 6				200	µSec
	Amplitude	4, 5, 6	Load Step 10% ⇔ 50%	-750		750	mV
	Recovery	4, 5, 6				300	µSec
AFL5015S	Amplitude	4, 5, 6	Load Step 50% ⇔ 100%	-750		750	mV
	Recovery	4, 5, 6				200	µSec
	Amplitude	4, 5, 6	Load Step 10% ⇔ 50%	-750		750	mV
	Recovery	4, 5, 6				300	µSec
AFL5028S	Amplitude	4, 5, 6	Load Step 50% ⇔ 100%	-1200		1200	mV
	Recovery	4, 5, 6				200	µSec
	Amplitude	4, 5, 6	Load Step 10% ⇔ 50%	-1200		1200	mV
	Recovery	4, 5, 6				300	µSec
LINE TRANSIENT RESPONSE			Note 1, 2, 3				
	Amplitude		V_{IN} Step = 30 ⇔ 80 Volts	-500		500	mV
	Recovery					500	µSec
TURN-ON CHARACTERISTICS			V_{IN} = 30, 50, 80 Volts. Note 4				
	Overshoot	4, 5, 6	Enable 1, 2 on. (Pins 4, 12 high or			250	mV
	Delay	4, 5, 6	open)	50	75	120	mSec
LOAD FAULT RECOVERY			Same as Turn On Characteristics.				
LINE REJECTION			MIL-STD-461D, CS101, 30Hz to 50KHz Note 1	40	50		dB

Notes to Specifications:

1. Parameters not 100% tested but are guaranteed to the limits specified in the table.
2. Recovery time is measured from the initiation of the transient to where V_{OUT} has returned to within ±1% of V_{OUT} at 50% load.
3. Line transient transition time ≥ 100 µSec.
4. Turn-on delay is measured with an input voltage rise time of between 100 and 500 volts per millisecond.
5. Current limit point is that condition of excess load causing output voltage to drop to 90% of nominal.
6. Parameter verified as part of another test.
7. All electrical tests are performed with the remote sense leads connected to the output leads at the load.
8. Load transient transition time ≥ 10 µSec.
9. Enable inputs internally pulled high. Nominal open circuit voltage ≈ 4.0VDC.

AFL50XXS Circuit Description

Figure I. AFL Single Output Block Diagram



Circuit Operation and Application Information

The AFL series of converters employ a forward switched mode converter topology. (refer to Figure I.) Operation of the device is initiated when a DC voltage whose magnitude is within the specified input limits is applied between pins 1 and 2. If pin 4 is enabled (at a logical 1 or open) the primary bias supply will begin generating a regulated housekeeping voltage bringing the circuitry on the primary side of the converter to life. A power MOSFET is used to chop the DC input voltage into a high frequency square wave, applying this chopped voltage to the power transformer at the nominal converter switching frequency. Maintaining a DC voltage within the specified operating range at the input assures continuous generation of the primary bias voltage.

The switched voltage impressed on the secondary output transformer winding is rectified and filtered to generate the converter DC output voltage. An error amplifier on the secondary side compares the output voltage to a precision reference and generates an error signal proportional to the difference. This error signal is magnetically coupled through the feedback transformer into the controller section of the converter varying the pulse width of the square wave signal driving the MOSFET, narrowing the width if the output voltage is too high and widening it if it is too low, thereby regulating the output voltage.

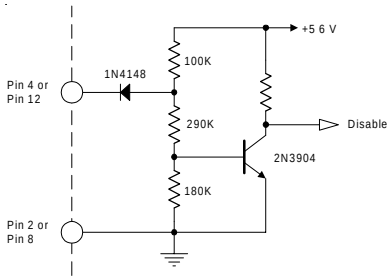
Remote Sensing

Connection of the **+** and **-** sense leads at a remotely located load permits compensation for excessive resistance between the converter output and the load when their physical separation could cause undesirable voltage drop. This connection allows regulation to the placard voltage at the point of application. When the remote sensing feature is not used, the sense lead should be connected to their respective output terminals at the converter. Figure III. illustrates a typical remotely sensed application.

Inhibiting Converter Output

As an alternative to application and removal of the DC voltage to the input, the user can control the converter output by providing TTL compatible, positive logic signals to either of two enable pins (pin 4 or 12). The distinction between these two signal ports is that enable 1 (pin 4) is referenced to the input return (pin 2) while enable 2 (pin 12) is referenced to the output return (pin 8). Thus, the user has access to an inhibit function on either side of the isolation barrier. Each port is internally pulled “high” so that when not used, an open connection on both enable pins permits normal converter operation. When their use is desired, a logical “low” on either port will shut the converter down.

Figure II. Enable Input Equivalent Circuit



Internally, these ports differ slightly in their function. In use, a low on Enable 1 completely shuts down all circuits in the converter, while a low on Enable 2 shuts down the secondary side while altering the controller duty cycle to near zero. Externally, the use of either port is transparent to the user save for minor differences in idle current. (See specification table).

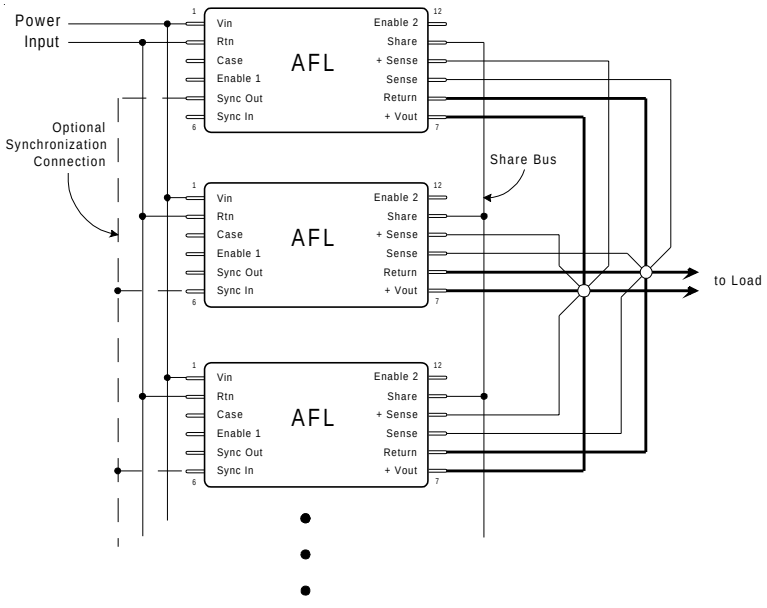
Synchronization of Multiple Converters

When operating multiple converters, system requirements often dictate operation of the converters at a common frequency. To accommodate this requirement, the AFL series converters provide both a synchronization input and output.

The sync input port permits synchronization of an AFL converter to any compatible external frequency source operating between 500 and 700 KHz. This input signal should be referenced to the input return and have a 10% to 90% duty cycle. Compatibility requires transition times less th an 100 ns, maximum low level of +0.8 volts and a minimum high level of +2.0 volts. The sync output of another converter which has been designated as the master oscillator provides a convenient frequency source for this mode of operation. When external synchronization is not required, the sync in pin should be left open (unconnected)thereby permitting the converter to operate at its' own internally set frequency.

The sync output signal is a continuous pulse train set at 550 ±50 KHz, with a duty cycle of 15 ±5%. This signal is referenced to the input return and has been tailored to be compatible with the AFL sync input port. Transition times are less than 100 ns and the low level output impedance is less than 50 ohms. This signal is active when the DC input voltage is within the specified operating range and the converter is not inhibited. This output has adequate drive reserve to synchronize at least five additional converters. A typical connection is illustrated in Figure III.

Figure III. Preferred Connection for Parallel Operation



Parallel Operation-Current and Stress Sharing

Figure III. illustrates the preferred connection scheme for operation of a set of AFL converters with outputs operating in parallel. Use of this connection permits equal sharing among the members of a set whose load current exceeds the capacity of an individual AFL. An important feaure of the AFL series operating in the parallel mode is that in addition to sharing the current, the stress induced by temperature will also be shared. Thus if one member of a paralleled set is operating at a higher case temperature, the current it provides to the load will be reduced as compensation for the temperature induced stress on that device.

When operating in the shared mode, it is important that symmetry of connection be maintained as an assurance of optimum load sharing performance. Thus, converter outputs should be connected to the load with equal lengths of wire of the same gauge and sense leads from each converter should be connected to a common physical point, preferably at the load along with the converter output and return leads. All converters in a paralleled set must have their share pins connected together. This arrangement is diagrammatically illustrated in Figure III. showing the outputs and return pins connected at a star point which is located close as possible to the load.

As a consequence of the topology utilized in the current sharing circuit, the share pin may be used for other functions. In applications requiring only a single converter, the voltage appearing on the share pin may be used as a "current monitor". The share pin open circuit voltage is nominally +1.00v at no load and increases linearly with increasing output current to +2.20v at full load.

Thermal Considerations

Because of the incorporation of many innovative technological concepts, the AFL series of converters is capable of providing very high output power from a package of very small volume. These magnitudes of power density can only be obtained by combining high circuit efficiency with effective methods of heat removal from the die junctions. This requirement has been effectively addressed inside the device; but when operating at maximum loads, a significant amount of heat will be generated and this heat must be conducted away from the case. To maintain the case temperature at or below the specified maximum of 125°C, this heat must be transferred by conduction to an appropriate heat dissipater held in intimate contact with the converter base-plate.

Since the effectiveness of this heat transfer is dependent on the intimacy of the baseplate/heatsink interface, it is strongly recommended that a high thermal conductivity heat transferring medium is inserted between the baseplate and heatsink. The material most frequently utilized at the factory during all testing and burn-in processes is sold under the trade name of Sil-Pad® 400[1] . This particular product is an insulator but electrically conductive versions are also available. Use of these materials assures maximum surface contact with the heat dissipater thereby compensating for any minor surface variations. While other available types of heat conductive materials and thermal compounds provide similar effectiveness, these alternatives are often less convenient and can be somewhat messy to use.

[1]Sil-Pad is a registered Trade Mark of Bergquist, Minneapolis, MN

A conservative aid to estimating the total heat sink surface area ($A_{HEAT\ SINK}$) required to set the maximum case temperature rise (ΔT) above ambient temperature is given by the following expression:

$$A_{HEAT\ SINK} \approx \left\{\frac{\Delta T}{80P^{0.85}}\right\}^{-1.43} - 3.0$$

where

ΔT = Case temperature rise above ambient

P = Device dissipation in Watts = $P_{OUT}\left\{\frac{1}{Eff} - 1\right\}$

As an example, it is desired to maintain the case temperature of an AFL5015S at ≤ +85°C while operating in an open area whose ambient temperature is held at a constant +25°C; then

$$\Delta T = 85 - 25 = 60°C$$

If the worst case full load efficiency for this device is 83%; then the power dissipation at full load is given by

$$P = 120 \bullet \left\{\frac{1}{.83} - 1\right\} = 120 \bullet (0.205) = 24.6W$$

and the required heat sink area is

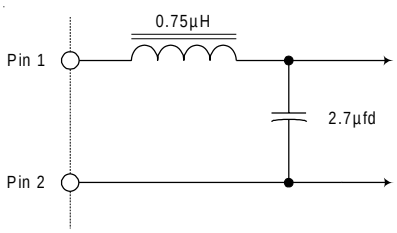
$$A_{HEAT\ SINK} = \left\{\frac{60}{80 \bullet 24.6^{0.85}}\right\}^{-1.43} - 3.0 = 71\ in^2$$

Thus, a total heat sink surface area (including fins, if any) of 71 in^2 in this example, would limit case rise to 60°C above ambient. A flat aluminum plate, 0.25" thick and of approximate dimension 4" by 9" (36 in^2 per side) would suffice for this application in a still air environment. Note that to meet the criteria in this example, both sides of the plate require unrestricted exposure to the ambient air.

Input Filter

The AFL50XXS series converters incorporate a LC input filter whose elements dominate the input load impedance characteristic at turn-on. The input circuit is as shown in Figure IV.

Figure IV. Input Filter Circuit



Undervoltage Lockout

A minimum voltage is required at the input of the converter to initiate operation. This voltage is set to 26.5 ± 1.5 volts. To preclude the possibility of noise or other variations at the input falsely initiating and halting converter operation, a hysteresis of approximately 2 volts is incorporated in this circuit. Thus if the input voltage droops to 24.5 ± 1.5 volts, the converter will shut down and remain inoperative until the input voltage returns to ≈ 25 volts.

Output Voltage Adjust

In addition to permitting close voltage regulation of remotely located loads, it is possible to utilize the converter sense pins to incrementally increase the output voltage over a limited range. The adjustments made possible by this method are intended as a means to "trim" the output to a voltage setting for some particular application, but are not intended to create an adjustable output converter. These output voltage setting variations are obtained by connecting an appropriate resistor value between the +sense and -sense pins while connecting the -sense pin to the output return pin as shown in Figure V. below. The range of adjustment and corresponding range of resistance values can be determined by use of the following equation.

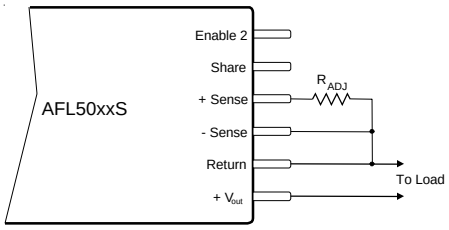
$$R_{adj} = 100 \bullet \left\{ \frac{V_{NOM}}{V_{OUT} - V_{NOM} - .025} \right\}$$

Where V_{NOM} = device nominal output voltage, and

V_{OUT} = desired output voltage

Finding a resistor value for a particular output voltage, is simply a matter of substituting the desired output voltage and the nominal device voltage into the equation and solving for the corresponding resistor value.

Figure V. Connection for V_{OUT} Adjustment



Note: R_{adj} must be set $\geq 500\Omega$

Attempts to adjust the output voltage to a value greater than 120% of nominal should be avoided because of the potential of exceeding internal component stress ratings and subsequent operation to failure. Under no circumstance should the external setting resistor be made less than 500W. By remaining within this specified range of values, completely safe operation fully within normal component derating limits is assured.

Examination of the equation relating output voltage and resistor value reveals a special benefit of the circuit topology utilized for remote sensing of output voltage in the AFL50XXS series of converters. It is apparent that as the resistance increases, the output voltage approaches the nominal set value of the device. In fact the calculated limiting value of output voltage as the adjusting resistor becomes very large is ≈ 25mV above nominal device voltage.

The consequence is that if the +sense connection is unintentionally broken, an AFL50XXS has a fail-safe output voltage of Vout + 25mV, where the 25mV is independent of the nominal output voltage. It can be further demonstrated that in the event of both the + and - sense connections being broken, the output will be limited to Vout + 440mV. This 440 mV is also essentially constant independent of the nominal output voltage.

General Application Information

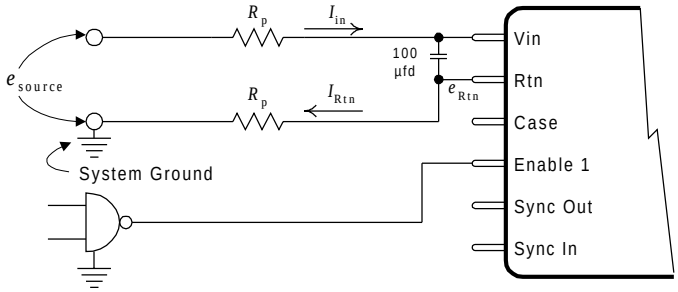
The AFL50XXS series of converters are capable of providing large transient currents to user loads on demand. Because the nominal input voltage range in this series is relatively low, the resulting input current demands will be correspondingly large. It is important therefore, that the line impedance be kept very low to prevent steady state and transient input currents from degrading the supply voltage between the voltage source and the converter input. In applications requiring high static currents and large transients, it is recommended that the input leads be made of adequate size to minimize resistive losses, and that a good quality capacitor of approximately100µfd be connected directly across the input terminals to assure an adequately low impedance at the input terminals. Table I relates nominal resistance values and selected wire sizes.

Table 1. Nominal Resistance of Cu Wire

Wire Size, AWG	Resistance per ft
24 Ga	25.7 mΩ
22 Ga	16.2 mΩ
20 Ga	10.1 mΩ
18 Ga	6.4 mΩ
16 Ga	4.0 mΩ
14 Ga	2.5 mΩ
12 Ga	1.6 mΩ

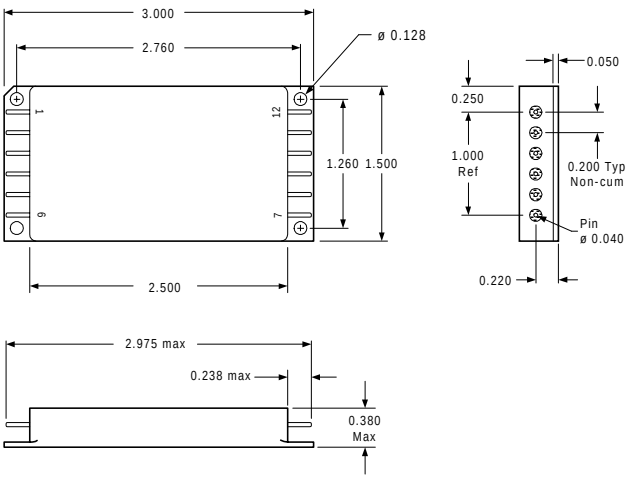
Incorporation of a 100 µfd capacitor at the input terminals is recommended as compensation for the dynamic effects of the parasitic resistance of the input cable reacting with the complex impedance of the converter input, and to provide an energy reservoir for transient input current requirements.

Figure VI. Problems of Parasitic Resistance in input Leads
(See text)



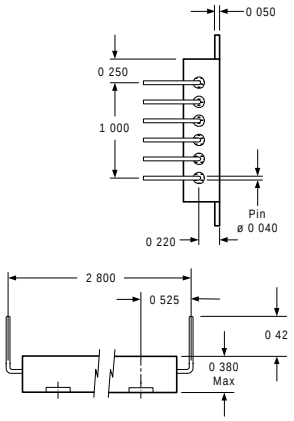
AFL50XXS Case Outlines

Case X

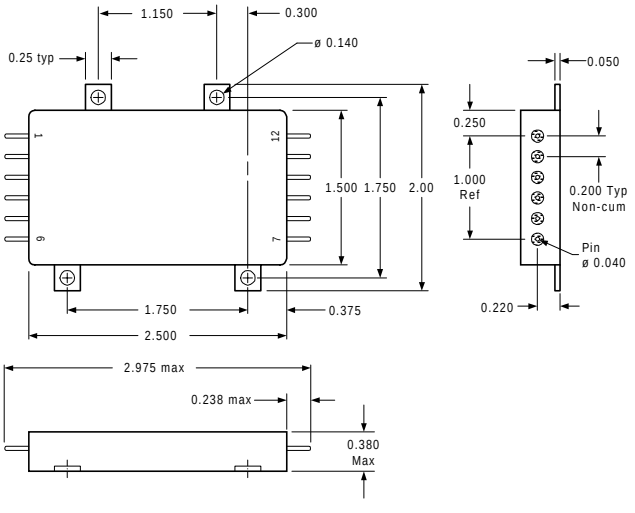


Case W

Pin Variation of Case Y

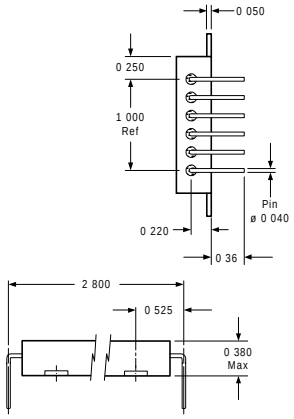


Case Y



Case Z

Pin Variation of Case Y



Tolerances, unless otherwise specified: .XX = ±0.010
.XXX = ±0.005

BERYLLIA WARNING: These converters are hermetically sealed; however they contain BeO substrates and should not be ground or subjected to any other operations including exposure to acids, which may produce Beryllium dust or fumes containing Beryllium

Available Screening Levels and Process Variations for AFL50XXS Series.

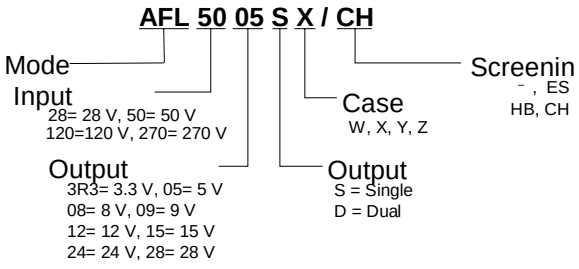
Requirement	MIL-STD-883 Method	No Suffix	ES Suffix	HB Suffix	CH Suffix
Temperature Range		-20°C to +85°C	-55°C to +125°C	-55°C to +125°C	-55°C to +125°C
Element Evaluation					MIL-PRF-38534
Internal Visual	2017	*	Yes	Yes	Yes
Temperature Cycle	1010		Cond B	Cond C	Cond C
Constant Acceleration	2001,		500g	Cond A	Cond A
Burn-in	1015	48hrs @ 85°C	48hrs @ 125°C	160hrs @ 125°C	160hrs @ 125°C
Final Electrical (Group A)	MIL-PRF-38534	25°C	25°C	-55, +25, +125°C	-55, +25, +125°C
Seal, Fine & Gross	1014	*	Cond A, C	Cond A, C	Cond A, C
External Visual	2009	*	Yes	Yes	Yes

* per Commercial Standards

AFL50XXS Pin Designation

Pin No.	Designation
1	Positive Input
2	Input Return
3	Case
4	Enable 1
5	Sync Output
6	Sync Input
7	Positive Output
8	Output Return
9	Return Sense
10	Positive Sense
11	Share
12	Enable 2

Part Numbering



International
IOR Rectifier

WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, Tel: (310) 322 3331
ADVANCED ANALOG: 2270 Martin Av., Santa Clara, California 95050, Tel: (408) 727-0500
Visit us at www.irf.com for sales contact information.
Data and specifications subject to change without notice. 07/02