

10/100 Base-T Half Port Magnetic Module

Design For Power over Ethernet (PoE) Applications



Compliant with IEEE802.3af standard including 350uH OCL with 8mA DC bias



350mA balanced DC line current capability



Design to withstand IR-reflow with peak temperature per IPC/JEDEC J-STD-020C



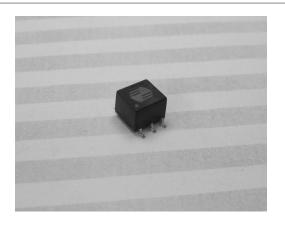
Half port design which is suitable for Tx or Rx application



Operating temperature 0°C to +70°C.



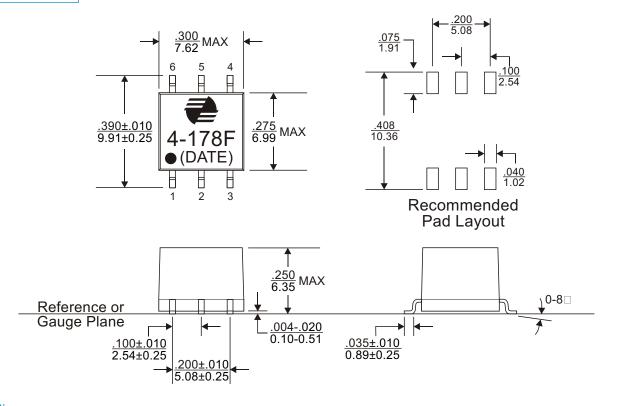
RoHS compliant



ELECTRICAL SPECIFICATION @ 25°C							
Part Number	Turns Ratio	Insertion Loss (dB Typ)	Return Loss (dB Min)			Hipot (Vrms)	
		.1-100MHz	1-30MHz	40MHz	50MHz	60-80MHz	
824-00178F	1CT:1CT	0.5	16.0	14.0	13.0	12.0	1500

MECHANICAL DIMENSIONS

Package: SMD06



Notes:

- 1. All dimensions are specified in $\frac{\text{inches}}{\text{mm}}$ with higher precedence in inches.
- 2. Unless otherwise specified, all tolerances are $\pm \frac{.010}{0.25}$.

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SCHEMATICS

824-00178F

CHIP SIDE (Primary) (Secondary)

FOR MORE INFORMATION, PLEASE CONTACT

HEADQUARTER

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