



Features

- ✧ For IF SAW filter
- ✧ High attenuation
- ✧ Single-ended operation
- ✧ Dual In-line Package
- ✧ RoHS compliant (2002/95/EC), Pb-free

Specifications

Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	64.85	65	65.15
Insertion Loss	dB	-	25	28
1.5 dB Bandwidth	MHz	24	24.09	-
3 dB Bandwidth	MHz	-	24.26	-
35 dB Bandwidth	MHz	-	25.12	25.2
Passband Variation	dB	-	0.9	2
Absolute Delay	usec	-	2.96	-
Ultimate Rejection(f0±13MHz)	dB	40	52	-
Material Temperature coefficient	KHz/°C	-5.3		
Substrate Material	-	128LN		
Ambient Temperature	°C	25		
DC Voltage	V	0		
Input Power	dBm	-	-	10
ESD Class	-	1A		
Package Size	DIP3512 (35.0x12.8x4.7mm3)			

Notes:

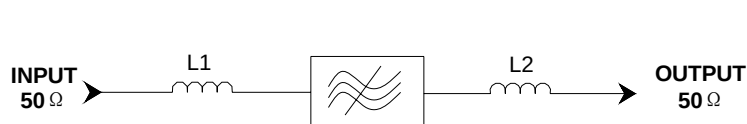
1. All specifications are based on the test circuit shown;
2. In production, all specifications are measured by Agilent Network analyzer and full 2 port calibration at room temperature;
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances;
4. This is the optimum impedance in order to achieve the performance show.



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Part Number	LBN06505		
Rev. Date	2011-01-30		
Ver.	1.0	Page	1/3

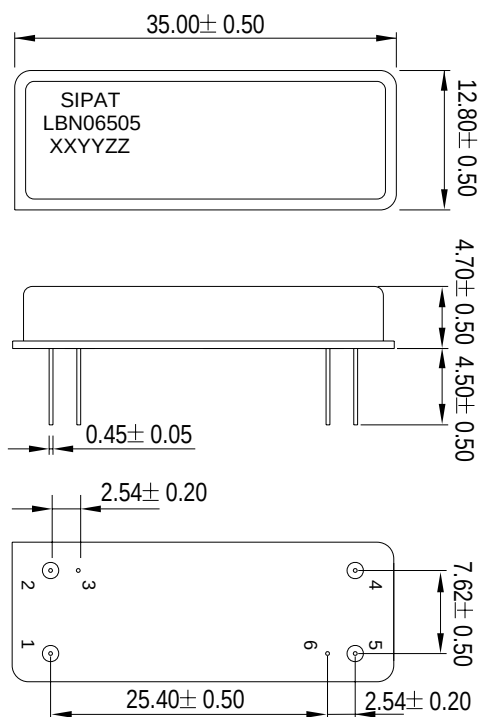
Matching Configuration



L1=180nH L2=150nH
Source/Load Impedance=50 ohm

Notes - Component values may change depending
on board layout.

Package Dimension



Pad Configuration:

Input 1
Output 5
Ground All Others

Marking Configuration:

- 1) SIPAT: Manufacturer Name
- 2) LBN06505: Part Number
- 3) XXYY: Date(Year/month)
- 4) ZZ: Identified Code

Package: DIP3512

Unit: mm



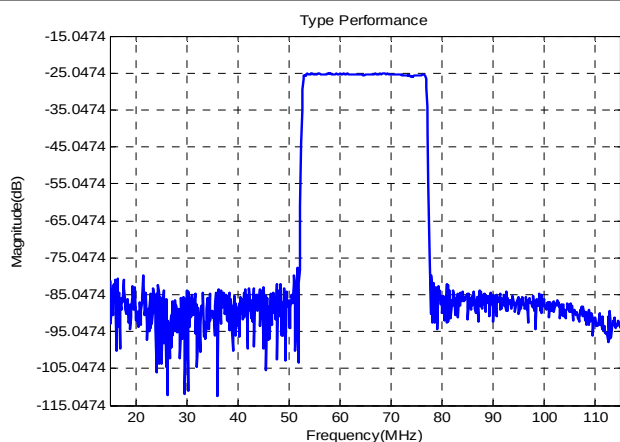
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Typical Performance

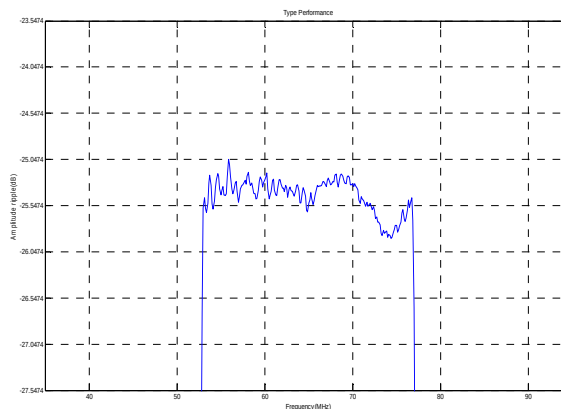
Frequency Response



Horizontal: 10MHz/Div

Vertical: 10dB/Div

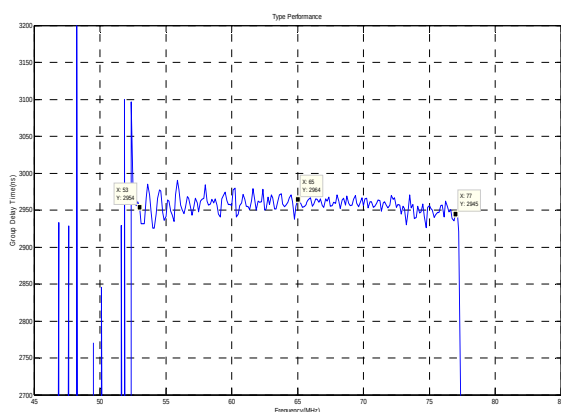
Passband Response



Horizontal: 10MHz/Div

Vertical: 0.5dB/Div

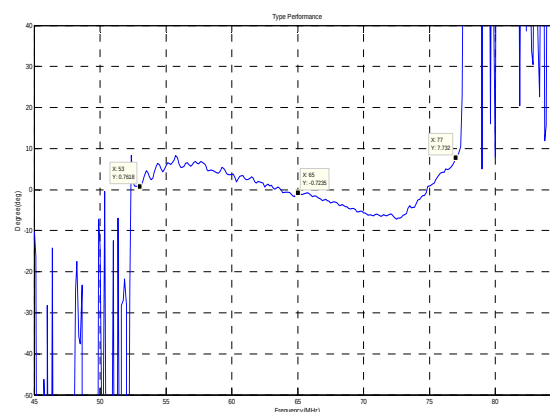
Group Delay Variation($f_0 \pm 12$ MHz)



Horizontal: 5MHz/Div

Vertical: 50ns/Div

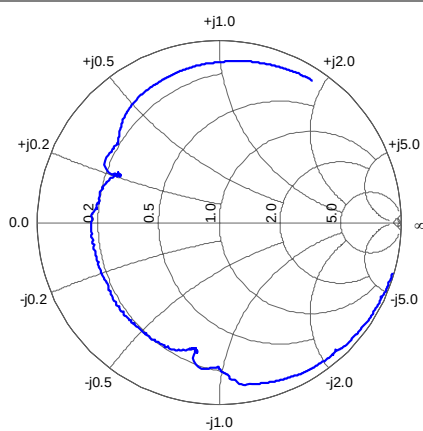
Phase Linearity($f_0 \pm 12$ MHz)



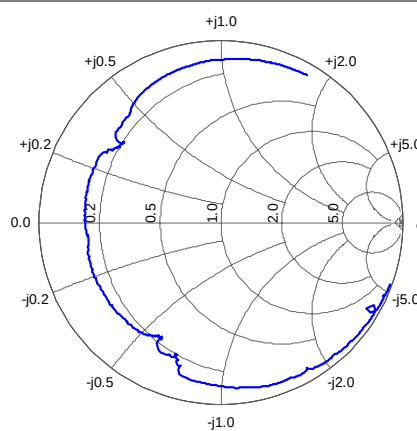
Horizontal: 5MHz/Div

Vertical: 10deg/Div

Smith Chart S11



Smith Chart S22



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