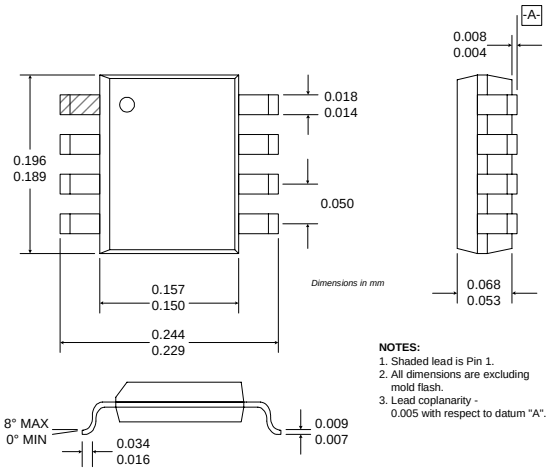
VCO/HIGH-ISOLATION BUFFER AMPLIFIER

Typical Applications

- 2-Way Paging
- ISM Band Systems
- Wireless Local Loop Systems
- GPS Receivers
- Cellular Systems
- Low Voltage Applications

Product Description

The RF2504 is an integrated oscillator and buffer amplifier chain designed to achieve extremely low sensitivity to fluctuations in load impedance and power supply noise, thereby greatly reducing load pulling and pushing. The IC offers great flexibility, yet is easy to use. This product was designed for use in applications with low supply voltages. It has a power-down feature and is designed to operate from 700MHz to 1500MHz with the help of an external resonator. Frequency control is achieved with an external varactor diode. The IC's ease of use, reduced load pulling, small size, and low cost make it an ideal LO (Local Oscillator) for almost any wireless application.



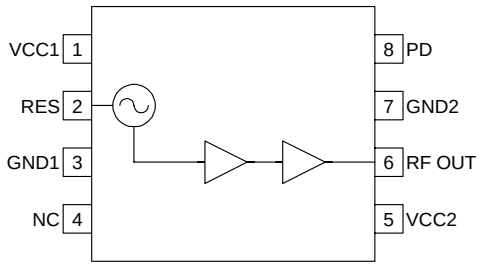
Package Style: SOIC-8

Optimum Technology Matching® Applied

- ☑ Si BJT
- ☐ Si Bi-CMOS
- ☐ GaAs HBT
- ☐ SiGe HBT
- ☐ GaAs MESFET
- ☐ Si CMOS

Features

- High-Isolation / Reduced Load Pulling
- Low Current Consumption
- -6dBm Output Power
- Digitally Controlled Power Down Mode
- 700MHz to 1500MHz Operating Range
- Single 2.2V to 5V Supply



Functional Block Diagram

Ordering Information

RF2504	VCO/High-Isolation Buffer Amplifier
RF2504 PCBA	Fully Assembled Evaluation Board

RF Micro Devices, Inc.
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Greensboro, NC 27409, USA

Tel (336) 664 1233
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Absolute Maximum Ratings

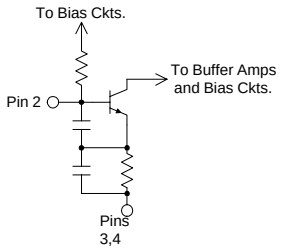
Parameter	Rating	Unit
Supply Voltage	-0.5 to +5.8	V_{DC}
Power Down Voltage (V_{PD})	-0.5 to +5.8	V_{DC}
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-55 to +150	°C



Caution! ESD sensitive device.

RF Micro Devices believes the furnished information is correct and accurate at the time of this printing. However, RF Micro Devices reserves the right to make changes to its products without notice. RF Micro Devices does not assume responsibility for the use of the described product(s).

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall					
Operating Frequency		700 to 1500		MHz	
915MHz Operation					T=25°C, V_{CC}=2.7V, Z_{LOAD}=50Ω, V_{PD}=2.7V
Output Power		-6		dBm	
2nd Harmonic		-8		dBc	
3rd Harmonic		-19		dBc	
Load Pulling		200		kHz	Into 1.67VSWR Load
V_{CC} Pushing		4.7		MHz/V	
Phase Noise		-104		dBc	100kHz Offset. Better phase noise is achievable at the expense of output power.
		-83		dBc	10kHz Offset
Power Supply					
Operating Voltage		2.2 to 5.0		V_{DC}	At 2.2V and -40°C, output power will be reduced to typically -11dBm.
Supply Current		5.5		mA	

Pin	Function	Description	Interface Schematic
1	VCC1	Power supply connection for the VCO. This pin should be well bypassed close to the package with a capacitor suitable for the frequency of operation as well as a capacitor to minimize low frequency noise from the voltage supply. The ground side of the capacitors should connect immediately to ground plane.	
2	RES	Connection point for the resonator circuit. The resonator is an inductive element. Changing the effective inductance, either physically or with a varactor tuned circuit, will change the frequency of operation. Note that all parasitics on the circuit board will contribute to the effective inductance and will influence the frequency of operation. These effects become more pronounced at higher operating frequencies. This pin has DC bias present. A DC blocking capacitor, suitable for the frequency of operation, should be used if the external circuitry has DC present or presents a DC path to ground. See Application Example Schematic and Theory of Operation section of this data sheet for design details.	
3	GND1	Ground connection for the VCO. Keep traces physically short and connect immediately to ground plane for best performance. In order to minimize load pulling, it is recommended that pin 3 have a different return path to ground than pin 7 (i.e., separate vias to a common ground plane).	See Pin 2
4	NC	Not connected.	
5	VCC2	Power supply connection for the buffer amplifiers. This pin should be well bypassed close to the package with a capacitor suitable for the frequency of operation. The ground side of the capacitor should connect immediately to ground plane.	
6	RF OUT	RF output pin. This is an open-collector output and must be biased externally. A shunt bias/matching inductor to V_{CC} and a series blocking/matching capacitor are recommended. See Application Example Schematic.	
7	GND3	Ground connection for the buffer amplifiers. Keep traces physically short and connect immediately to ground plane for best performance. In order to minimize load pulling, it is recommended that pin 3 have a different return path to ground than pin 7 (i.e., separate vias to a common ground plane).	
8	PD	Power Down pin for the VCO and buffer amplifiers. A logic “low” (0.0 to 0.7V) turns the entire device off and supply current drops to less than 1μA. A logic “high” (≥3.0V) turns the device on. Note that the voltage on this pin should never exceed $5.5V_{DC}$.	

Application Notes

The RF2504 has two functional parts: an oscillator and buffer amplifier. The functional blocks have separate ground and VCC pins to increase the isolation and reduce load pulling, one of the key design objectives. An external resonator is used to add design flexibility, and the loaded Q of this resonator will affect the performance of the resulting oscillator.

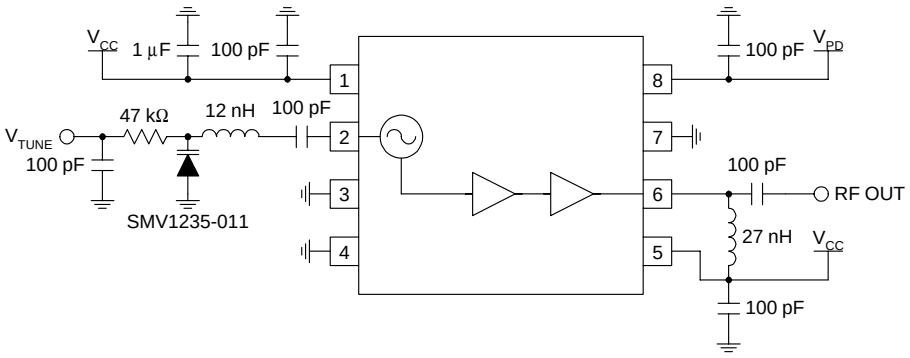
To create an oscillation, negative resistance is generated at pin 2 with a circuit similar to a Colpitts oscillator. The input impedance at pin 2, measured with a vector network analyzer, is shown here in the data sheet. In general, the impedance looks like a negative resistance in series with a capacitor. The negative resistance decays as the frequency increases. An oscillator is created when an inductive element is placed on pin 2 that is the conjugate of the capacitive reactance. A greater inductive element will create a lower frequency of oscillation.

The S11 looking into pin 2 is also shown here in the data sheet. It has return gain from 500 MHz to 2200 MHz at room temperature. The specified frequency range of 750 MHz to 1500 MHz defines the region where the output power is relatively flat. At lower and higher frequencies, the power will tend to roll off from the nominal value. The specified frequency range is conservatively set to ensure oscillation and maintain performance, but the RF2504 can be used over a broader frequency range with degraded performance.

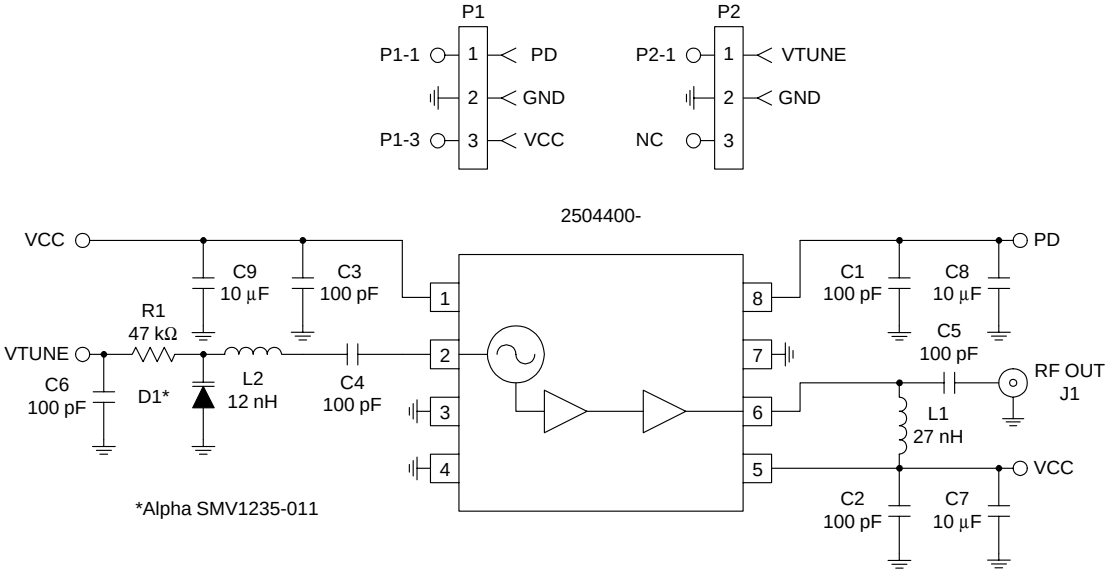
The overall Q of the external resonator will affect performance. Lower Q means lower power, higher phase noise, and more load pulling. If the Q is too low, the circuit will not oscillate. The IC is designed to oscillate into a resonator with Q>10. The performance is measured with a microstrip resonator or high quality inductor, which usually has a Q>50. These measurements define the best performance that can be expected from the ICs. Lower Q resonators, particularly those including a lossy varactor, might have degraded performance.

The specified output power is measured into a 50Ω load. The IC has a high output impedance, and if desired, output matching can be used to obtain more power by transforming 50Ω into a higher impedance. On the RF2504, this could be accomplished by simply changing the values of the external output inductor and capacitor.

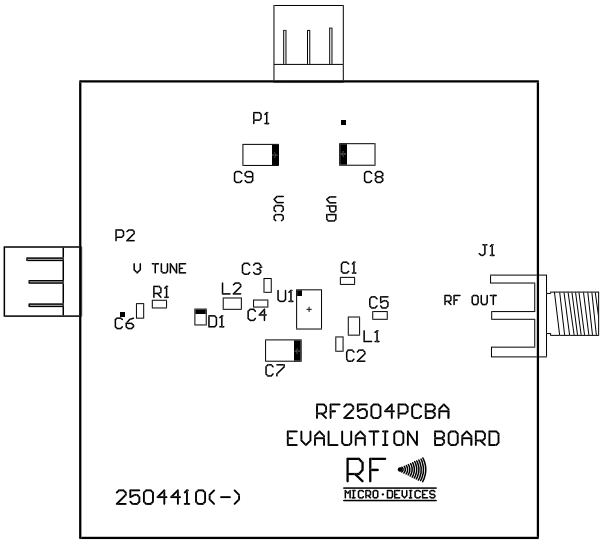
Application Schematic
915 MHz Operation

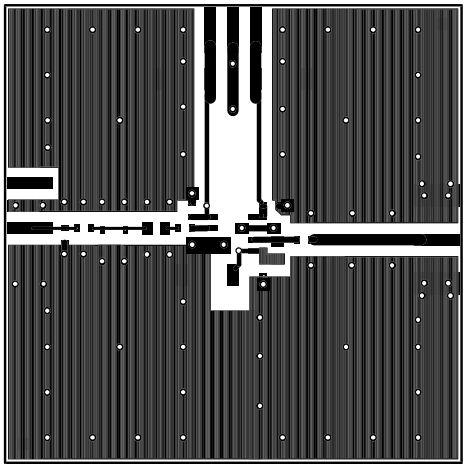


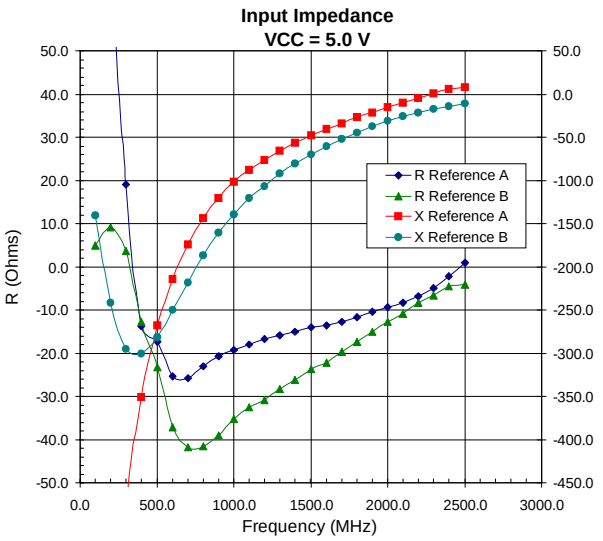
Evaluation Board Schematic
915MHz Operation
(Download Bill of Materials from www.rfmd.com.)

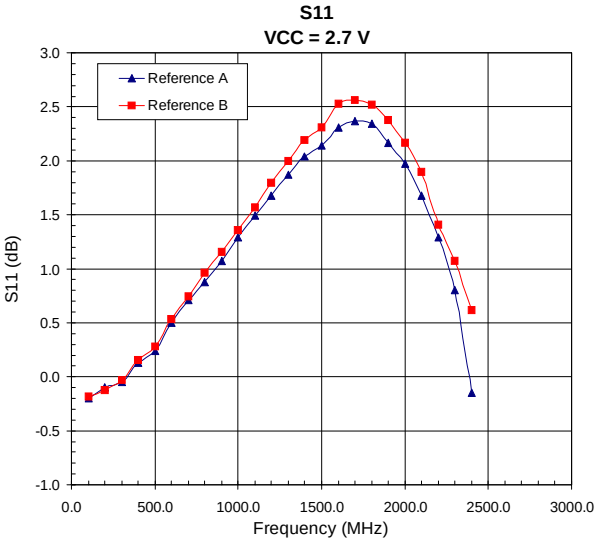


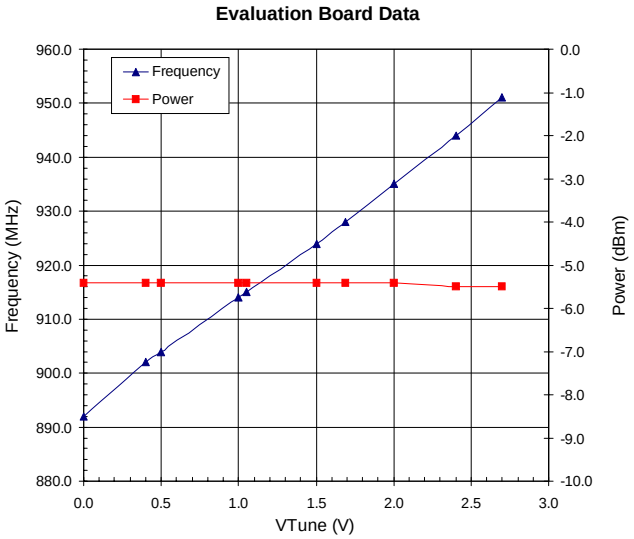
Evaluation Board Layout
915MHz Operation
Board Size 2.0” x 2.0”

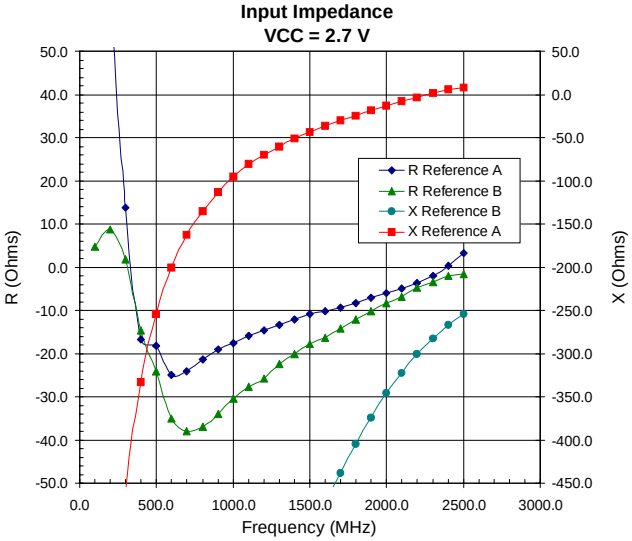


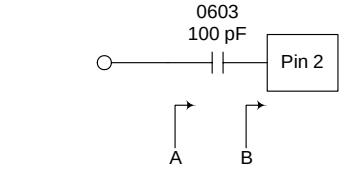












Reference positions for input impedance measurements of RF2504

12 PLLs and VCOs