

CMOS TEMPERATURE SENSOR IC

S-58LM20A Series

The S-58LM20A Series is a high-accuracy temperature sensor IC on a single chip, provides output voltage which is linear against the temperature change.

Each chip consists of a temperature sensor, a constant current circuit, and an operational amplifier.

This IC is able to be used under the wide temperature range of -55°C to 130°C . This IC has much better linearity than other conventional temperature sensors such as thermistor, it is possible to achieve the extensive application for temperature control.

■ Features

- Accuracy against temperature $\pm 2.5^{\circ}\text{C}$ (-55 to $+130^{\circ}\text{C}$)
- Linear output voltage $-11.77\text{ mV}/^{\circ}\text{C}$ Typ.
 $T_a = -30^{\circ}\text{C}$: 2.205 V Typ.
 $T_a = +30^{\circ}\text{C}$: 1.515 V Typ.
 $T_a = +130^{\circ}\text{C}$: 0.303 V Typ.
- Nonlinearity $\pm 0.4\%$ Typ. (-20 to $+80^{\circ}\text{C}$)
- Operation in wide range of power supply voltage $V_{DD} = 2.4$ to 5.5 V (-30 to $+130^{\circ}\text{C}$)
 $V_{DD} = 2.7$ to 5.5 V (-55 to $+130^{\circ}\text{C}$)
 $4.5\text{ }\mu\text{A}$ Typ. ($+25^{\circ}\text{C}$) $6.0\text{ }\mu\text{A}$ Max. (-55 to $+130^{\circ}\text{C}$)
- Low current consumption
- Built-in operational amplifier
- Output voltage referred to V_{SS}
- Small package SNT-4A, SC-82AB, WLP-4B
- Lead-free product

■ Applications

- Compensation of high-frequency circuits such as cellular phones and radio equipment
- Compensation of oscillation frequency in crystal oscillator
- LCD contrast compensation
- Compensation of amplifier gain
- Compensation of auto focus circuits
- Temperature detection in battery management
- Overheating prevention for charged batteries or halogen lights

■ Package

Package Name	Drawing Code			
	Package	Tape	Reel	Land
SC-82AB	NP004-A	NP004-A	NP004-A	—
SNT-4A	PF004-A	PF004-A	PF004-A	PF004-A
WLP-4B	HB004-B	HB004-B	HB004-B	—

■ Block Diagram

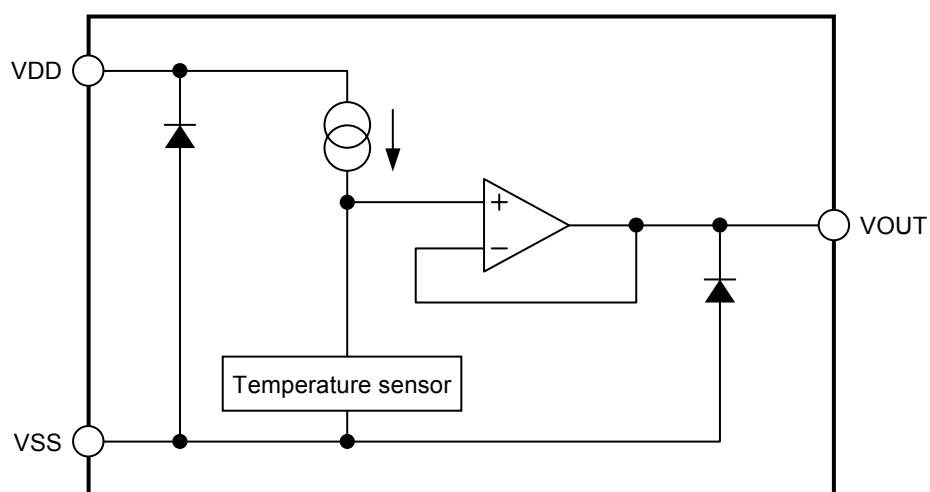


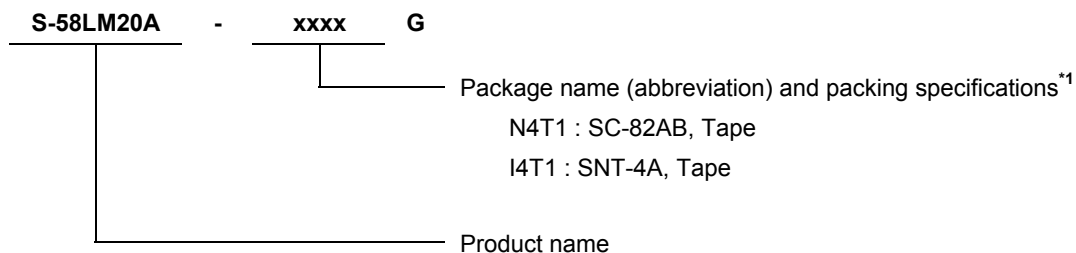
Figure 1

■ Product Name Structure

- Users can select the product type in the S-58LM20A Series. Regarding the contents of the product name, refer to “1. Product name” and regarding details, refer to “2. Product name list”.

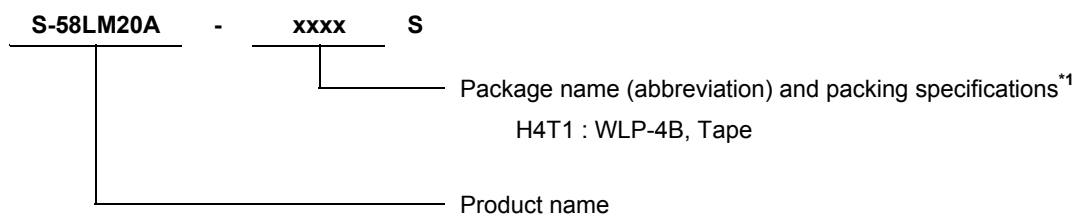
1. Product name

(1) SC-82AB, SNT-4A package



*1. Refer to the taping specifications.

(2) WLP-4B package



*1. Refer to the taping specifications.

2. Product name list

Table 1

Product Name	Temperature Accuracy	Package
S-58LM20A-N4T1G	±2.5°C	SC-82AB
S-58LM20A-I4T1G	±2.5°C	SNT-4A
S-58LM20A-H4T1S	±2.5°C	WLP-4B

■ Pin Configuration

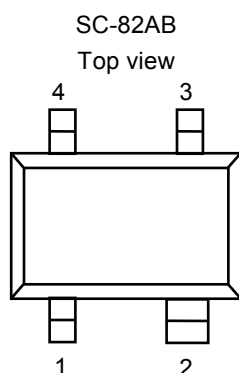


Figure 2

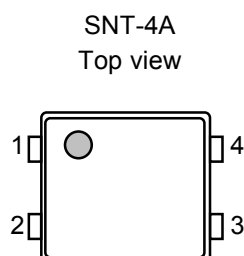


Figure 3

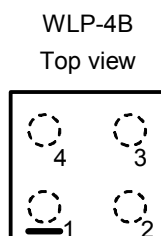


Figure 4

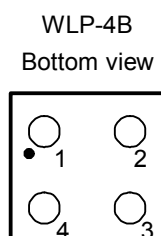


Figure 5

Table 2

Pin No.	Pin Name	Description
1	VDD	Power supply pin
2	VSS	GND pin
3	NC ^{*1}	No connection
4	VOUT	Output voltage pin

*1. The NC pin is electrically open.

The NC pin can be connected to VDD and VSS.

Table 3

Pin No.	Pin Name	Description
1	VSS	GND pin
2	VDD	Power supply pin
3	VOUT	Output voltage pin
4	NC ^{*1}	No connection

*1. The NC pin is electrically open.

The NC pin can be connected to VDD and VSS.

Table 4

Pin No.	Pin Name	Description
1	VDD	Power supply pin
2	VSS ^{*1}	GND pin
3	VSS ^{*1}	GND pin
4	VOUT	Output voltage pin

*1. Connect both VSS pins to GND.

■ Absolute Maximum Ratings

Table 5

(Ta = 25°C unless otherwise specified)

Item		Symbol	Absolute Maximum Rating	Unit
Power supply pin voltage		V_{DD}	$V_{SS} - 0.3$ to $V_{SS} + 6.5$	V
Output voltage		V_{OUT}	$V_{SS} - 0.3$ to $V_{DD} + 0.3$	V
Power dissipation	SC-82AB	P_D	350 ^{*1}	mW
	SNT-4A		300 ^{*1}	mW
	WLP-4B		290 ^{*1}	mW
Operating ambient temperature		T_{opr}	-55 to +130	°C
Storage temperature		T_{stg}	-65 to +150	°C

*1. When mounted on board

[Mounted board]

(1) Board size: 114.3 mm × 76.2 mm × 1.6 mm

(2) Board name: JEDEC STANDARD51-7

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

■ Electrical Characteristics

Table 6

(Ta = 25°C, V_{DD} = 2.7 V, I_{OUT} = 0 A unless otherwise specified)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	Test Circuit
Range of power supply voltage	V _{DD}	-30°C ≤ Ta ≤ +130°C	2.4	—	5.5	V	1
		-55°C ≤ Ta ≤ +130°C	2.7	—	5.5	V	1
Output voltage $V_{OUT} = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) + 1.8639 \text{ V}$	V _{OUT}	Ta = -30°C	2.177	2.205	2.234	V	1
		Ta = +30°C	1.486	1.515	1.545	V	1
		Ta = +130°C	0.272	0.303	0.335	V	1
Temperature sensitivity	V _{SE}	-30°C ≤ Ta ≤ +130°C	-12.20	-11.77	-11.40	mV/°C	—
Nonlinearity	ΔN _L	-20°C ≤ Ta ≤ +80°C	—	±0.4	—	%	—
Operating temperature range	Topr	—	-55	—	130	°C	—
Current consumption	I _{DD}	-55°C ≤ Ta ≤ +130°C	—	4.5	6.0	μA	1
Current consumption – Power supply voltage	ΔI _{DD1}	V _{DD} = 2.4 V to 5.5 V	—	0.1	—	μA	1
Current consumption – Temperature	ΔI _{DD2}	—	—	-11	—	nA/°C	1
Line regulation	ΔV _{OUT1}	V _{DD} = 2.4 V to 5.5 V	—	—	3.3	mV/V	2
Load regulation ^{*1}	ΔV _{OUT2}	I _{OUT} = 0 to 16 μA	—	—	0.156	mV/μA	2

*1. Do not flow current into the output voltage pin.

■ Test Circuit

1.

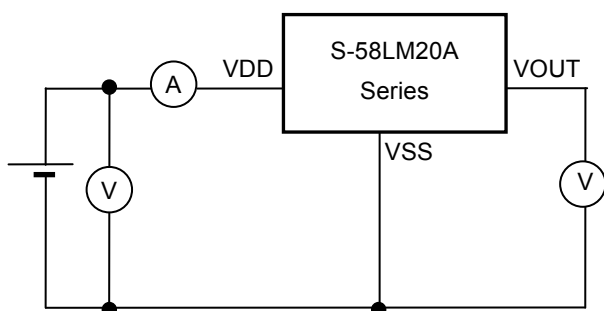


Figure 6

2.

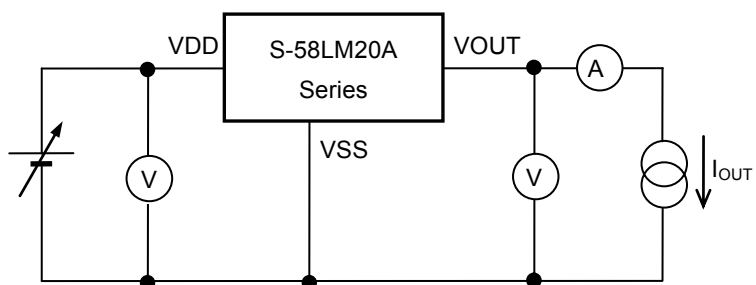


Figure 7

■ Explanation of Terms

1. Output voltage (V_{OUT})

V_{OUT} indicates the output voltage at $T_a = -30^\circ\text{C}$, $T_a = +30^\circ\text{C}$, and $T_a = +130^\circ\text{C}$.

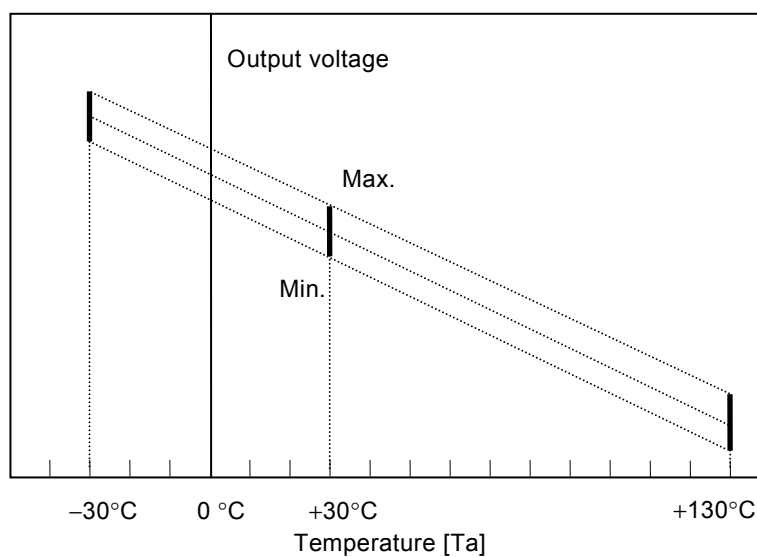


Figure 8

2. Temperature Sensitivity (V_{SE})

V_{SE} is the temperature coefficient of output voltage which is calculated from an output voltage when $T_a = -30^\circ\text{C}$ and $T_a = +130^\circ\text{C}$.

V_{SE} is calculated from the following formula.

$$V_{SE} = \frac{[V_{OUT}^{*1} - V_{OUT}^{*2}]}{160^{*3}}$$

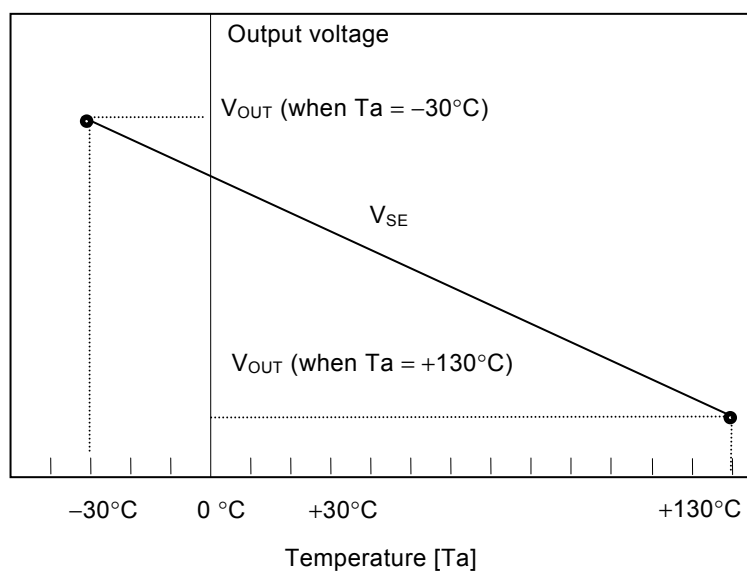


Figure 9

*1. V_{OUT} value at $T_a = +130^\circ\text{C}$ [V]

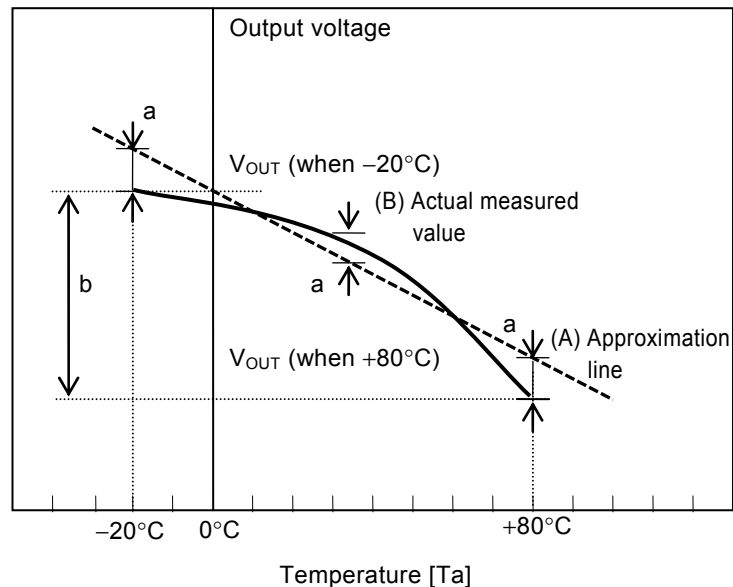
*2. V_{OUT} value at $T_a = -30^\circ\text{C}$ [V]

*3. The difference of the temperature between $T_a = +130^\circ\text{C}$ and $T_a = -30^\circ\text{C}$ [$^\circ\text{C}$]

3. Nonlinearity (ΔN_L)

ΔN_L is the nonlinearity of output voltage. Its deviation with the approximation line is shown below.
 ΔN_L is calculated from the following formula.

$$\Delta N_L = \frac{a^{*1}}{b^{*2}} \times 100$$



- *1. The maximum deviation of the actual measurement of output voltage (B) and an approximation line (A) in temperature -20°C to $+80^{\circ}\text{C}$. The approximation line is the one to be drawn so that "a" should be the minimum value.
- *2. The difference of the actual measured value of output voltage when -20°C and $+80^{\circ}\text{C}$.

Figure 10

4. Line regulation (ΔV_{OUT1})

ΔV_{OUT1} indicates the dependency of output voltage against input voltage. This indicates how much the output voltage varies when changing the input voltage after fixing the output current constant.

5. Load regulation (ΔV_{OUT2})

ΔV_{OUT2} indicates the dependency of output voltage against output current. This indicates how much the output voltage varies when changing output current after fixing the input voltage constant.

6. Current consumption – Power supply voltage (ΔI_{DD1})

ΔI_{DD1} indicates the dependency of current consumption against power supply voltage. This indicates how much current consumption varies when changing the temperature after fixing an output current constant.

7. Current consumption – Temperature (ΔI_{DD2})

ΔI_{DD2} indicates the dependency of current consumption against temperature. This indicates how much current consumption varies when changing the temperature after fixing an output current constant.

■ Precautions

- Wire each pin of VDD, VSS and VOUT carefully in order to set them in low impedance when wiring an IC on a patterned board.
- In this IC, if load capacitance of the VOUT pin is large, VOUT pin voltage may oscillate. It is recommended not to use an external capacitor between the VOUT and VSS pin. When using an external capacitor, set near the VOUT pin. When connecting an A/D converter etc. to the VOUT pin, the input pin capacitance of the A/D converter and the parasitic capacitance component between wires are included as load capacitance. To prevent oscillation, it is recommended to use the following output load condition.

Load capacitance of VOUT pin (C_{L1}) : 300 pF or less

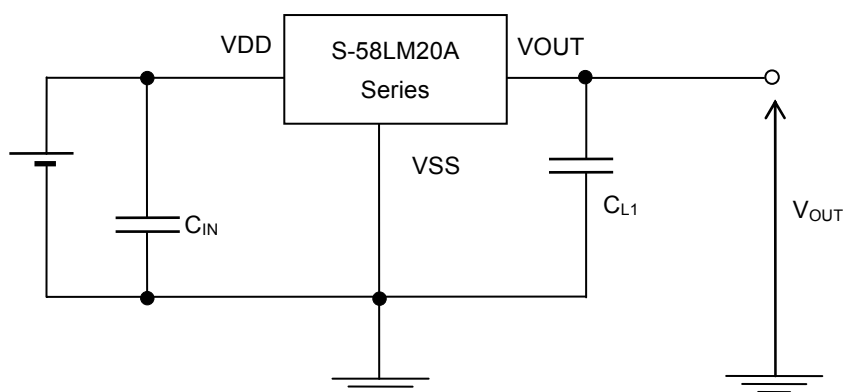


Figure 11

Caution The above connection diagram and constant will not guarantee successful operation. Perform through evaluation using the actual application to set the constant.

In this IC, it is necessary to add a capacitor from an output pin to GND with a series resistor in the ambience having excessive noise, as seen in **Figure 12** and **Figure 13**. In the combination shown in **Table 7**, a time constant against heat of this IC is much later than the time constant composed of RC, therefore it does not affect on the response time of this IC.

Table 7

R_{L2} (Ω)	C_{L2} (μF)
200	1
470	0.1
680	0.01
1 k	0.001

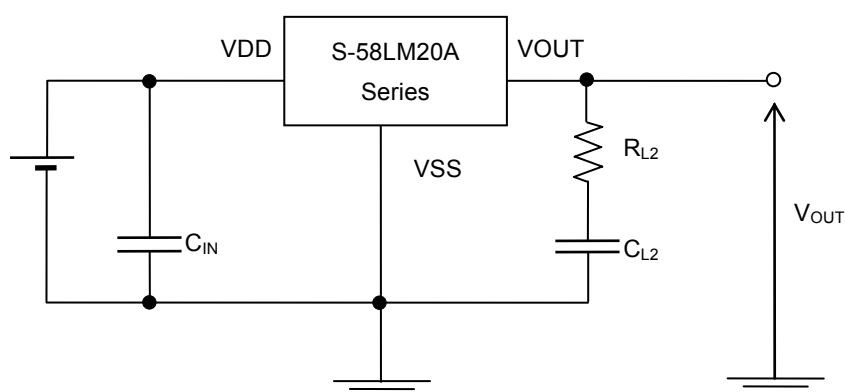


Figure 12

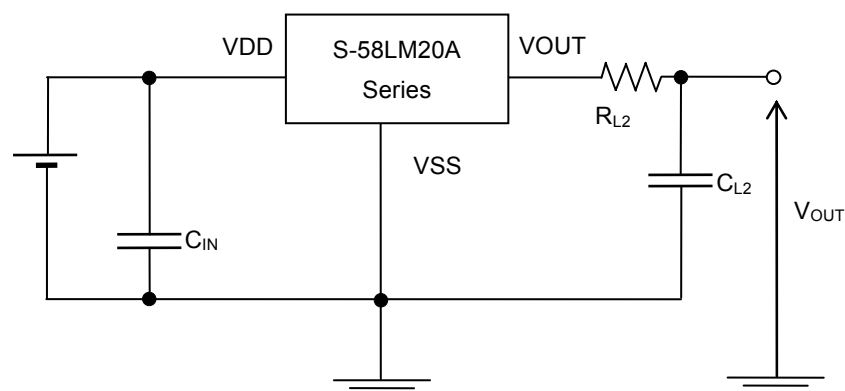


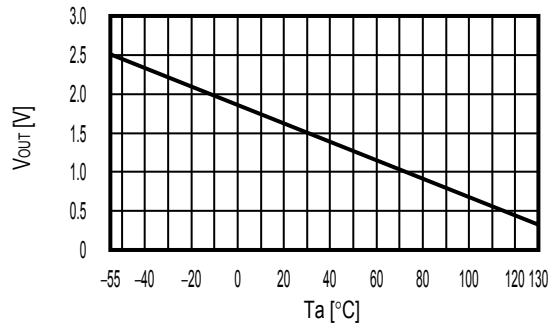
Figure 13

Caution The above connection diagram and constant will not guarantee successful operation. Perform through evaluation using the actual application to set the constant.

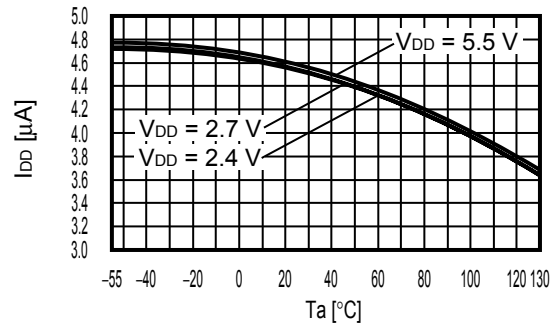
- For stabilization, set a capacitor (C_{IN}) of approx. 0.1 μ F between VDD and VSS pin.
- Do not connect a pull-up resistor to the output pin.
- The application condition for input voltage, output voltage and load voltage must not exceed the package power dissipation.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- Regarding the current at the output pin, refer to load regulation and footnote *1 in **Table 6 “■ Electrical Characteristics”**.
- SII claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.

■ **Characteristics (Typical Data)**

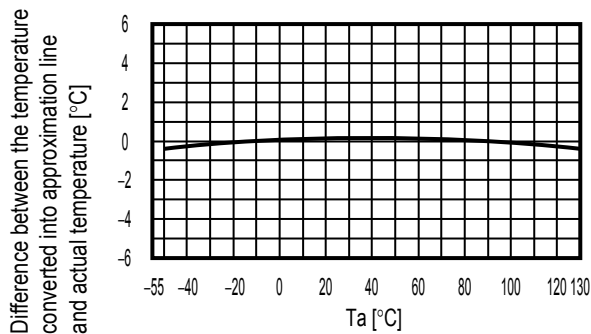
1. Output voltage (V_{OUT}) vs. Temperature (T_a)



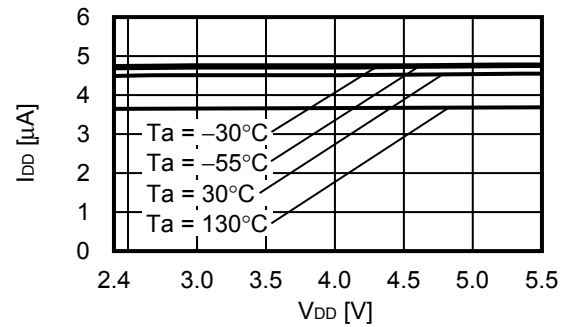
2. Current consumption (I_{DD}) vs. Temperature (T_a)



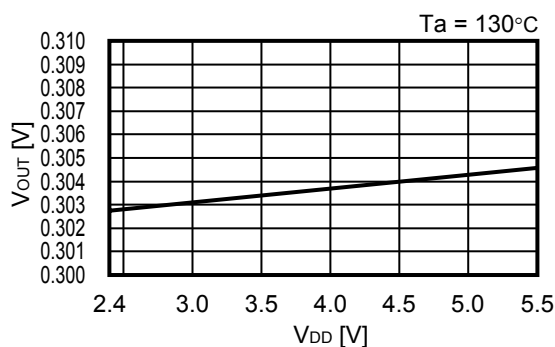
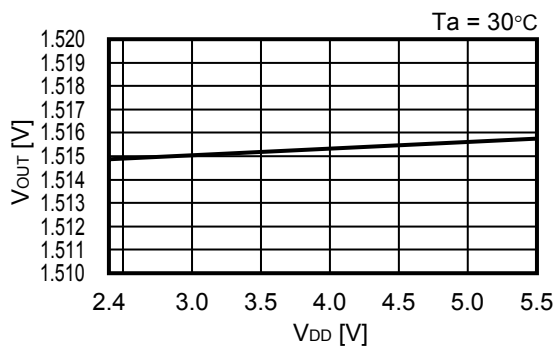
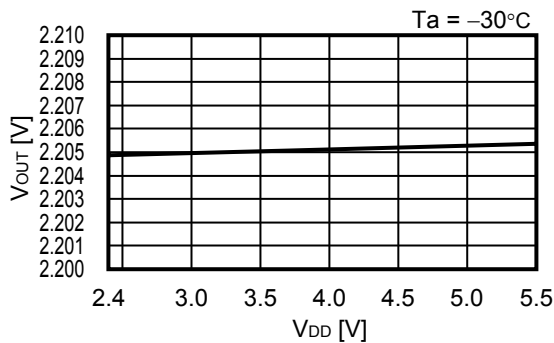
3. Error range of each temperature

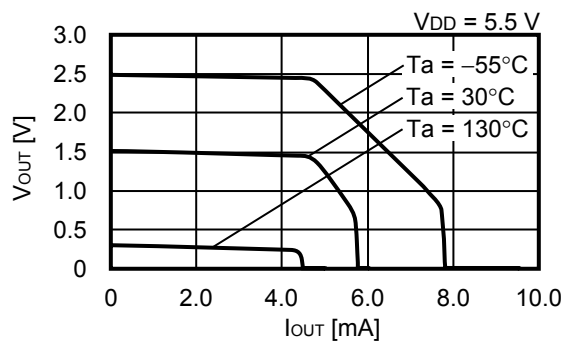
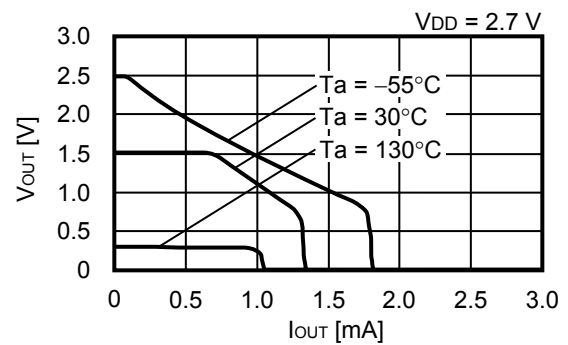
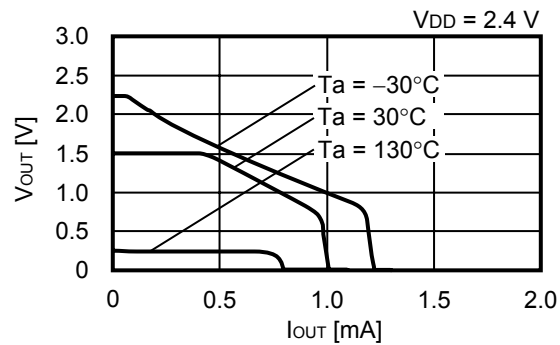


4. Current consumption (I_{DD}) vs. Power supply voltage (V_{DD})

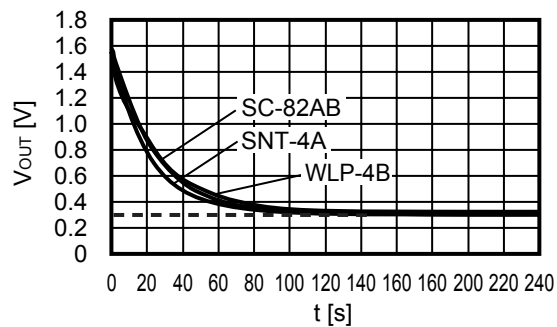


5. Output voltage (V_{OUT}) vs. Power supply voltage (V_{DD})

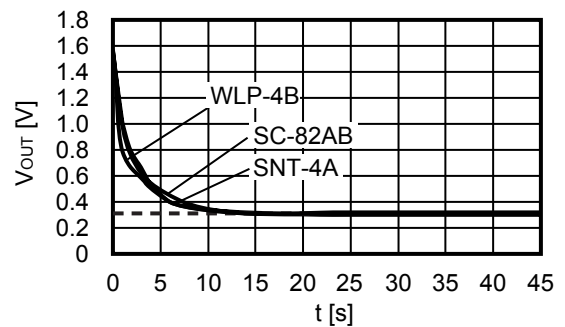
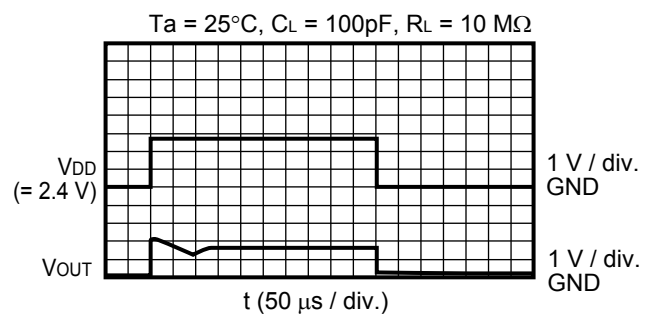
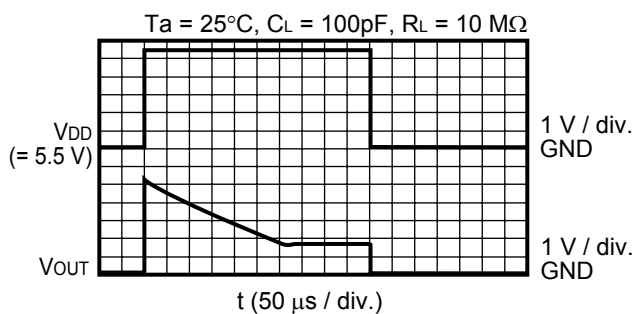


6. Output voltage (V_{OUT}) vs. Load current (I_{OUT})**7. Heat response Output voltage (V_{OUT}) vs. Time (t)**

When packages are put into the air of 130°C from the air of 25°C

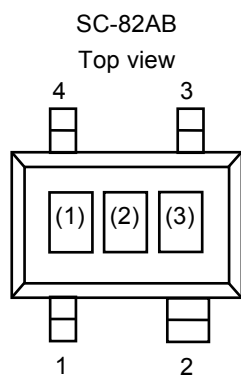


When packages are put into the liquid of 130°C from the air of 25°C

**8. Start up response**

■ Marking Specifications

1. SC-82AB

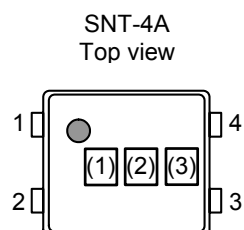


(1) to (3) : Product code (refer to **Product name vs. Product code**)

Product name vs. Product code

Product name	Product code		
	(1)	(2)	(3)
S-58LM20A-N4T1G	D	R	E

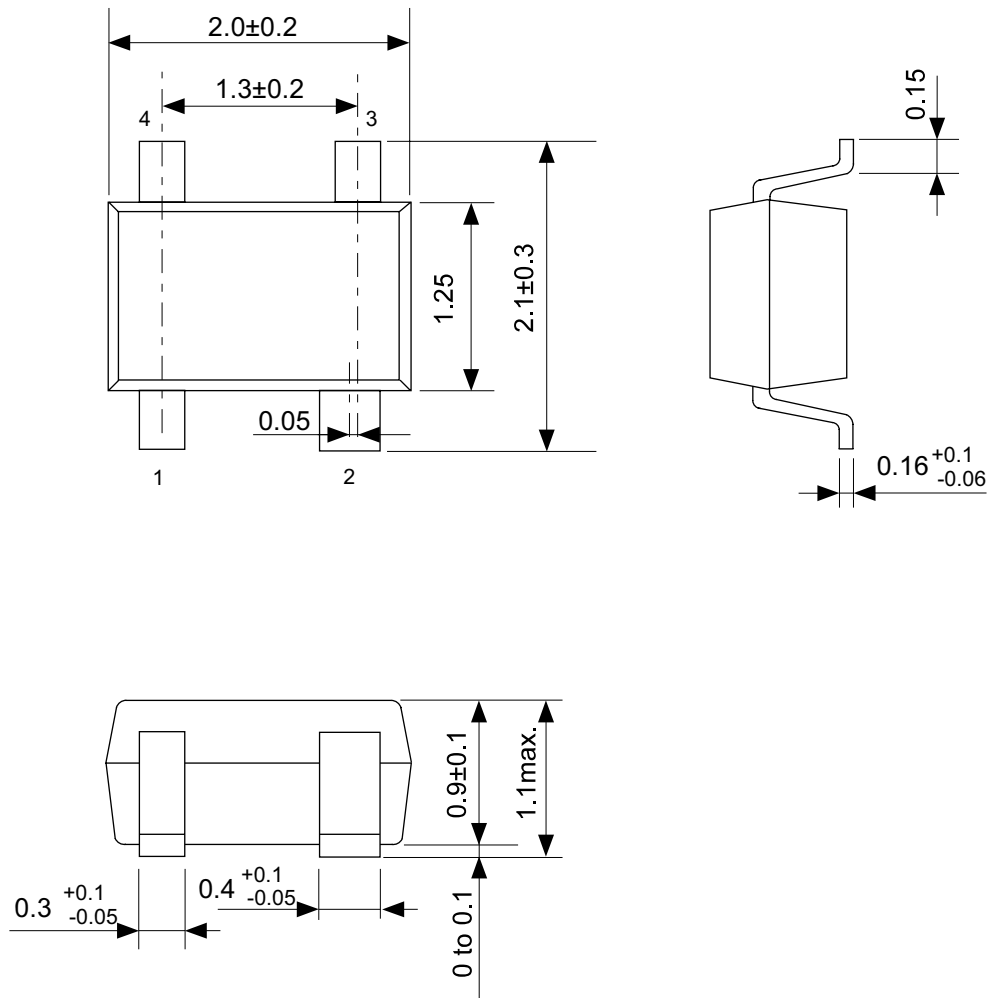
2. SNT-4A



(1) to (3) : Product code (refer to **Product name vs. Product code**)

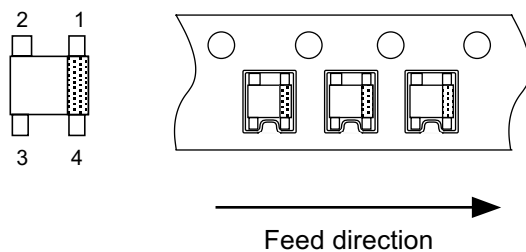
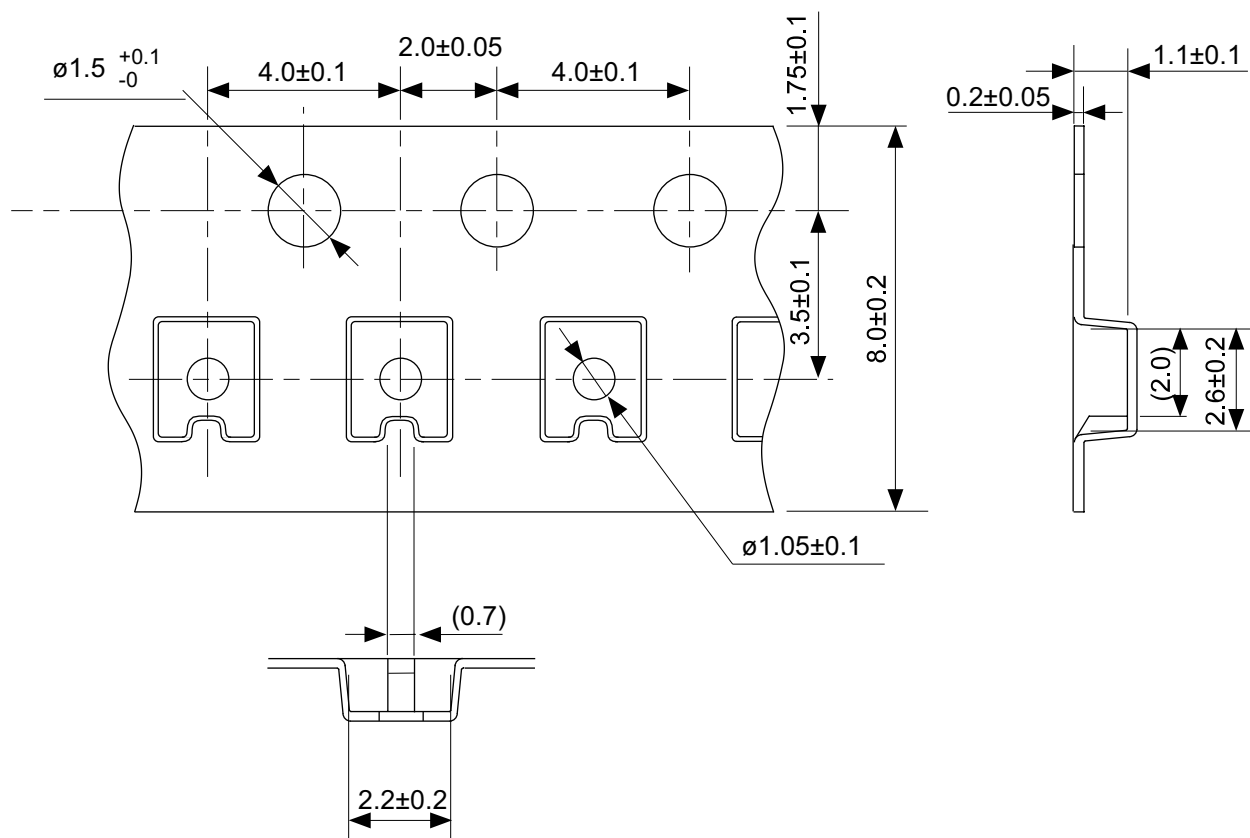
Product name vs. Product code

Product name	Product code		
	(1)	(2)	(3)
S-58LM20A-I4T1G	D	R	E



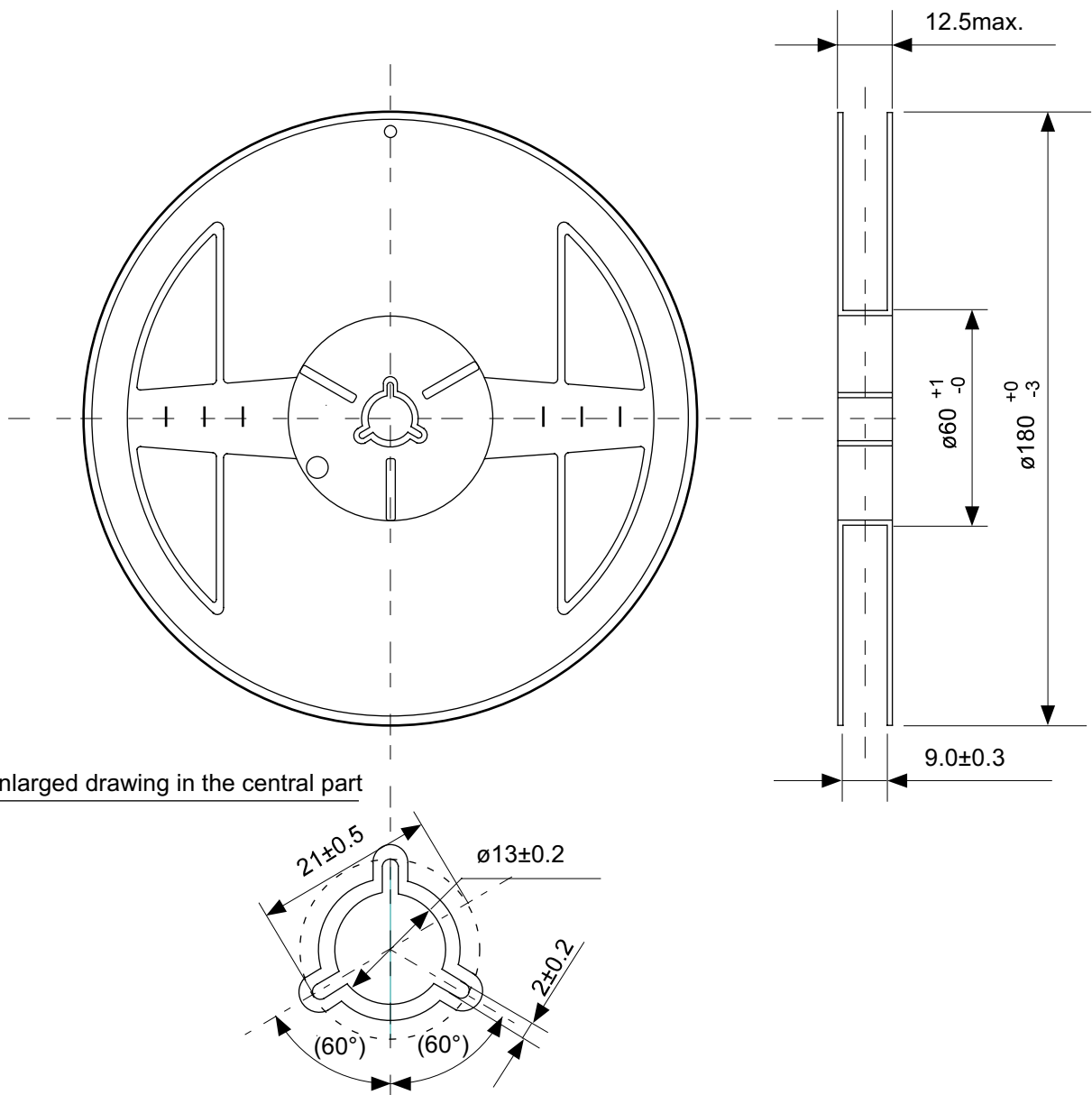
No. NP004-A-P-SD-1.1

TITLE	SC82AB-A-PKG Dimensions
No.	NP004-A-P-SD-1.1
SCALE	
UNIT	mm
Seiko Instruments Inc.	



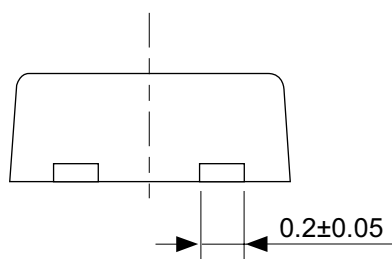
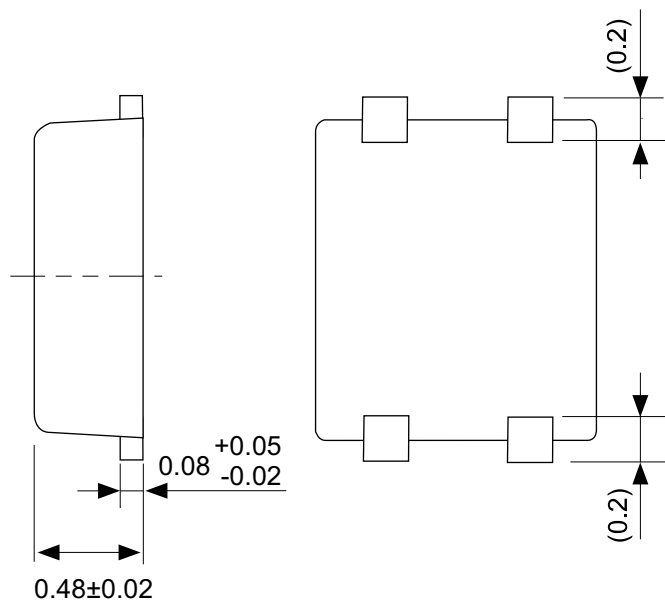
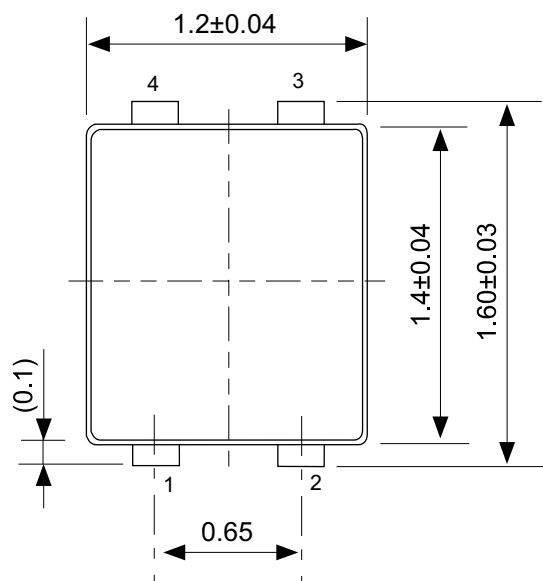
No. NP004-A-C-SD-3.0

TITLE	SC82AB-A-Carrier Tape
No.	NP004-A-C-SD-3.0
SCALE	
UNIT	mm
Seiko Instruments Inc.	



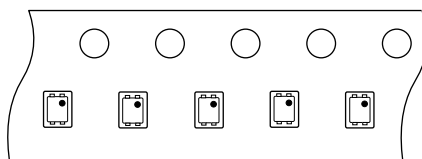
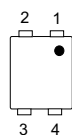
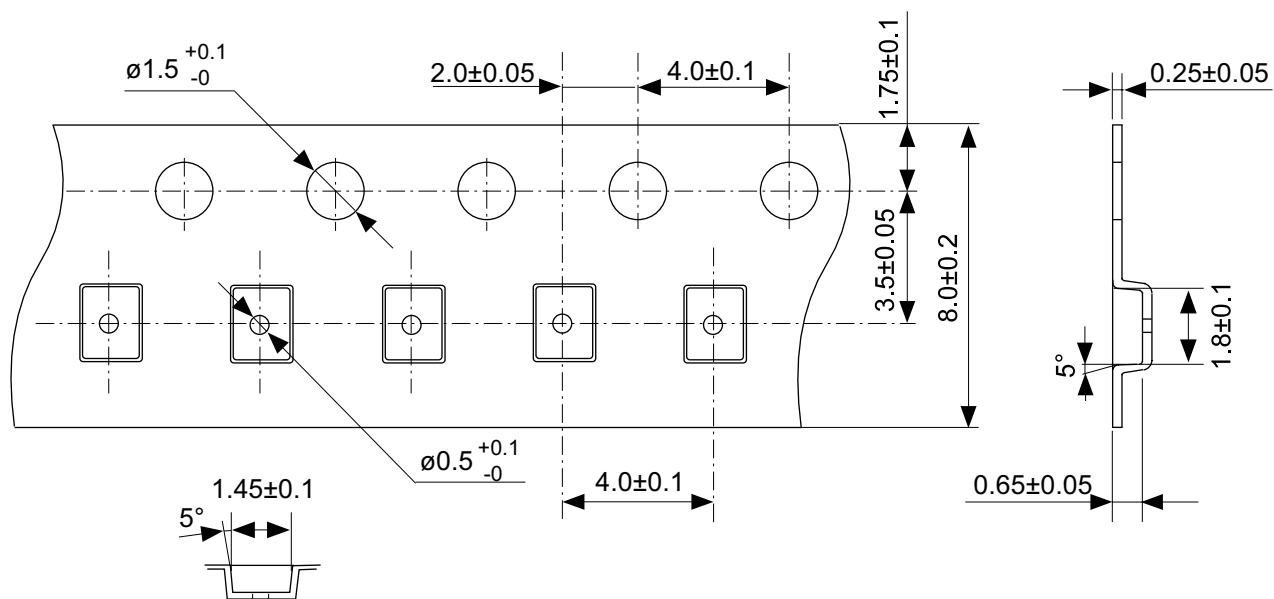
No. NP004-A-R-SD-1.1

TITLE	SC82AB-A-Reel		
No.	NP004-A-R-SD-1.1		
SCALE		QTY.	3,000
UNIT	mm		
Seiko Instruments Inc.			



No. PF004-A-P-SD-4.0

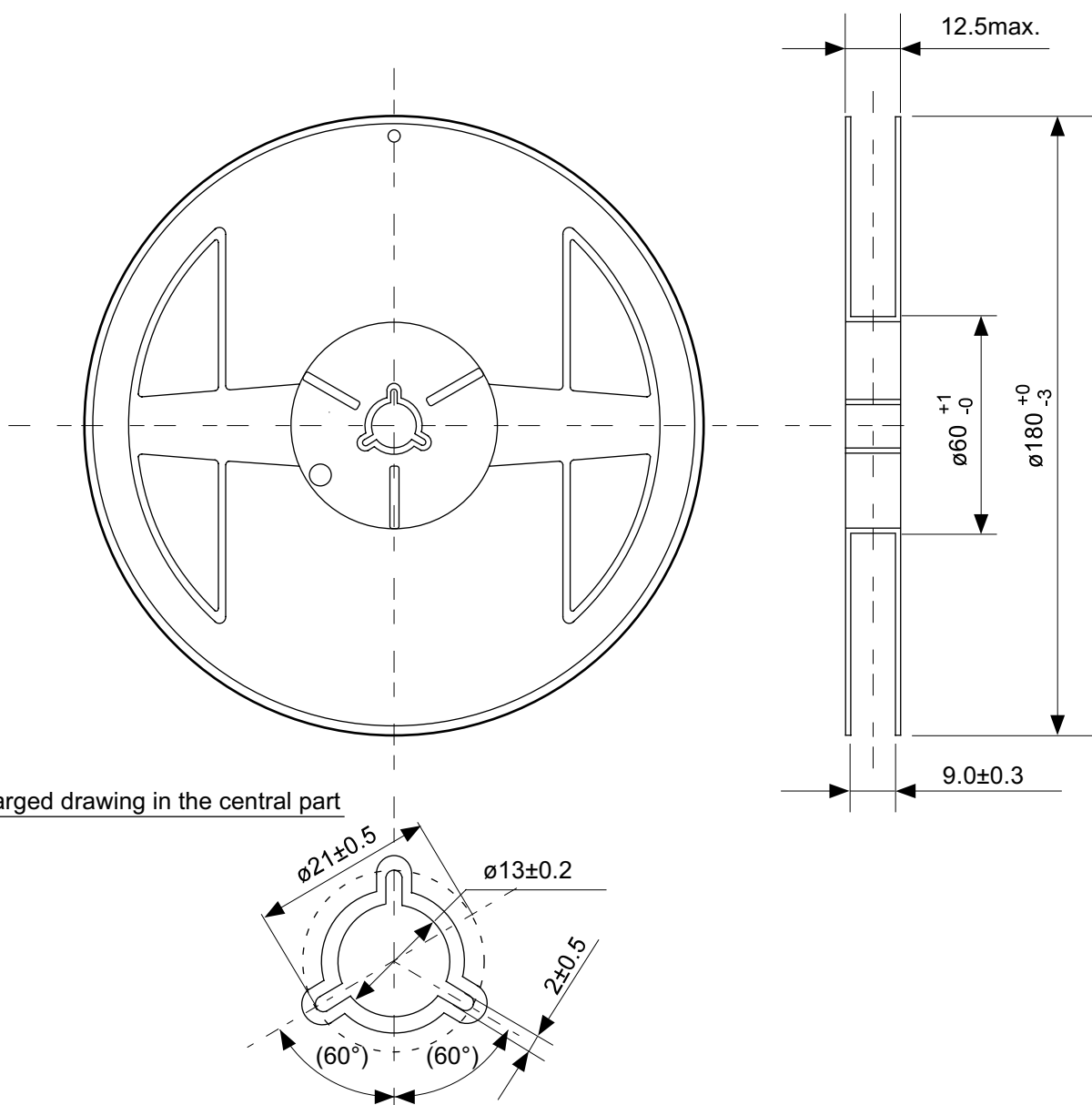
TITLE	SNT-4A-A-PKG Dimensions
No.	PF004-A-P-SD-4.0
SCALE	
UNIT	mm
Seiko Instruments Inc.	



Feed direction

No. PF004-A-C-SD-1.0

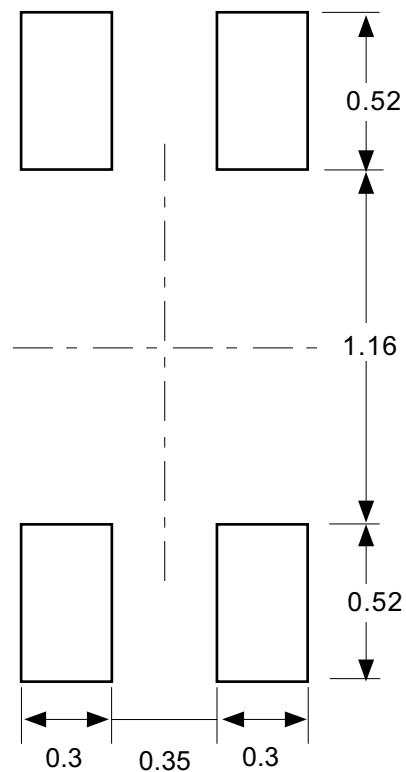
TITLE	SNT-4A-A-Carrier Tape
No.	PF004-A-C-SD-1.0
SCALE	
UNIT	mm
Seiko Instruments Inc.	



Enlarged drawing in the central part

No. PF004-A-R-SD-1.0

TITLE	SNT-4A-A-Reel		
No.	PF004-A-R-SD-1.0		
SCALE		QTY.	5,000
UNIT	mm		
Seiko Instruments Inc.			

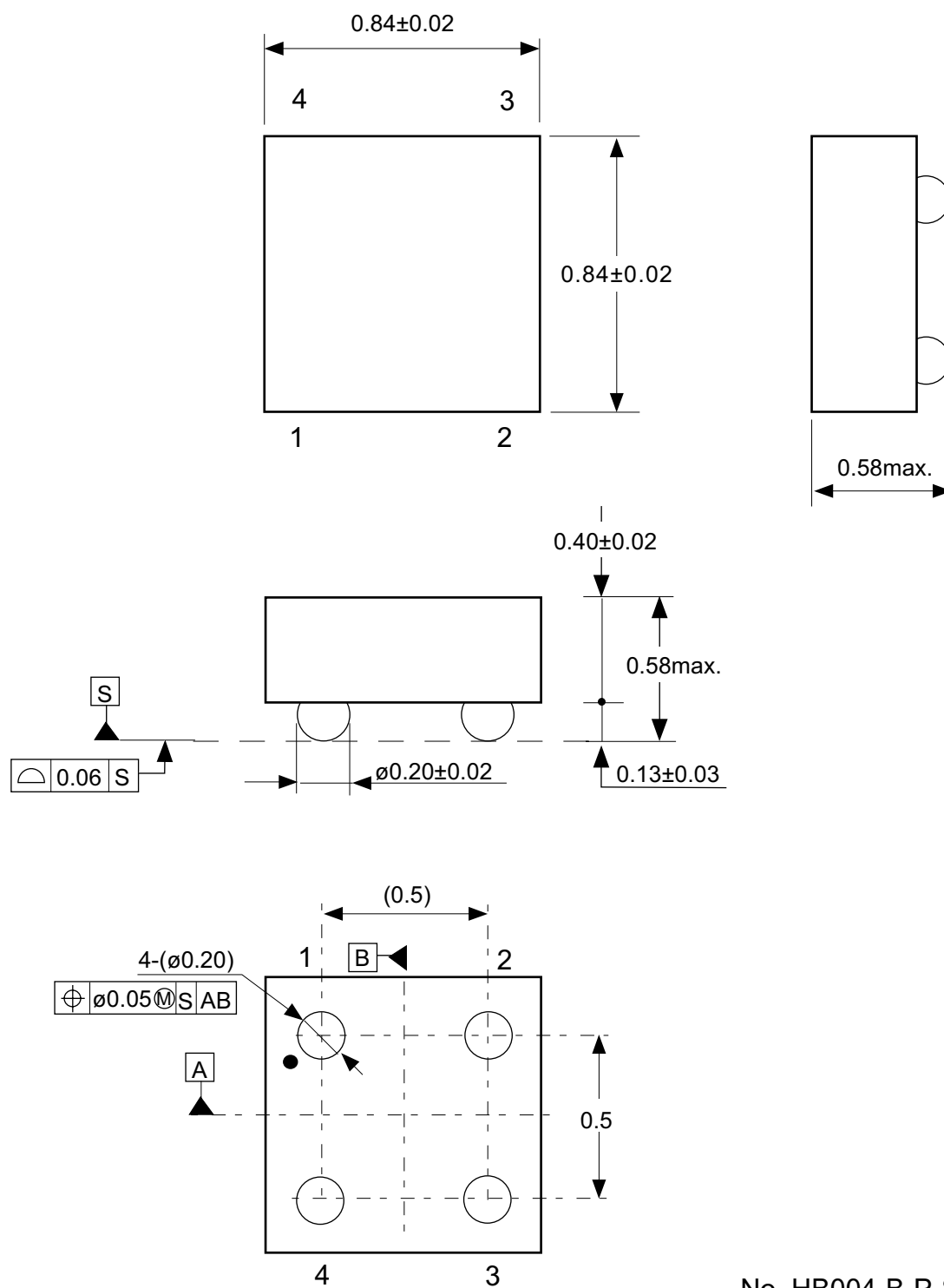


Caution Making the wire pattern under the package is possible. However, note that the package may be upraised due to the thickness made by the silk screen printing and of a solder resist on the pattern because this package does not have the standoff.

注意 パッケージ下への配線パターン形成は可能ですが、本パッケージはスタンドオフが無いので、パターン上のレジスト厚み、シルク印刷の厚みによってパッケージが持ち上がる場合がありますのでご配慮ください。

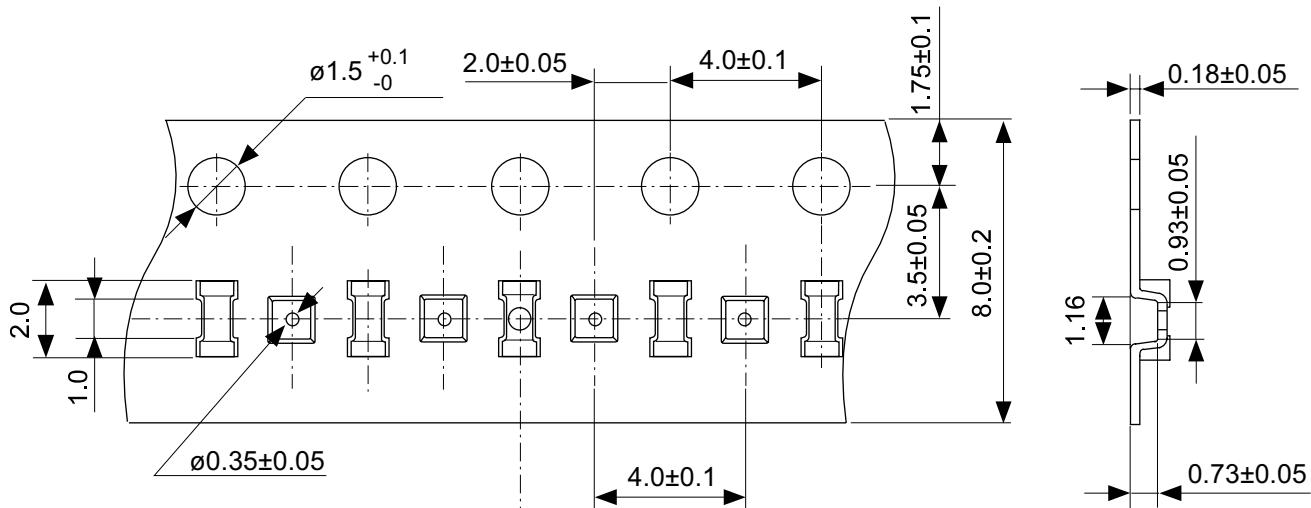
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TITLE	SNT-4A-A-Land Recommendation
No.	PF004-A-L-SD-3.0
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UNIT	mm
Seiko Instruments Inc.	

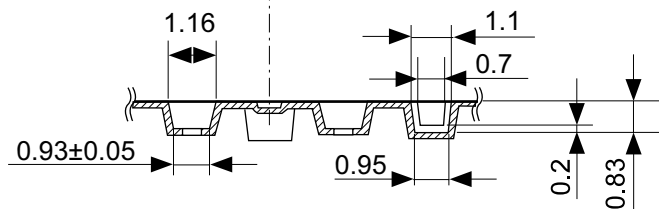


No. HB004-B-P-SD-1.0

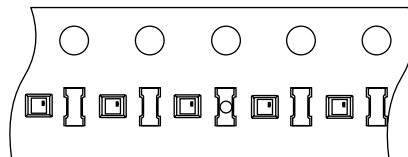
TITLE	WLP-4B-B-PKG Dimensions
No.	HB004-B-P-SD-1.0
SCALE	
UNIT	mm
Seiko Instruments Inc.	



Count mark(R0.3,Depth 0.2)
(Every 10 pockets)



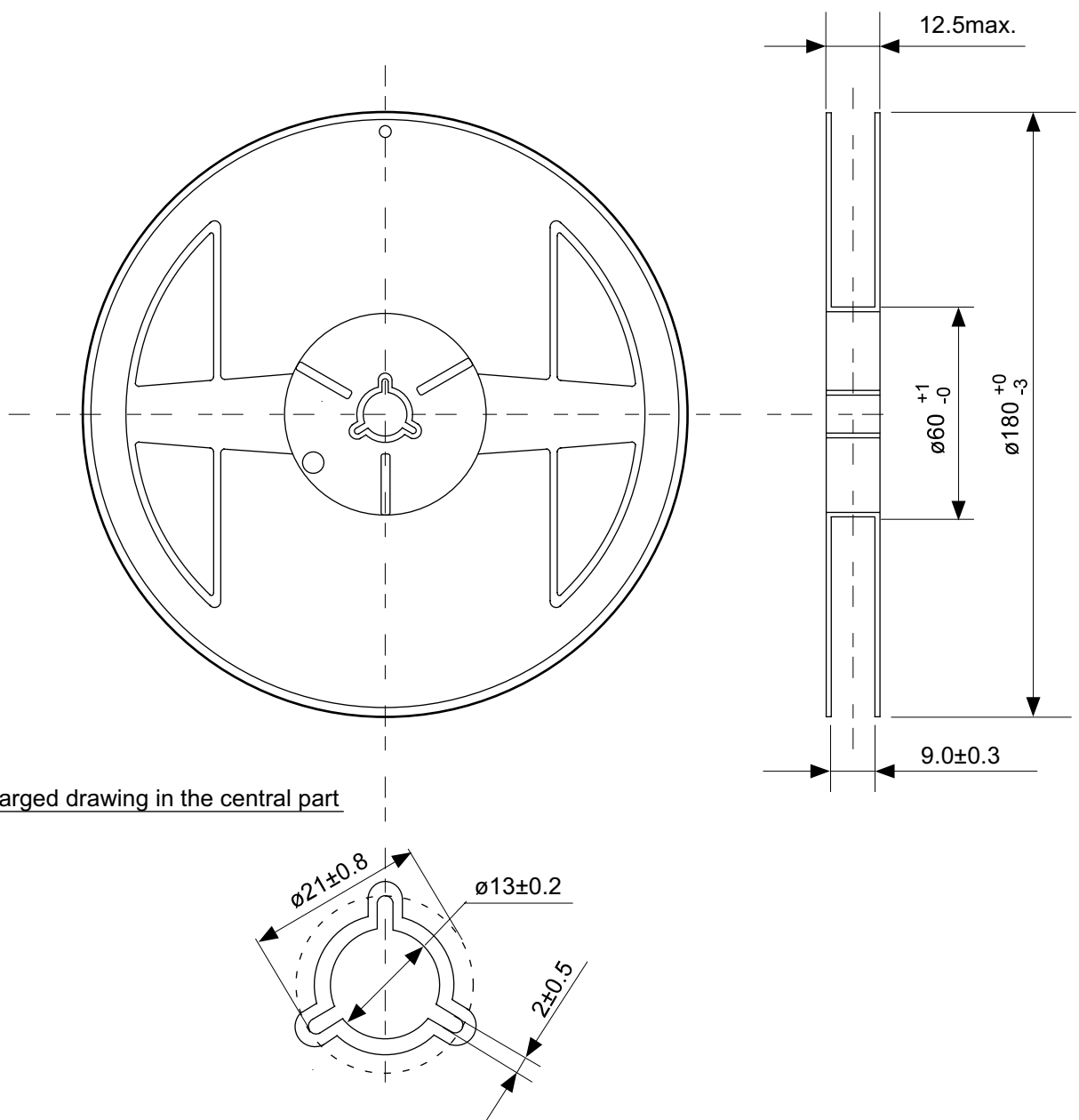
2 1
3 4



Feed direction

No. HB004-B-C-SD-1.0

TITLE	WLP-4B-B-Carrier Tape
No.	HB004-B-C-SD-1.0
SCALE	
UNIT	mm
Seiko Instruments Inc.	



Enlarged drawing in the central part

No. HB004-B-R-SD-1.0

TITLE	WLP-4B-B-Reel		
No.	HB004-B-R-SD-1.0		
SCALE		QTY.	3,000
UNIT	mm		
Seiko Instruments Inc.			

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