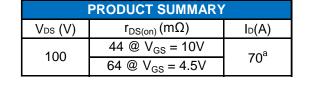
N-Channel 100-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

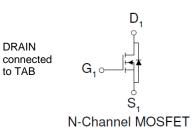
- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits





О

GΟ S Top View



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)								
Parameter		Symbol	Limit	Units				
Drain-Source Voltage		V _{DS}	100	V				
Gate-Source Voltage			±20	v				
Continuous Drain Current ^a	T _A =25°C	I _D	70	А				
Pulsed Drain Current ^b	I _{DM} 280			A				
Continuous Source Current (Diode Conduction) ^a		I _S	70	А				
Power Dissipation ^a	T _A =25°C	PD	300	W				
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C				

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	t <= 10 sec	P	62.5	°C/W
	Steady State	— R _{θJA}	0.5	

Notes

- Surface Mounted on 1" x 1" FR4 Board. a.
- Pulse width limited by maximum junction temperature b.

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA		
	I _{DSS}	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25			
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	140			А		
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$			44	mΩ		
	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$			64			
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		20		S		
Diode Forward Voltage	V_{SD}	$I_{S} = 35 \text{ A}, V_{GS} = 0 \text{ V}$		1		V		
	Dynamic							
Total Gate Charge	Qg	$V_{DS} = 50 \text{ V}, V_{GS} = 5.5 \text{ V},$ $I_{D} = 20 \text{ A}$		22		nC		
Gate-Source Charge	Q_gs			7.3				
Gate-Drain Charge	Q_gd			14				
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 50 V, R_L = 2.5 Ω,$ $I_D = 20 A,$ $V_{GEN} = 10 V, R_{GEN} = 6 Ω$		9		ns		
Rise Time	t _r			13				
Turn-Off Delay Time	t _{d(off)}			44				
Fall Time	t _f			16				
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		1318		pF		
Output Capacitance	C _{oss}			147				
Reverse Transfer Capacitance	C _{rss}			143				

Notes

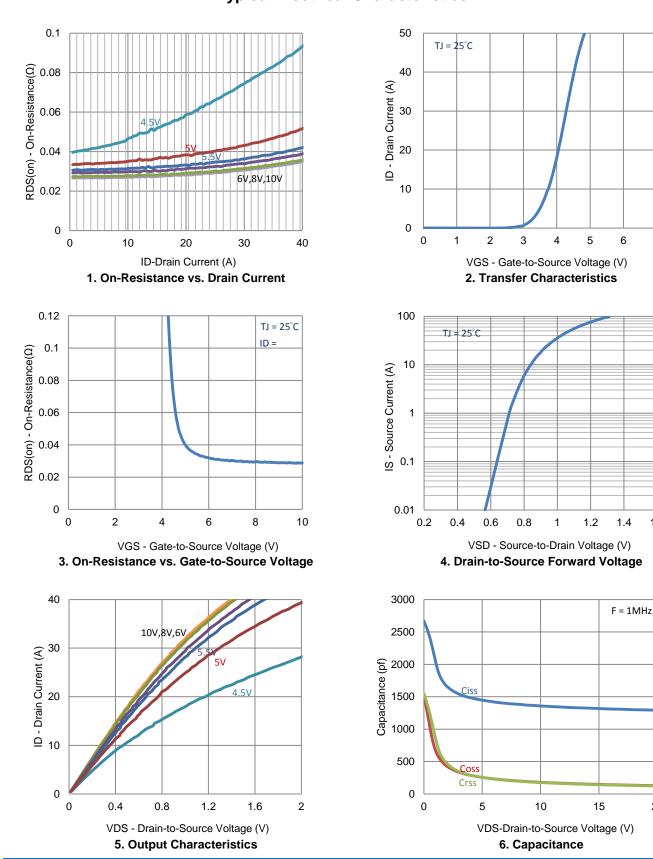
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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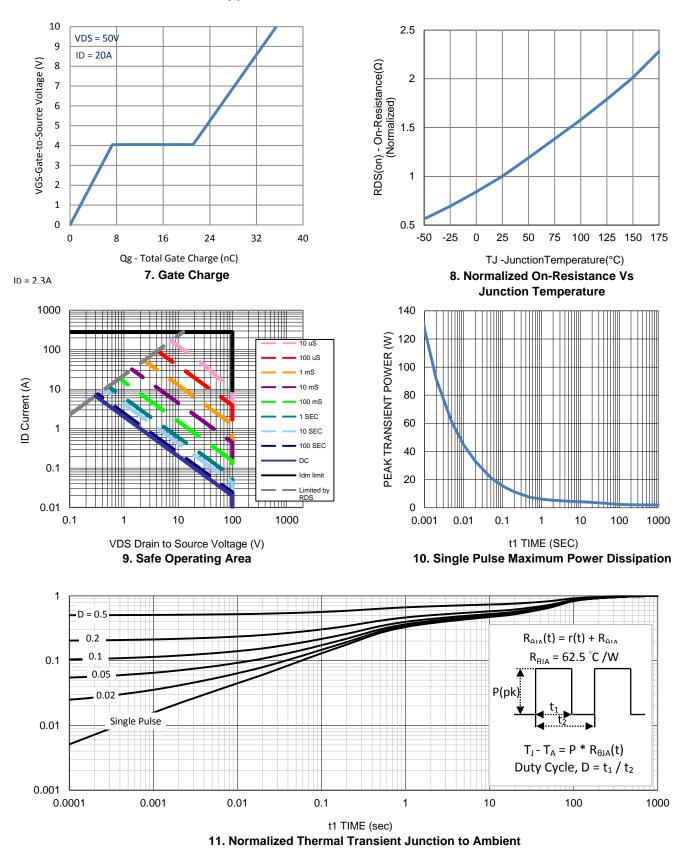
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Typical Electrical Characteristics



Typical Electrical Characteristics

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Package Information

