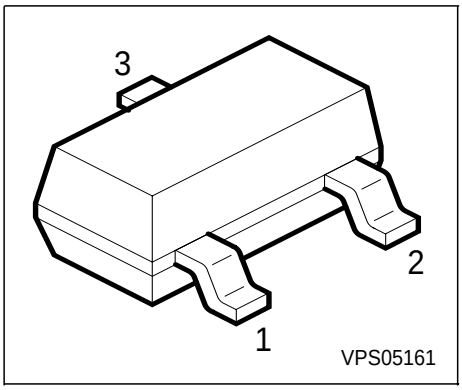
PNP Silicon Switching Transistor

- High DC current gain: 0.1mA to 500 mA
- Low collector-emitter saturation voltage
- Complementary type: SMBT 2222A (NPN)



Type	Marking	Ordering Code	Pin Configuration			Package
SMBT 2907A	s2F	Q68000-A6474	1=B	2=E	3=C	SOT-23

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	60	V
Collector-base voltage	V_{CBO}	60	
Emitter-base voltage	V_{EBO}	5	
DC collector current	I_C	600	mA
Total power dissipation, T_S = 77 °C	P_{tot}	330	mW
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-65 ... +150	

Thermal Resistance

Junction ambient 1)	R_{thJA}	≤290	K/W
Junction - soldering point	R_{thJS}	≤220	

1) Package mounted on pcb 40mm x 40mm x 1.5mm / 6cm^2 Cu

Electrical Characteristicsn at T_A = 25°C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage I_C = 10 mA, I_B = 0	$V_{(BR)CEO}$	60	-	-	V
Collector-base breakdown voltage I_C = 10 µA, I_B = 0	$V_{(BR)CBO}$	60	-	-	
Emitter-base breakdown voltage I_E = 10 µA, I_C = 0	$V_{(BR)EBO}$	5	-	-	
Collector cutoff current V_{CB} = 50 V, I_E = 0	I_{CBO}	-	-	10	nA
Collector cutoff current V_{CB} = 50 V, I_E = 0 , T_A = 150 °C	I_{CBO}	-	-	10	µA
Emitter cutoff current V_{EB} = 3 V, I_C = 0	I_{EBO}	-	-	10	nA
DC current gain 1) I_C = 100 µA, V_{CE} = 10 V I_C = 1 mA, V_{CE} = 10 V I_C = 10 mA, V_{CE} = 10 V I_C = 150 mA, V_{CE} = 10 V I_C = 500 mA, V_{CE} = 10 V	h_{FE}	 75 100 100 100 50	 - - - - -	 - - - 300 -	-
Collector-emitter saturation voltage1) I_C = 150 mA, I_B = 15 mA I_C = 500 mA, I_B = 50 mA	V_{CEsat}	 - -	 - -	 0.4 1.6	V
Base-emitter saturation voltage 1) I_C = 150 mA, I_B = 15 mA I_C = 500 mA, I_B = 50 mA	V_{BEsat}	 - -	 - -	 1.3 2.6	

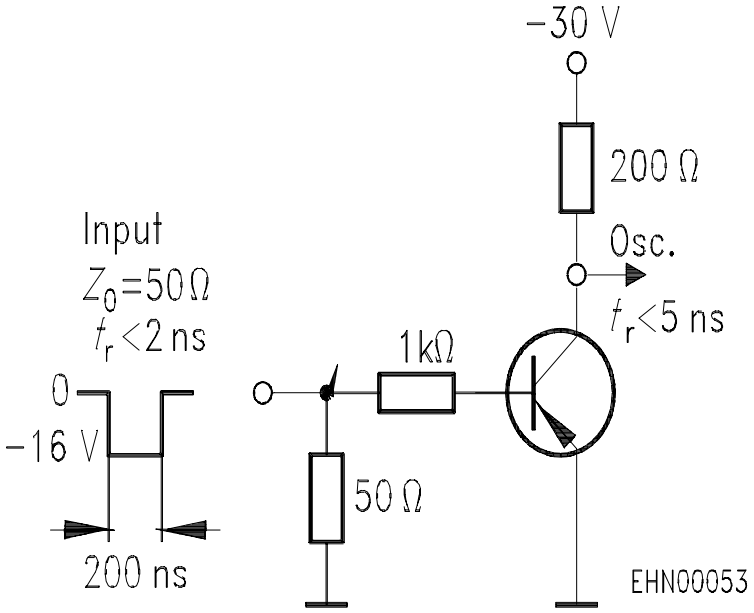
1) Pulse test: t ≤ 300µs, D = 2%

Electrical Characteristics at T_A = 25°C, unless otherwise specified.

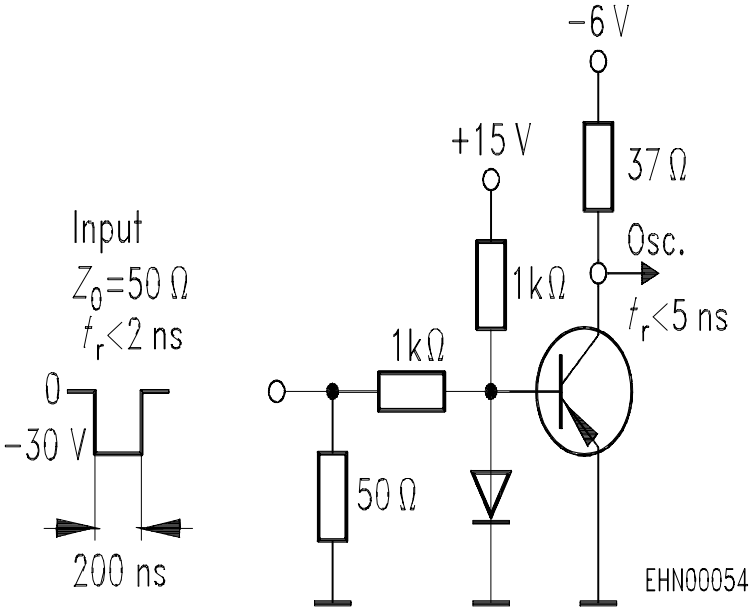
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics					
Transition frequency I_C = 20 mA, V_{CE} = 20 V, f = 100 MHz	f_T	200	-	-	MHz
Collector-base capacitance V_{CB} = 10 V, f = 1 MHz	C_{cb}	-	-	8	pF
Emitter-base capacitance V_{EB} = 0.5 V, f = 1 MHz	C_{eb}	-	-	30	
Delay time V_{CC} = 30 V, I_C = 150 mA, I_{B1} = 15 mA, $V_{BE(off)}$ = 0.5 V	t_d	-	-	10	ns
Rise time V_{CC} = 30 V, I_C = 150 mA, I_{B1} = 15 mA, $V_{BE(off)}$ = 0.5 V	t_r	-	-	40	
Storage time V_{CC} = 30 V, I_C = 150 mA, I_{B1}=I_{B2} = 15mA	t_{stg}	-	-	80	
Fall time V_{CC} = 30 V, I_C = 150 mA, I_{B1}=I_{B2} = 15mA	t_f	-	-	30	

Test circuits

Delay and rise time



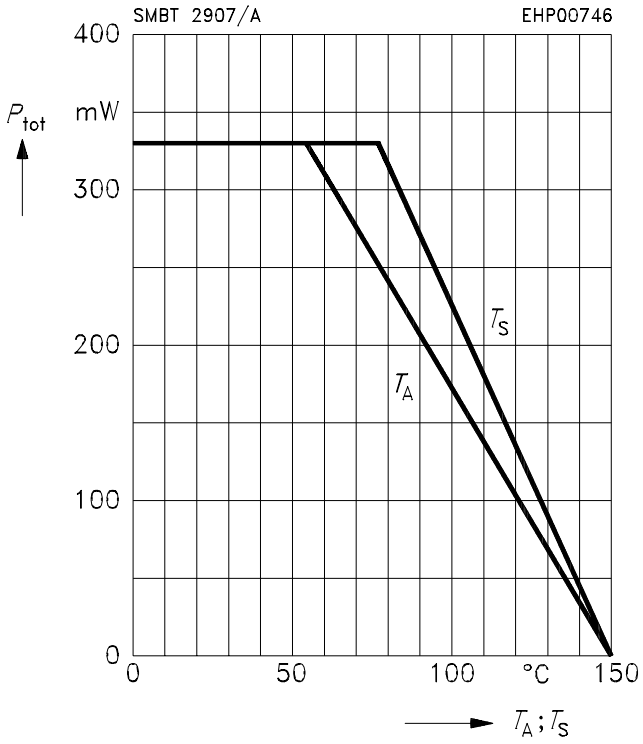
Storage and fall time



Oscillograph: $R > 100\Omega$, $C < 12pF$, $t_r < 5ns$

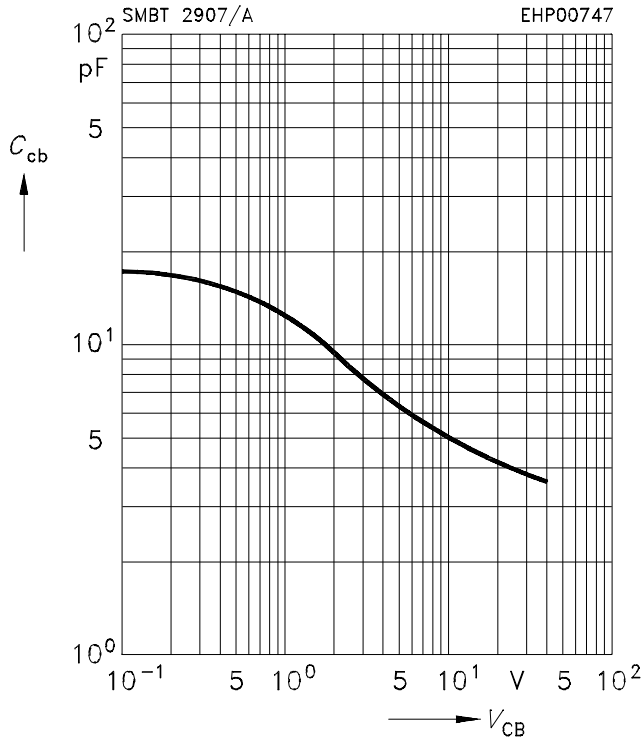
Total power dissipation $P_{tot} = f(T_A^*; T_S)$

* Package mounted on epoxy



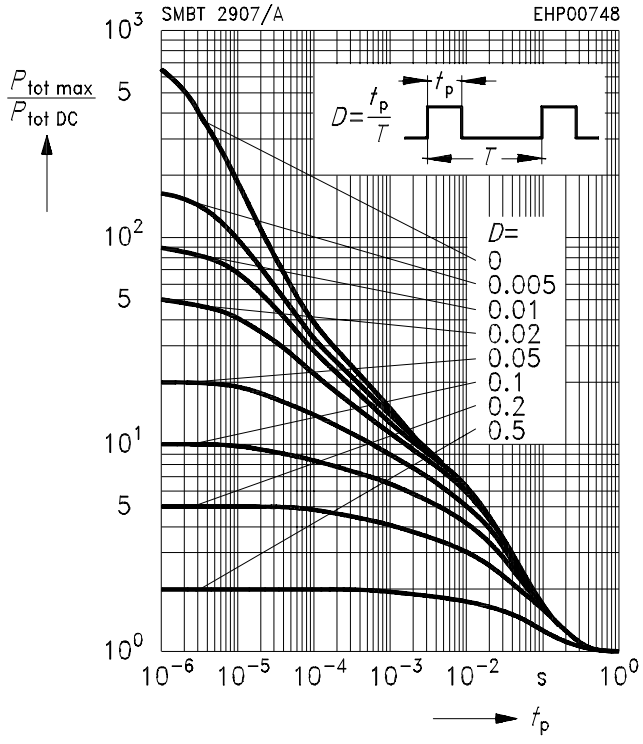
Collector-base capacitance $C_{CB} = f(V_{CB})$

f = 1MHz



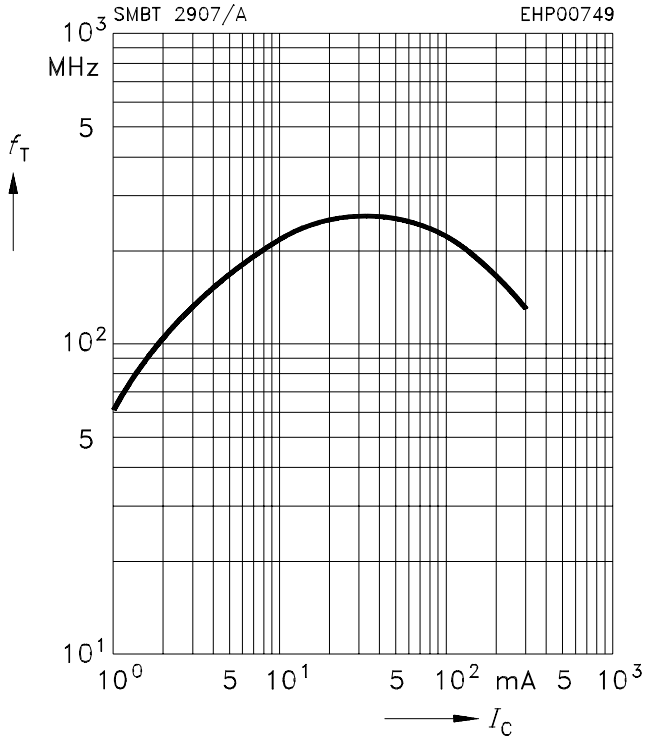
Permissible pulse load

$P_{totmax} / P_{totDC} = f(t_p)$

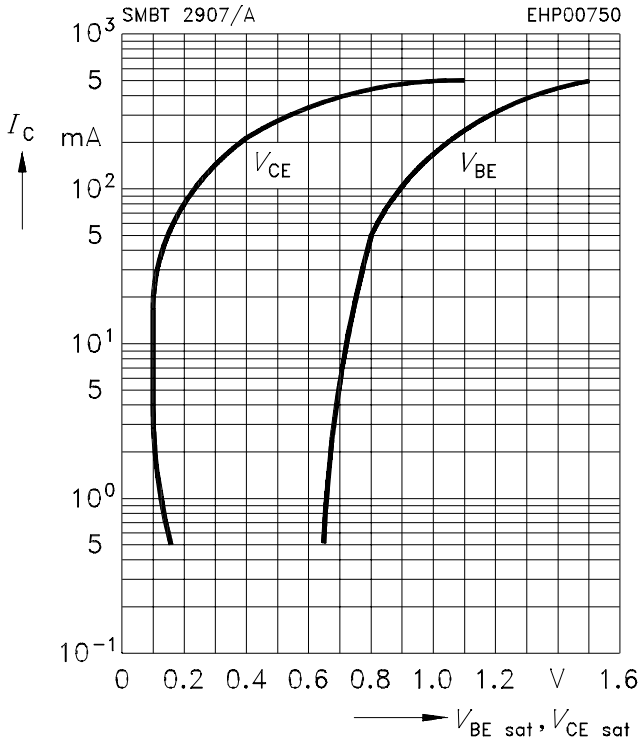


Transition frequency $f_T = f(I_C)$

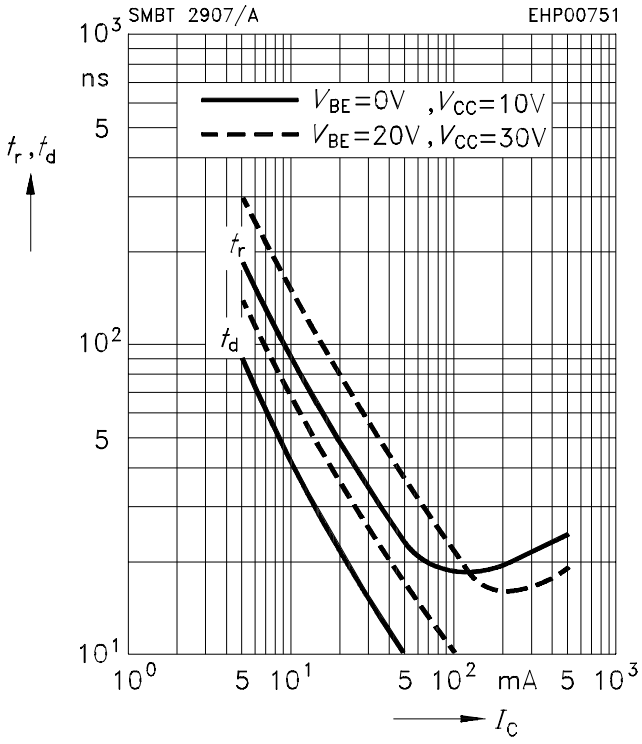
V_{CE} = 5V



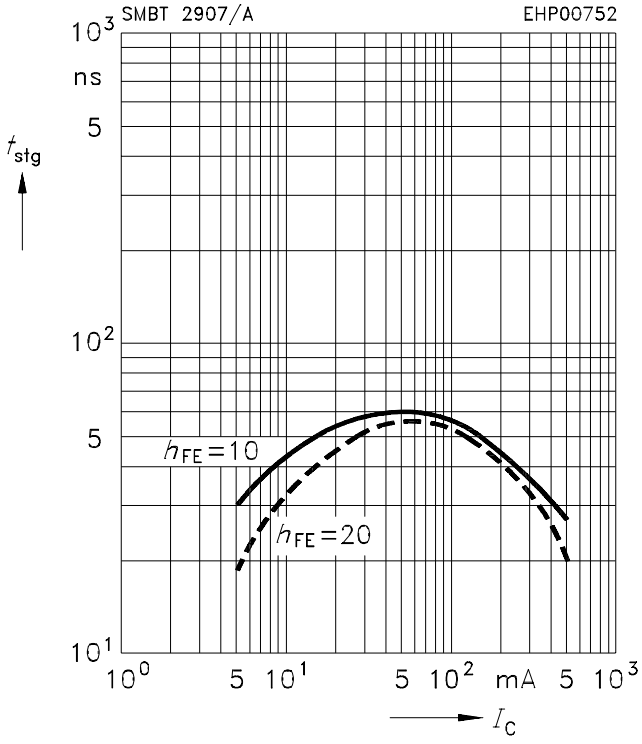
Saturation voltage $I_C = f(V_{BEsat}, V_{CEsat})$
$h_{FE} = 10$



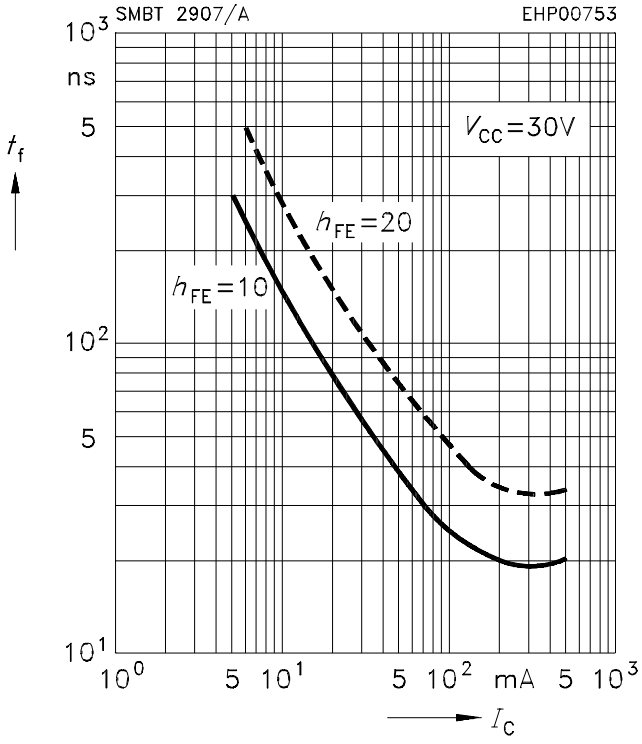
Delay time $t_d = f(I_C)$
Rise time $t_r = f(I_C)$



Storage time $t_{stg} = f(I_C)$



Fall time $t_f = f(I_C)$



DC current gain $h_{FE} = f(I_C)$

$V_{CE} = 5V$

