

Multilayer Ceramic Capacitors

OPEN-MODE DESIGN

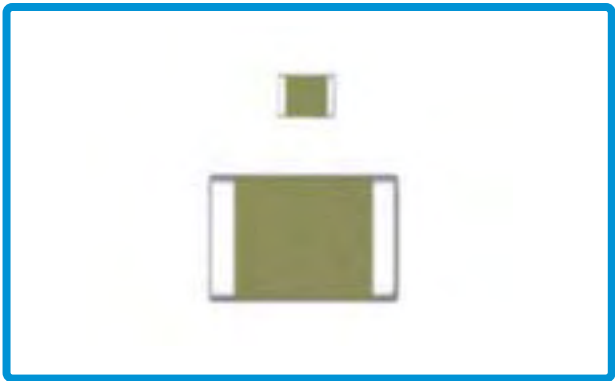


OP Series

MERITEK

FEATURES

- Minimize the short circuit probability due to flex cracks
- High current applications
- Input side filtering



PART NUMBER SYSTEM

	<u>OP</u>	<u>0805</u>	<u>XR</u>	<u>101</u>	<u>K</u>	<u>500</u>
Meritek Series						
Size						
Dielectric						
CODE	XR					
	X7R					
Capacitance						
Expressed in picofarads. First 2 digits are significant digits. Third digit denotes number of zeros to follow. Use R for decimal point for values less than 10pF.						
CODE	8R2	101				
pF	8.2	100				
Tolerance						
CODE	B	C	D	G	J	
	±.10pF	±.25pF	±.50pF	±2%	±5%	
For values less than 10 pF use C or D						
Rated Voltage						
2 significant digits + number of zeros.						
CODE	250	500				
	25V	50V				

INNER CONSTRUCTION OF OPEN-MODE DESIGN

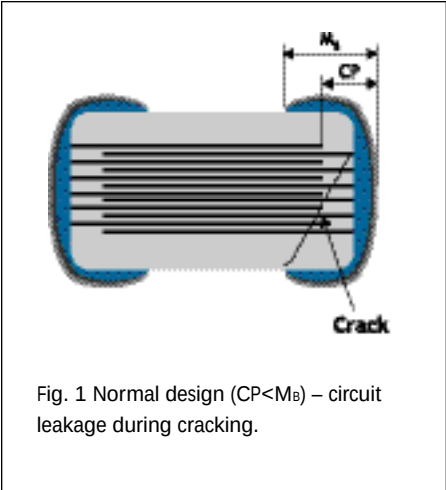


Fig. 1 Normal design ($CP < M_b$) – circuit leakage during cracking.

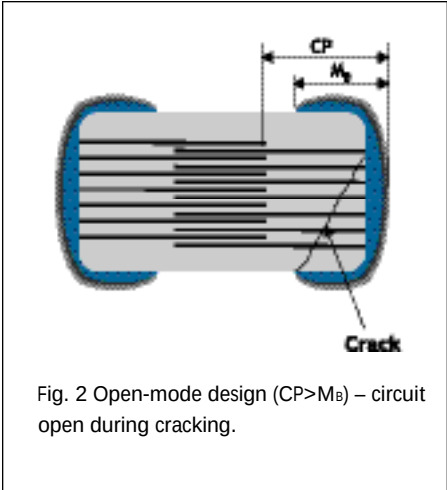


Fig. 2 Open-mode design ($CP > M_b$) – circuit open during cracking.

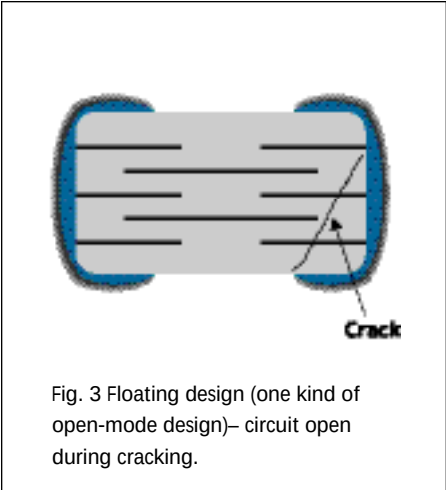


Fig. 3 Floating design (one kind of open-mode design) – circuit open during cracking.