Direct Attach DA3547™ LEDs

C*xxx*DA3547-S*xxx*00

Data Sheet

Cree's Direct Attach DA3547 LEDs are the next generation of solid-state LED emitters that combine highly efficient InGaN materials with Cree's proprietary device technology and silicon-carbide substrates to deliver superior value for the TV-backlighting and general-illumination markets. The DA3547 LEDs are among the brightest in the top-view market while delivering a low forward voltage, resulting in a very bright and highly efficient solution. The bondpad-down design allows for eutectic die attach, eliminating the need for wire bonds, and enables superior performance from improved thermal management. The design is optimally suited for industry-standard top-view packages.

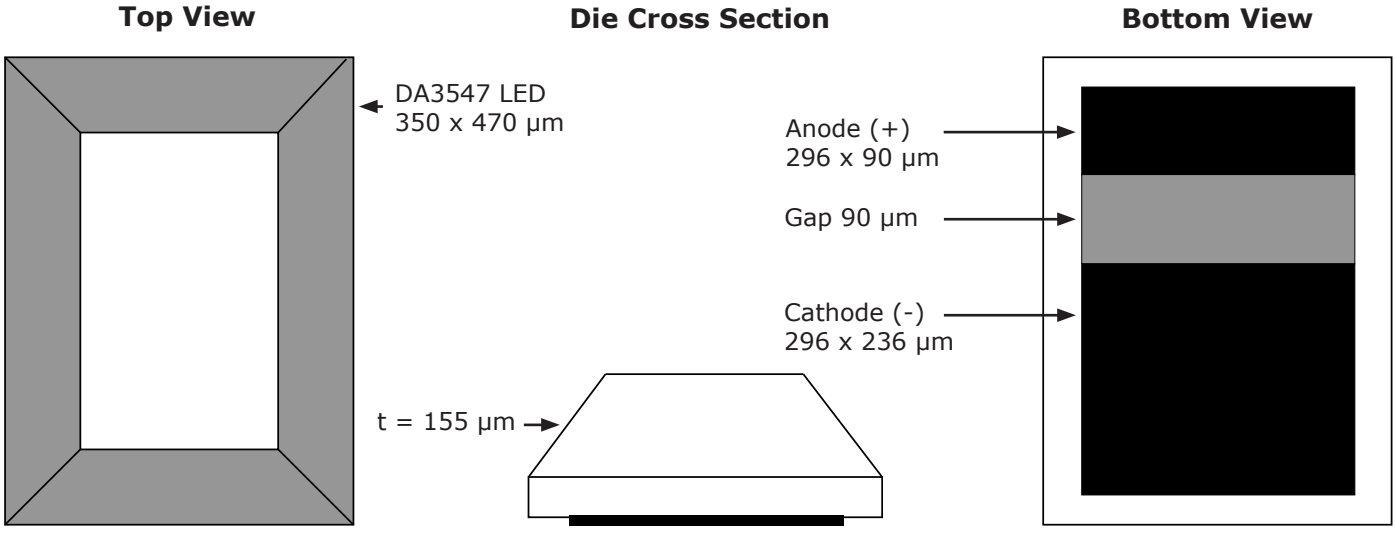
FEATURES

- Rectangular LED RF Performance
  - 450 & 460 nm – 76 mW min
- High Reliability - Eutectic Attach
- Low Forward Voltage (Vf) – 3.1 V Typical at 50 mA
- Maximum DC Forward Current – 150 mA
- 1000-V ESD Threshold Rating
- InGaN Junction-Down Design for Improved Thermal Management
- No Wire Bonds Required

APPLICATIONS

- Large LCD Backlighting
  - Television
- General Illumination
- Medium LCD Backlighting
  - Portable PCs
  - Monitors
- LED Video Displays
- White LEDs

C*xxx*DA3547-S*xxx*00 Chip Diagram



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Maximum Ratings at T_A = 25°C Notes 1,3, & 4	CxxxDA3547-Sxxx00
DC Forward Current	150 mA
Peak Forward Current (1/10 duty cycle @ 1 kHz)	200 mA
LED Junction Temperature	150°C
Reverse Voltage	5 V
Operating Temperature Range	-40°C to +100°C
Storage Temperature Range	-40°C to +100°C
Electrostatic Discharge Threshold (HBM) Note 2	1000 V
Electrostatic Discharge Classification (MIL-STD-883E) Note 2	Class 2

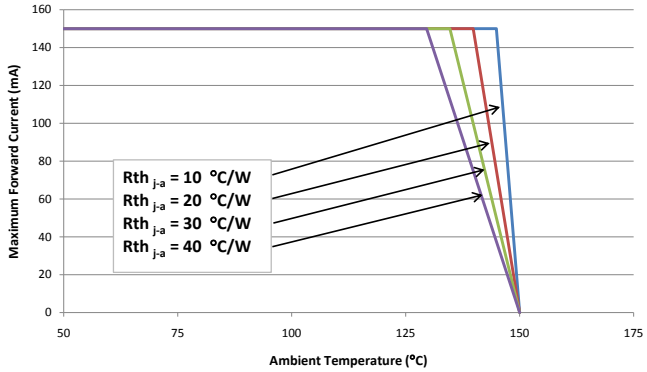
Typical Electrical/Optical Characteristics at T_A = 25°C, If = 50 mA Note 3

Part Number	Forward Voltage (V_f, V)			Reverse Current [I(Vr=5V), µA]	Full Width Half Max (λ_D, nm)
	Min.	Typ.	Max.	Max.	Typ.
C450DA3547-Sxxx00	2.8	3.1	3.4	2	20
C460DA3547-Sxxx00	2.8	3.1	3.4	2	21

Mechanical Specifications	CxxxDA3547-Sxxx00	
Description	**Dimension**	**Tolerance**
P-N Junction Area (µm)	296 x 416	±35
Chip Bottom Area (µm)	350 x 470	±35
Chip Top Area (µm)	200 x 320	±35
Chip Thickness (µm)	155	±15
AuSn Bond Pad Width – Anode (um)	90	±15
AuSn Bond Pad Length – Anode (um)	296	±35
AuSn Bond Pad Width – Cathode (um)	236	±35
AuSn Bond Pad Length – Cathode (um)	296	±35
Bond Pad Gap (µm)	90	±15
AuSn Bond Pad Thickness (µm)	3	±0.5

Notes:

1. Maximum ratings are package-dependent. The above ratings were determined using a chip sub-mount on MCPCB (with silicone encapsulation and intrinsic AuSn metal die attach) for characterization. Ratings for other packages may differ. Junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (< 5 seconds).
2. Product resistance to electrostatic discharge (ESD) according to the HBM is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the maximum ESD ratings shown.
3. All products conform to the listed minimum and maximum specifications for electrical and optical characteristics when assembled and operated at 50 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are within the range of average values expected by manufacturer in large quantities and are provided for information only. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy encapsulant and intrinsic AuSn metal die attach). Optical characteristics measured in an integrating sphere using Illuminance E.
4. The maximum forward current is determined by the thermal resistance between the LED junction and ambient. It is crucial for the end-product to be designed in a manner that minimizes the thermal resistance from the LED junction to ambient in order to optimize product performance.



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Standard Bins for C*xxx*DA3547-S*xxx*00

LED chips are sorted to the **radiant flux** and **dominant wavelength** bins shown. A sorted die sheet contains die from only one bin. Sorted die kit (C*xxx*DA3547-S*xxxxx*) orders may be filled with any or all bins (C*xxx*DA3547-*xxxxx*) contained in the kit. All radiant flux and dominant wavelength values shown and specified are at If = 50 mA.

C450DA3547-S07600

Radiant Flux (mW)	445 – 447.5	447.5 – 450	450 – 452.5	452.5 – 455
88	C450DA3547-0313	C450DA3547-0314	C450DA3547-0315	C450DA3547-0316
82	C450DA3547-0309	C450DA3547-0310	C450DA3547-0311	C450DA3547-0312
76	C450DA3547-0305	C450DA3547-0306	C450DA3547-0307	C450DA3547-0308

Dominant Wavelength (nm)

C460DA3547-S07600

Radiant Flux (mW)	455 – 457.5	457.5 – 460	460 – 462.5	462.5 – 465
88	C460DA3547-0313	C460DA3547-0314	C460DA3547-0315	C460DA3547-0316
82	C460DA3547-0309	C460DA3547-0310	C460DA3547-0311	C460DA3547-0312
76	C460DA3547-0305	C460DA3547-0306	C460DA3547-0307	C460DA3547-0308

Dominant Wavelength (nm)

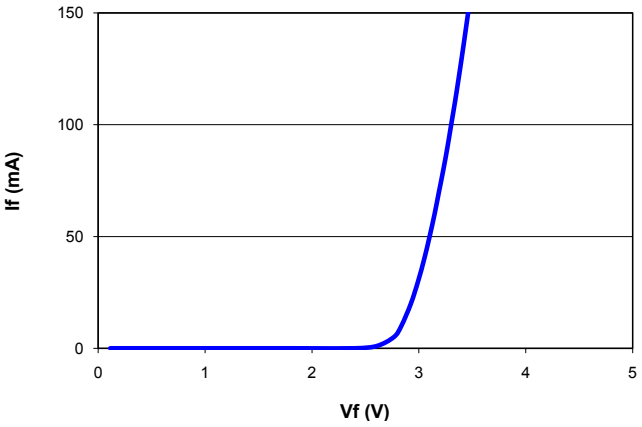
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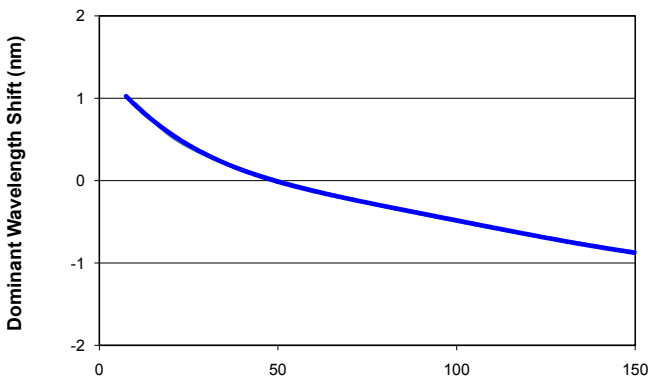
Characteristic Curves

These are representative measurements for the DA LED product. Actual curves will vary slightly for the various radiant flux and dominant wavelength bins.

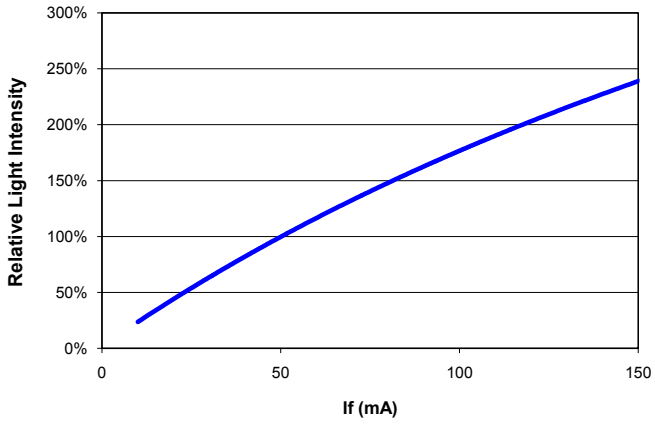
Forward Current vs. Forward Voltage



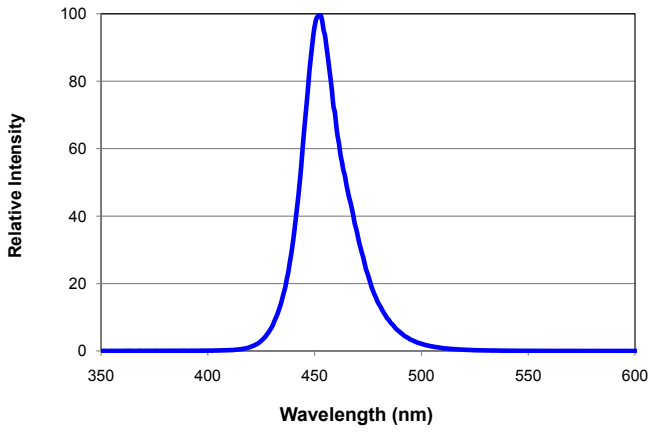
Wavelength Shift vs. Forward Current



Relative Intensity vs. Forward Current



Relative Intensity vs. Wavelength

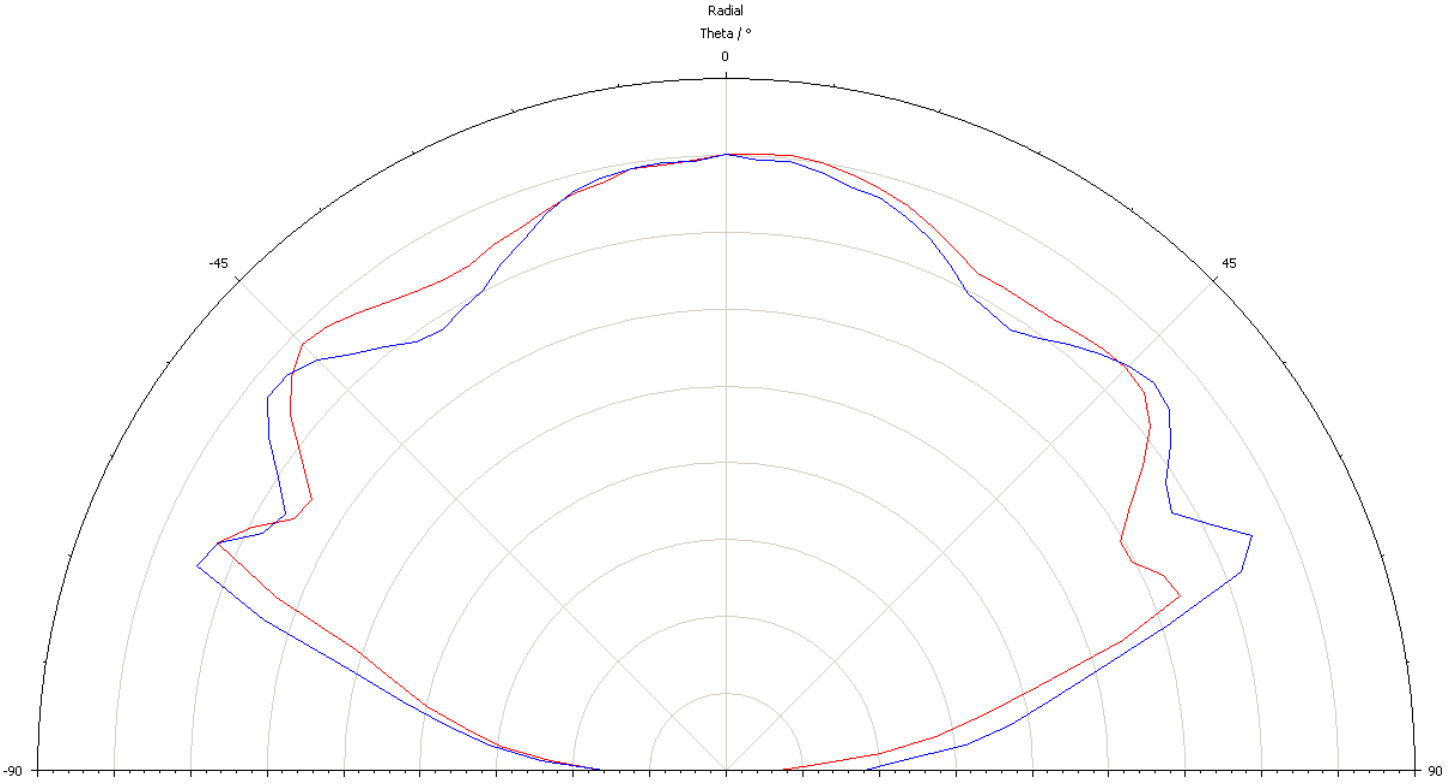


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Radiation Pattern

This is a representative radiation pattern for the DA LED product. Actual patterns will vary slightly for each chip.



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