Thyristors BT300 series

GENERAL DESCRIPTION

Glass passivated thyristors in a plastic envelope, intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

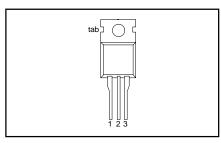
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V _{DRM} ,	Repetitive peak off-state voltages	500R 500	600R 600	800R 800	V
T(AV) T(RMS) TSM	Average on-state current RMS on-state current Non-repetitive peak on-state current	5 8 65	5 8 65	5 8 65	A A A

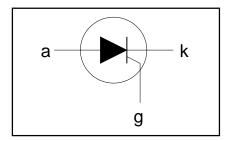
PINNING - TO220AB

PIN	DESCRIPTION		
1	cathode		
2	anode		
3	gate		
tab	anode		

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
V_{DRM} , V_{RRM}	Repetitive peak off-state voltages		-	-500R 500 ¹	-600R 600 ¹	-800R 800	V
$\begin{matrix} \mathbf{I}_{T(AV)} \\ \mathbf{I}_{T(RMS)} \\ \mathbf{I}_{TSM} \end{matrix}$	Average on-state current RMS on-state current Non-repetitive peak on-state current	half sine wave; $T_{mb} \le 111 ^{\circ}\text{C}$ all conduction angles half sine wave; $T_j = 25 ^{\circ}\text{C}$ prior to surge	-		5 8		A A
124		t = 10 ms t = 8.3 ms	- -		65 71		A A
l²t dl _⊤ /dt	I ² t for fusing Repetitive rate of rise of on-state current after triggering	t = 10 ms $I_{TM} = 10 \text{ A}; I_G = 50 \text{ mA};$ $dI_G/dt = 50 \text{ mA/}\mu\text{s}$	-		21 50		A²s A/μs
I _{GM} V _{GM} V _{RGM}	Peak gate current Peak gate voltage Peak reverse gate voltage		- - -		2 5 5 5		A V V W
P _{GM} P _{G(AV)} T _{stg} T _j	Peak gate power Average gate power Storage temperature Operating junction temperature	over any 20 ms period	- -40 -		0.5 150 125		ο̈ο̈́δ

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15 A/ μ s.

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Thermal resistance junction to mounting base		-	-	1.8	K/W
R _{th i-a}	Thermal resistance junction to ambient	in free air	-	60	-	K/W

STATIC CHARACTERISTICS

 $T_j = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$	-	2	15	mA
l I	Latching current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	10	40	mΑ
I _H	Holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	10	20	mΑ
Ϊ́Τ	On-state voltage	$I_{T} = 12 \text{ A}$	-	1.35	1.6	V
V _{GT}	Gate trigger voltage	$\dot{V}_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$	-	0.6	1.5	V
		$V_{\rm D} = V_{\rm DRM(max)}$; $I_{\rm T} = 0.1 \text{ A}$; $T_{\rm i} = 125 ^{\circ}\text{C}$	0.25	0.4	-	V
I_D, I_R	Off-state leakage current	$V_D = V_{DRM(max)}$; $V_R = V_{RRM(max)}$; $T_j = 125$ °C	-	0.1	0.5	mA

DYNAMIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV_D/dt t_{gt} t_{q}	Critical rate of rise of off-state voltage Gate controlled turn-on time Circuit commutated turn-off time	$\begin{split} V_{\text{DM}} = 67\% \ V_{\text{DRM(max)}}; \ T_j = 125 \ ^{\circ}\text{C}; \\ \text{exponential waveform.} \\ & \text{Gate open circuit} \\ & R_{\text{GK}} = 100 \ \Omega \\ I_{\text{TM}} = 10 \ A; \ V_{\text{D}} = V_{\text{DRM(max)}}; \ I_{\text{G}} = 0.1 \ A; \\ dI_{\text{G}}/dt = 5 \ A/\mu\text{S} \\ V_{\text{D}} = 67\% \ V_{\text{DRM(max)}}; \ T_j = 125 \ ^{\circ}\text{C}; \\ I_{\text{TM}} = 12 \ A; \ V_{\text{R}} = 25 \ V; \ dI_{\text{TM}}/dt = 30 \ A/\mu\text{S}; \\ dV_{\text{D}}/dt = 50 \ V/\mu\text{S}; \ R_{\text{GK}} = 100 \ \Omega \\ \end{split}$	50 200 -	100 1000 2 70	- - -	V/μs V/μs μs μs

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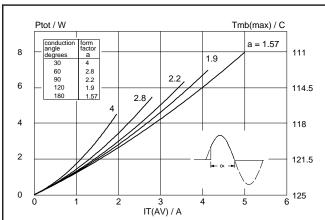


Fig.1. Maximum on-state dissipation, P_{tot} , versus average on-state current, $I_{T(AV)}$, where $a = form\ factor = I_{T(RMS)}/I_{T(AV)}$.

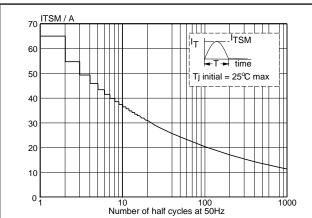


Fig.4. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, f = 50 Hz.

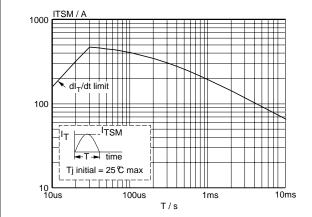


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \le 10$ ms.

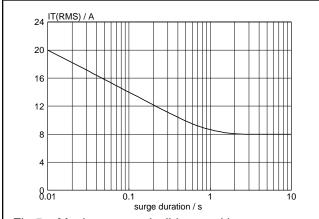


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50 Hz; $T_{mb} \le 111$ °C.

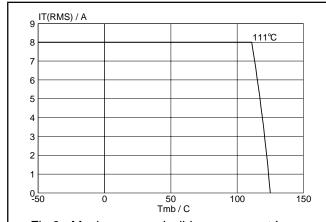
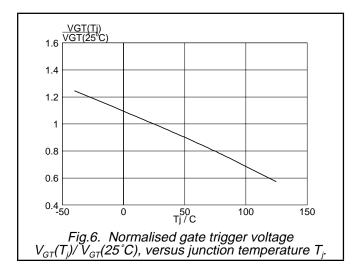
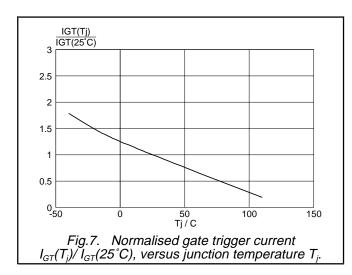
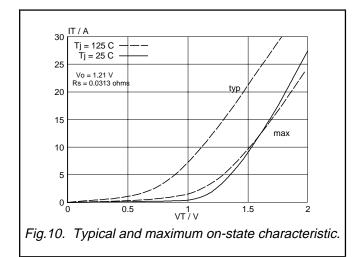


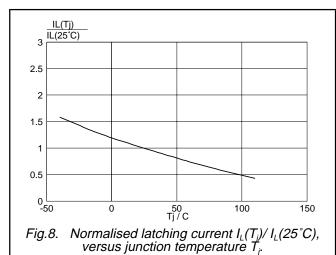
Fig.3. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

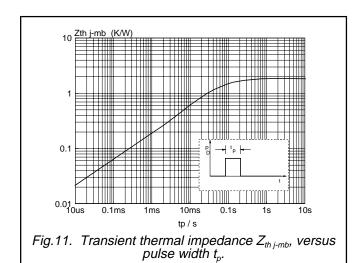


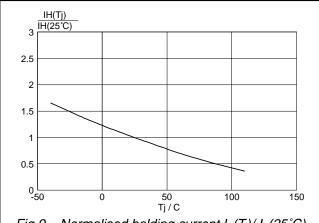
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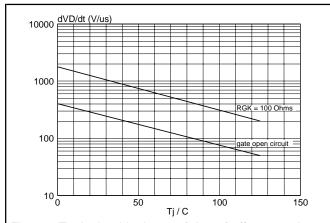
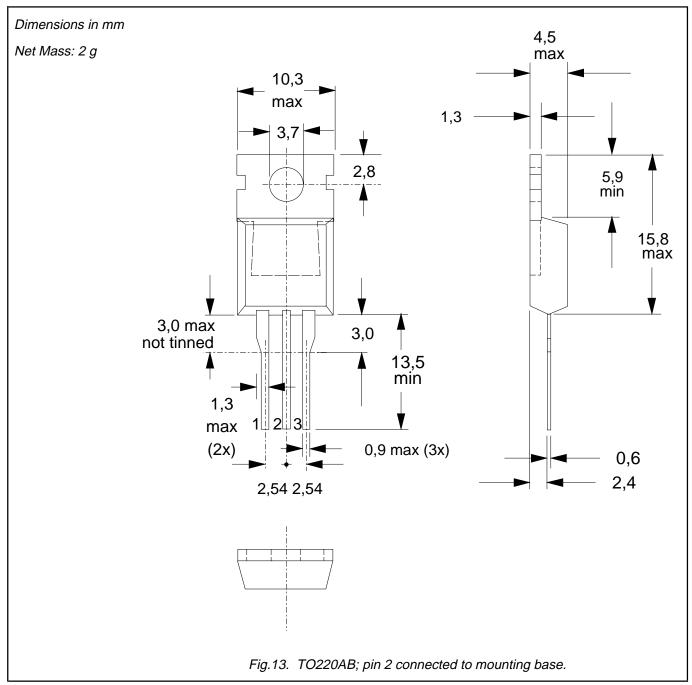


Fig.9. Normalised holding current $I_H(T_i)/I_H(25^{\circ}C)$, versus junction temperature T_j .

Fig.12. Typical, critical rate of rise of off-state voltage, dV_D/dt versus junction temperature T_j.

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MECHANICAL DATA



- Notes
 1. Refer to mounting instructions for TO220 envelopes.
 2. Epoxy meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status						
This data sheet contains target or goal specifications for product development.						
This data sheet contains preliminary data; supplementary data may be published later.						
This data sheet contains final product specifications.						
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Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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