

PJP730 / PJF730

400V N-Channel Enhancement Mode MOSFET

FEATURES

- 5.5A , 400V, $R_{DS(ON)}=0.95\Omega@V_{GS}=10V, I_D=3.0A$
- Low ON Resistance
- Fast Switching
- Low Gate Charge
- Fully Characterized Avalanche Voltage and Current
- Specially Designed for AC Adapter, Battery Charge and SMPS
- In compliance with EU RoHs 2002/95/EC Directives

MECHANICAL DATA

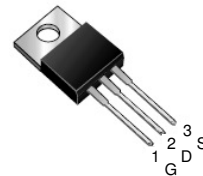
- Case: TO-220AB / ITO-220AB Molded Plastic
- Terminals : Solderable per MIL-STD-750,Method 2026

ORDERING INFORMATION

TYPE	MARKING	PACKAGE	PACKING
PJP730	P730	TO-220AB	50PCS/TUBE
PJF730	F730	ITO-220AB	50PCS/TUBE

TO-220AB / ITO-220AB

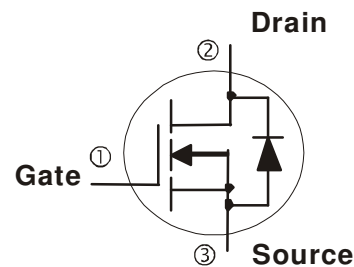
TO-220AB



ITO-220AB



INTERNAL SCHEMATIC DIAGRAM



Maximum RATINGS and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	Symbol	PJP730	PJF730	Units
Drain-Source Voltage	V_{DS}	400		V
Gate-Source Voltage	V_{GS}	± 30		V
Continuous Drain Current	I_D	5.5	5.5	A
Pulsed Drain Current ¹⁾	I_{DM}	22	22	A
Maximum Power Dissipation Derating Factor	P_D	87 0.7	50 0.4	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150		$^\circ\text{C}$
Avalanche Energy with Single Pulse $I_{AS}=5.7A, V_{DD}=50V, L=16.5mH$	E_{AS}	303		mJ
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	1.43	2.5	$^\circ\text{C/W}$
Junction-to Ambient Thermal Resistance	$R_{\theta JA}$	62.5	100	$^\circ\text{C/W}$

Note: 1. Maximum DC current limited by the package

PANJIT RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN,FUNCTIONS AND RELIABILITY WITHOUT NOTICE

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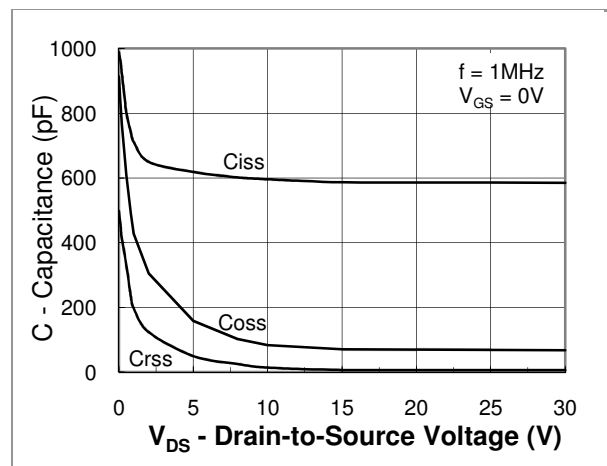
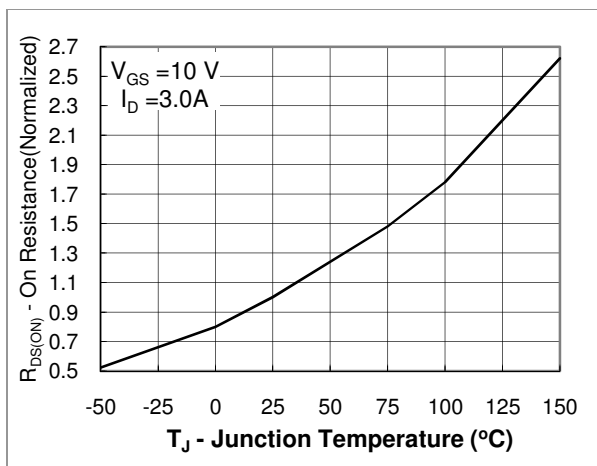
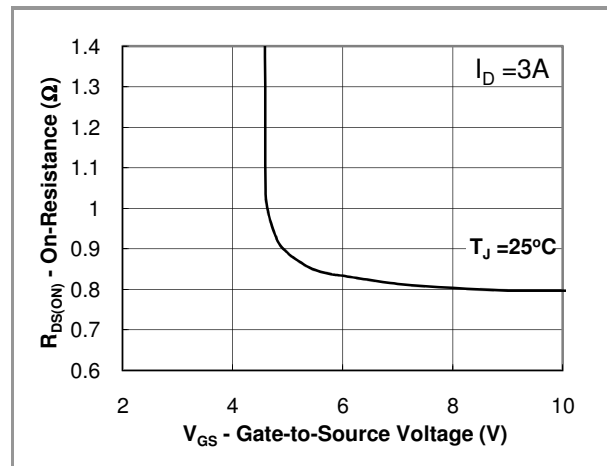
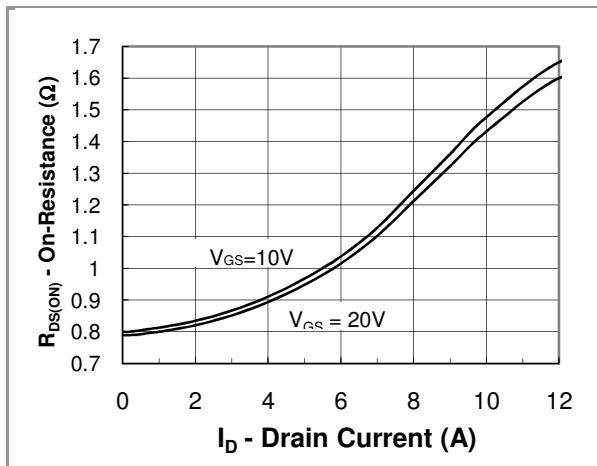
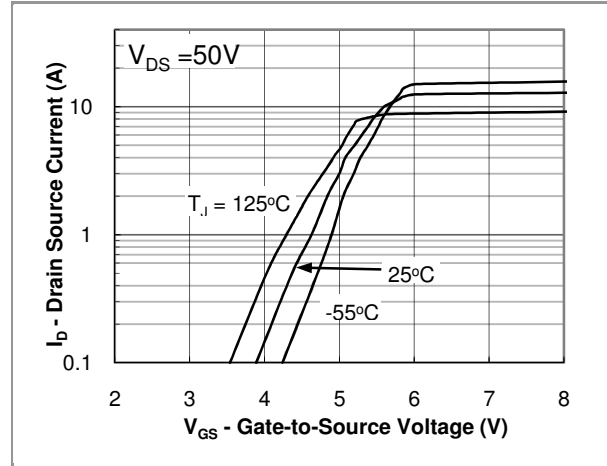
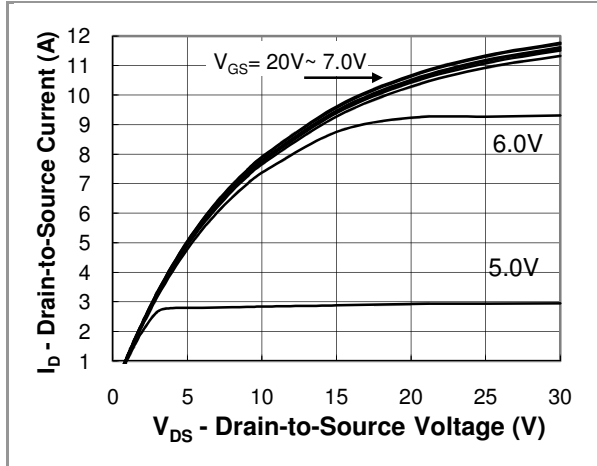
ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Units
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	400	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	-	4.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3A$	-	0.9	0.95	Ω
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=400V, V_{GS}=0V$	-	-	10	μA
Gate Body Leakage	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	± 100	nA
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=320V, I_D=5.5A$ $V_{GS}=10V$	-	16.8	-	nC
Gate-Source Charge	Q_{gs}		-	3.4	-	
Gate-Drain Charge	Q_{gd}		-	7.2	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=200V, I_D=5.5A$ $V_{GS}=10V, R_G=12\Omega$	-	10.7	16	ns
Turn-On Rise Time	t_r		-	15.6	18	
Turn-Off Delay Time	$t_{d(off)}$		-	24.4	36	
Turn-Off Fall Time	t_f		-	15.6	22	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V$ $f=1.0MHz$	-	560	680	pF
Output Capacitance	C_{oss}		-	69	85	
Reverse Transfer Capacitance	C_{rss}		-	6.2	7.8	
Source-Drain Diode						
Max. Diode Forward Current	I_S	-	-	-	5.5	A
Max.Pulsed Source Current	I_{SM}	-	-	-	22	A
Diode Forward Voltage	V_{SD}	$I_S=5.5A, V_{GS}=0V$	-	-	1.5	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_F=5.5A$ $di/dt=100A/us$	-	220	-	ns
Reverse Recovery Charge	Q_{rr}		-	2.0	-	μC

NOTE: Plus Test : Pluse Width ≤ 300us, Duty Cycle ≤ 2%.

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Typical Characteristics Curves ($T_a=25^\circ\text{C}$, unless otherwise noted)



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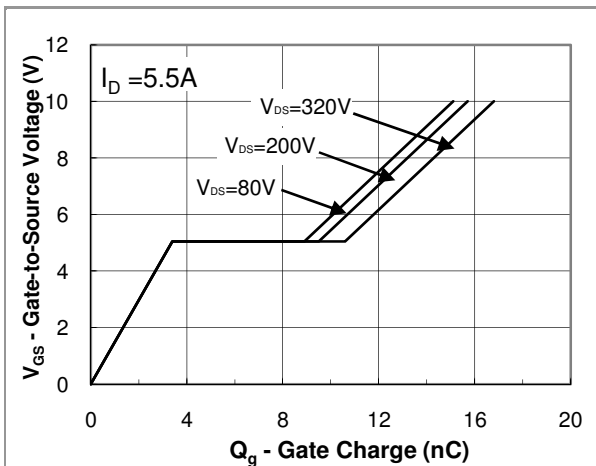


Fig. 7 Gate Charge Waveform

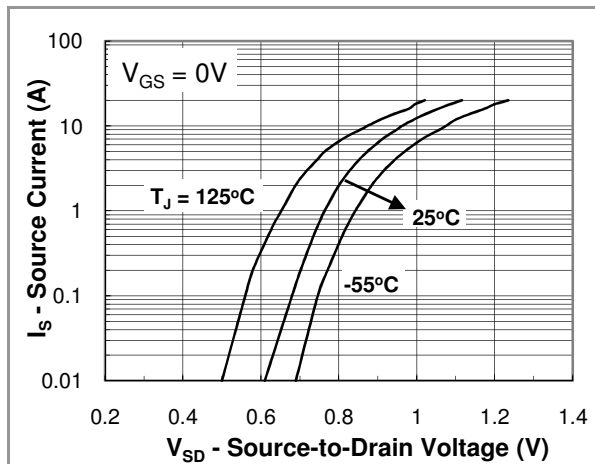


Fig. 8 Source-Drain Diode Forward Voltage

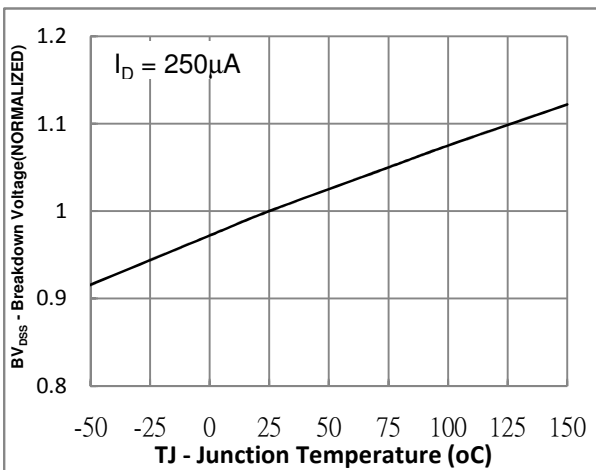


Fig. 9 Breakdown Voltage vs Junction Temperature



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LEGALSTATEMENT

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HALOGEN FREE PRODUCT DECLARATION

(Use green molding compound:ELER-8)

1. Pan Jit can produce halogen free product use molding compound for packing from Mar.2008 that contain Br<700 ppm,Cl<700ppm, Br+Cl<1000ppm,Sb₂O₃<100ppm.
2. If your company need halogen free product shall be note requirement green compound material on order for the halogen free product request.