RoHS Compliant

MagnaChip

MDE10N050

Single N-channel Trench MOSFET 100V, 120A, 5.0mΩ

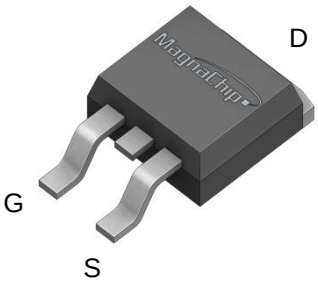
General Description

The MDE10N050 uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance, and excellent quality.

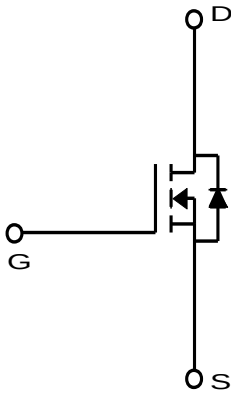
These devices can also be utilized in industrial applications such as Low Power Drives of E-bike (E-Vehicles), DC/DC converter, and general purpose applications.

Features

- V_{DS} = 100V
- I_D = 120A @V_{GS} = 10V
- Very low on-resistance $R_{DS(ON)}$ < 5.0 mΩ @V_{GS} = 10V
- 100% UIL Tested
- 100% Rg Tested
- 175 °C operating temperature



TO-263



Absolute Maximum Ratings (T_J = 25 °C)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (1)	T_C=25°C (Silicon Limited)	I_D	136	A
	T_C=25°C (Package Limited)		120	
	T_C=100°C (Silicon Limited)		96	
Pulsed Drain Current (2)		I_{DM}	480	
Power Dissipation	T_C=25°C	P_D	188	W
	T_C=100°C		93	
Single Pulse Avalanche Energy (3)		E_{AS}	288	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~175	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient (1)	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.8	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDE10N050RH	-55~175°C	TO-263	Reel	Halogen Free

Electrical Characteristics (T_J =25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D = 250μA, V_{GS} = 0V	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2.0	-	4.0	
Drain Cut-Off Current	I_{DSS}	V_{DS} = 100V, V_{GS} = 0V	-	-	1.0	μA
Gate Leakage Current	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V	-	-	±0.1	
Drain-Source ON Resistance	$R_{DS(ON)}$	V_{GS} = 10V, I_D = 50A	-	4.2	5.0	mΩ
Forward Transconductance	g_{fs}	V_{DS} = 10V, I_D = 50A	-	80	-	S
Dynamic Characteristics						
Total Gate Charge	Q_g	V_{DS} = 50V, I_D = 50A, V_{GS} = 10V	-	78	-	nC
Gate-Source Charge	Q_{gs}		-	24	-	
Gate-Drain Charge	Q_{gd}		-	17	-	
Input Capacitance	C_{iss}	V_{DS} = 40V, V_{GS} = 0V, f = 1.0MHz	-	5,429	-	pF
Reverse Transfer Capacitance	C_{rss}		-	47	-	
Output Capacitance	C_{oss}		-	1,108	-	
Turn-On Delay Time	$t_{d(on)}$	V_{GS} = 10V, V_{DS} = 50V, I_D = 50A , R_G = 3.0Ω	-	27	-	ns
Rise Time	t_r		-	14	-	
Turn-Off Delay Time	$t_{d(off)}$		-	63	-	
Fall Time	t_f		-	15	-	
Gate Resistance	Rg	f=1 MHz	-	2.5	-	Ω
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	I_S = 50A, V_{GS} = 0V	-	0.9	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 50A, dI/dt = 100A/μs	-	62		ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	124		nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7)
2. Pulse width limited by T_{Jmax}
3. E_{AS} is tested at starting Tj = 25℃, L = 1.0mH, I_{AS} = 24A, V_{GS} = 10V

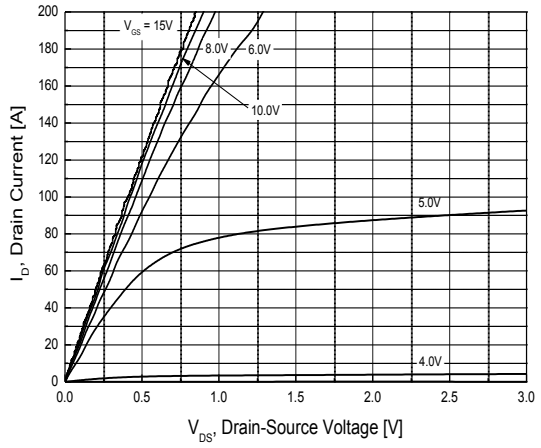


Fig.1 On-Region Characteristics

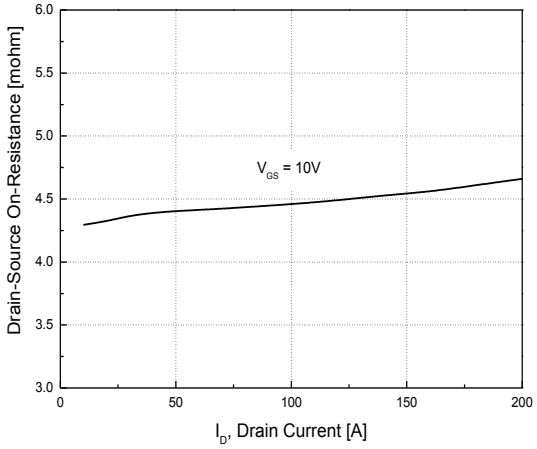


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

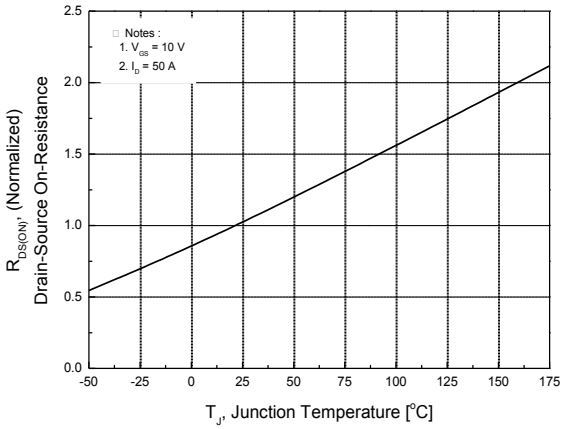


Fig.3 On-Resistance Variation with Temperature

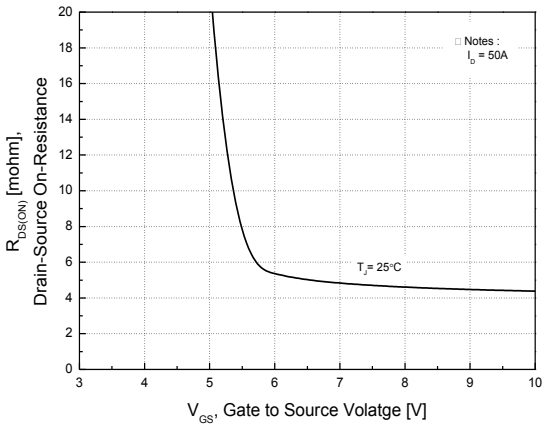


Fig.4 On-Resistance Variation with Gate to Source Voltage

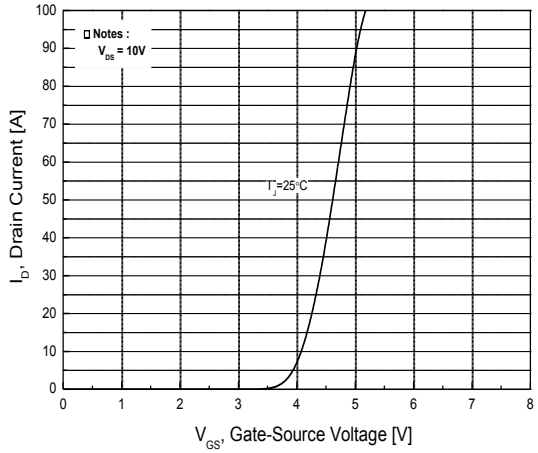


Fig.5 Transfer Characteristics

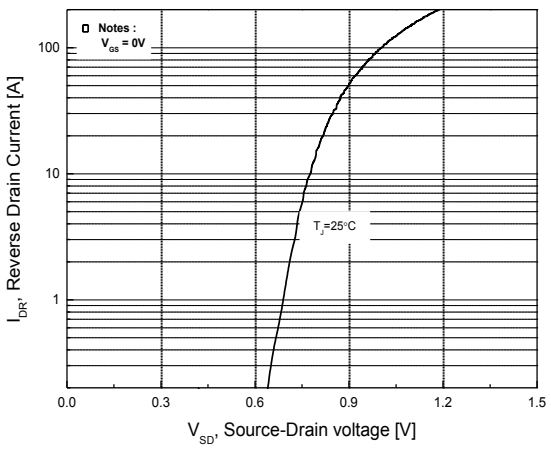


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

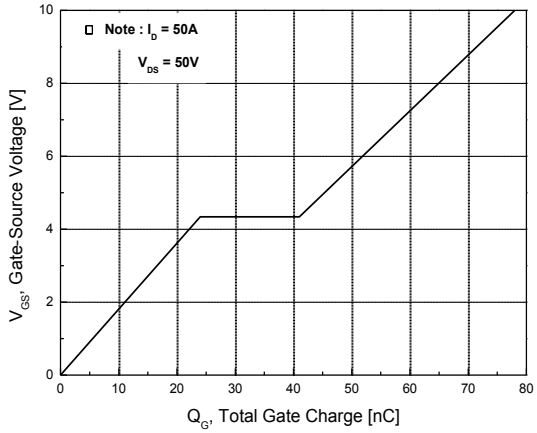


Fig.7 Gate Charge Characteristics

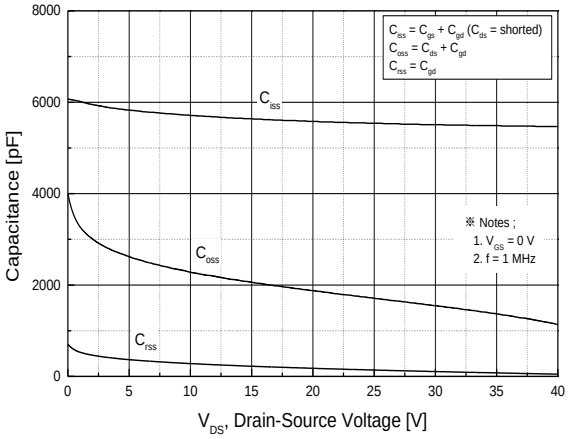


Fig.8 Capacitance Characteristics

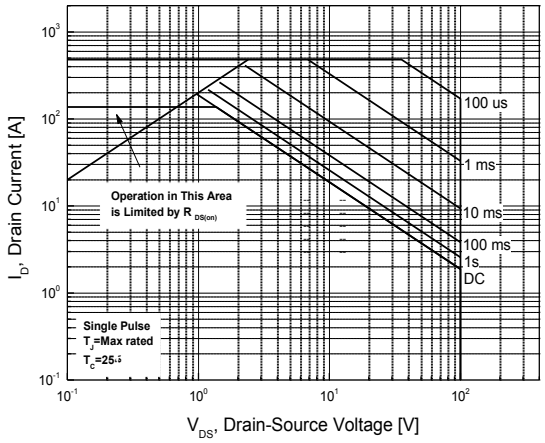


Fig.9 Maximum Safe Operating Area

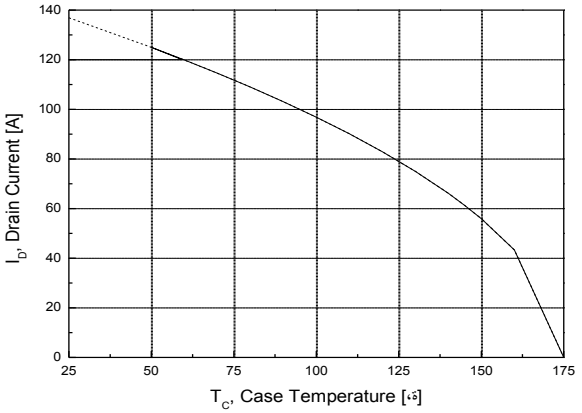


Fig.10 Maximum Drain Current vs. Case Temperature

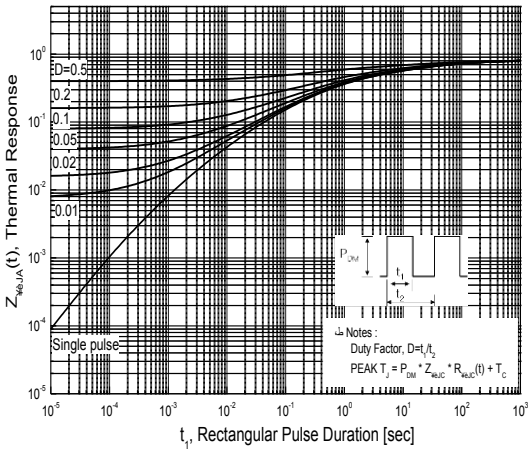
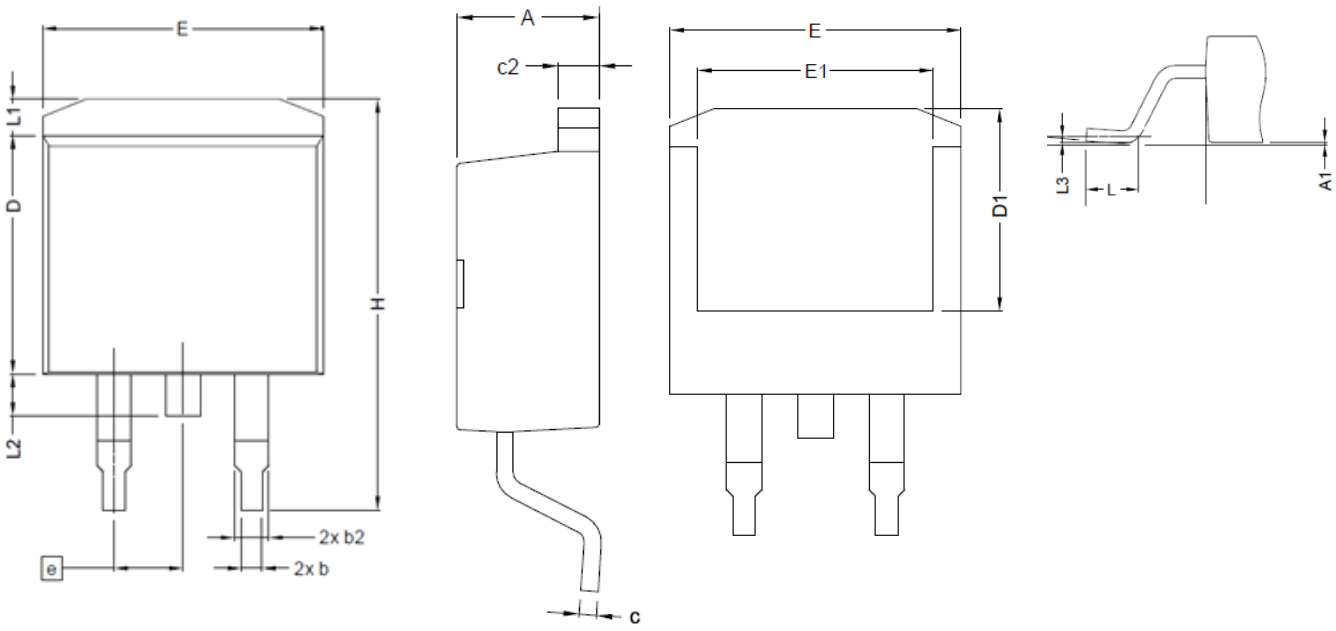


Fig.11 Transient Thermal Response Curve

Package Dimension

3 Leads, TO-263

Dimensions are in millimeters unless otherwise specified



Symbol	Millimeters(mm)	
	Min	Max
A	4.064	4.826
A1	-	0.254
b	0.508	0.99
b2	1.140	1.778
c	0.310	0.736
c2	1.140	1.650
D	8.382	9.652
D1	6.6	-
E	9.652	10.668
E1	6.223	-
e	BSC 2.54	
H	14.605	15.875
L	1.778	2.794
L1	-	1.676
L2	-	1.778
L3	BSC 0.254	

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