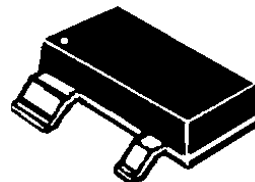




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USB50403C thru USB50424C

TVSarray™ Series



DESCRIPTION (500 watt)

This 4 pin 1 line **bidirectional ULTRA LOW CAPACITANCE** array is designed for use in applications requiring board level protection from voltage transients caused by electrostatic discharge (ESD) as defined by IEC 1000-4-2, electrical fast transients (EFT) per IEC 1000-4-4 and effects of secondary lightning.

These arrays are used to protect 1 discrete line utilizing pins (1,4 signal and 2,3 ground). The SOT-143 product provides board level protection from static electricity and other induced voltage surges that can damage sensitive circuitry.

These TRANSIENT VOLTAGE SUPPRESSOR (TVS) Diode Arrays protect 3.3 Volt components such as DRAM's, SRAM's, CMOS, HCMOS, HSIC, and low voltage interfaces up to 24Volts.

FEATURES

- Protects 3.3 up through 24V Components
- Protects 1 line bidirectional
- Provides electrically isolated protection
- SOT-143 Packaging
- **ULTRA LOW CAPACITANCE 3 pF**

MECHANICAL

- Molded SOT-143 Surface Mount
- Weight: .035 grams (approximate)
- Body Marked with device marking code
- Pin #1 defined by DOT on top of package

MAXIMUM RATINGS

- Operating Temperatures: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Peak Pulse Power: 500 Watts (8/20 µsec, Figure 1)
- Pulse Repetition Rate: <.01%

PACKAGING

- Tape & Reel EIA Standard 481-1-A
- 7 inch reel 3,000 pieces

ELECTRICAL CHARACTERISTICS @ 25°C Unless otherwise specified

PART NUMBER	DEVICE MARKING	STAND OFF VOLTAGE V_{WM}	BREAKDOWN VOLTAGE V_{BR} @1 mA	CLAMPING VOLTAGE V_C @ 1 Amp (FIGURE 2)	CLAMPING VOLTAGE V_C @ 5 Amp (FIGURE 2)	STAND OFF CURRENT I_b @ V_{WM}	CAPACITANCE (f=1 MHz) @0V C	TEMPERATURE COEFFICIENT OF V_{BR} α_{VBR}
		VOLTS	VOLTS	VOLTS	VOLTS	µA	pF	mV/°C
		MAX	MIN	MAX	MAX	MAX	MAX	MAX
USB50403C	53	3.3	4	8	11	200	3	-5
USB50405C	55	5.0	6.0	10.8	13	40	3	1
USB50412C	512	12.0	13.3	19	26	1	3	18
USB50415C	515	15.0	16.7	25	32	1	3	11
USB50424C	524	24.0	26.7	44	57	1	3	28

NOTE: Transient Voltage Suppression (TVS) product is normally selected based on its stand off Voltage V_{WM} . Product selected voltage should be equal to or greater than the continuous peak operating voltage of the circuit to be protected.

WAVE FORMS

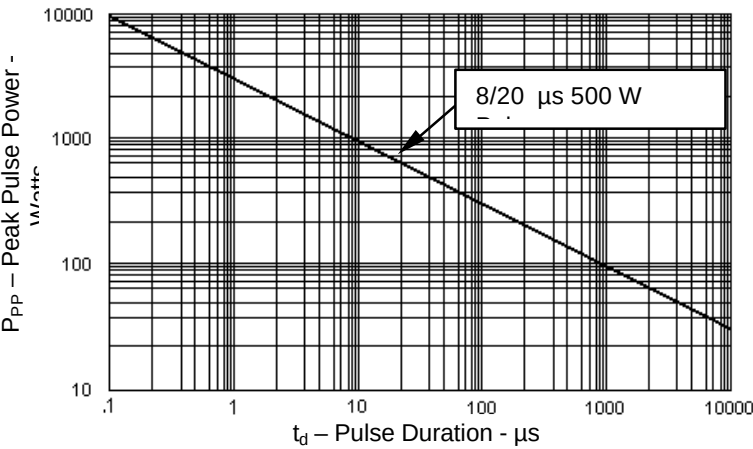


FIGURE 1
Peak Pulse Power Vs Pulse Time

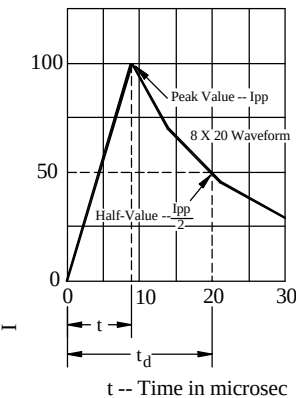


FIGURE 2
Pulse Wave Form

