

SANYO

No.1403A

2SK404

N-Channel Junction Silicon FET

HF Amp, AF Amp Applications

Features

- Large $|y_{fs}|$
- Low noise
- Small C_{rss}

Absolute Maximum Ratings/ $T_a = 25^\circ\text{C}$

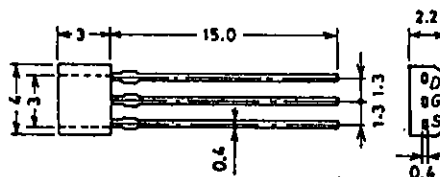
			unit
Drain to source voltage	V_{DSS}	20	V
Gate to drain voltage	V_{GSS}	-20	V
Gate current	I_G	10	mA
Drain current	I_D	20	mA
Allowable power dissipation	P_D	200	mW
Junction temperature	T_j	125	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +125	$^\circ\text{C}$

Electrical Characteristics/ $T_a = 25^\circ\text{C}$

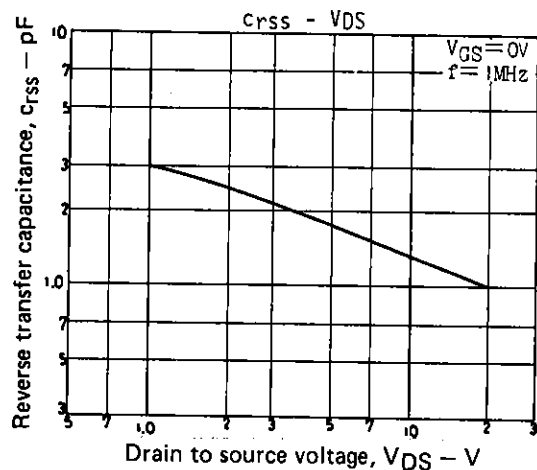
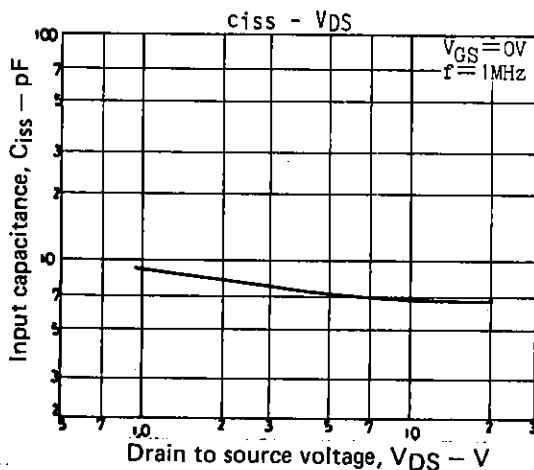
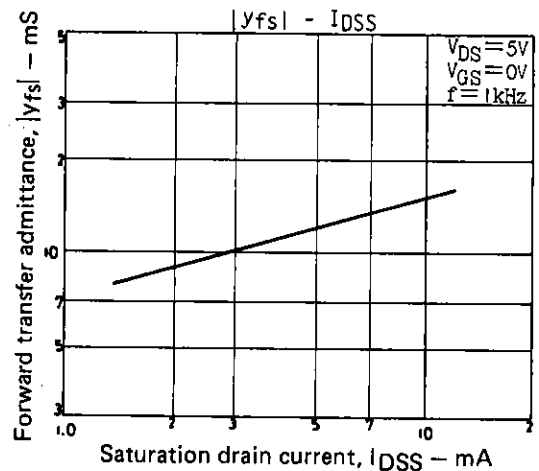
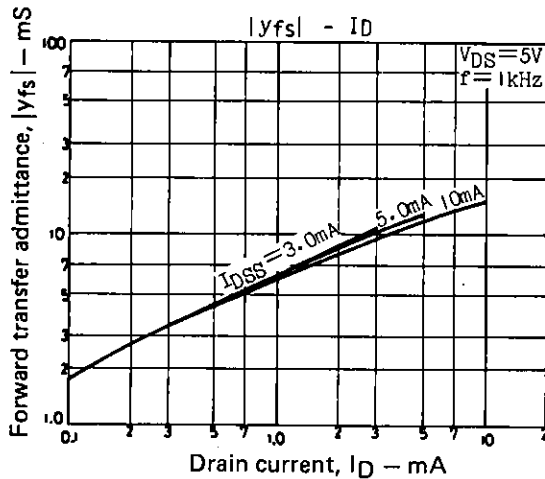
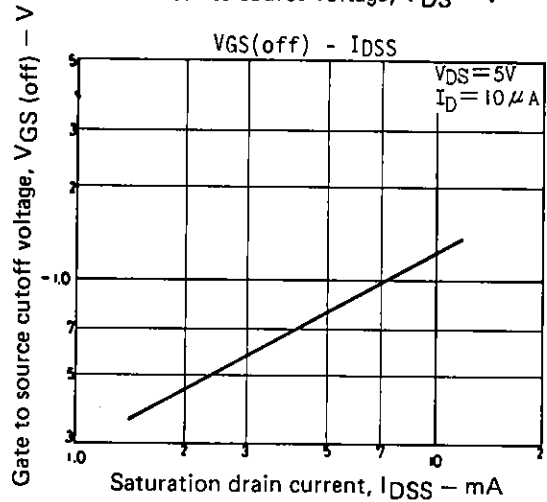
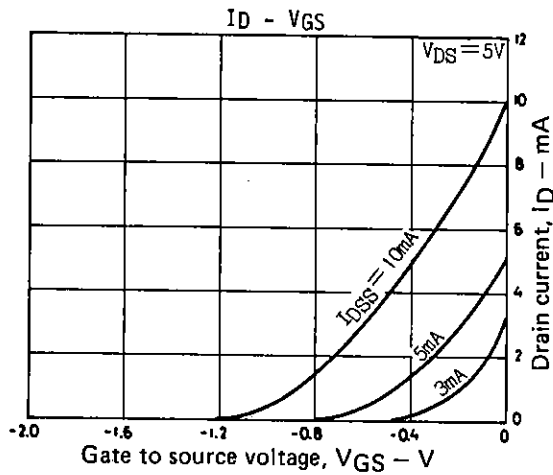
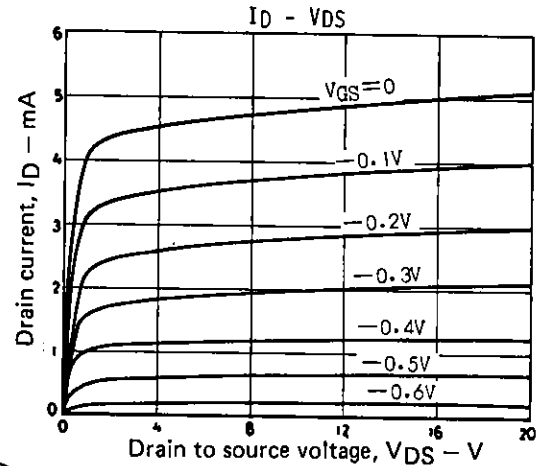
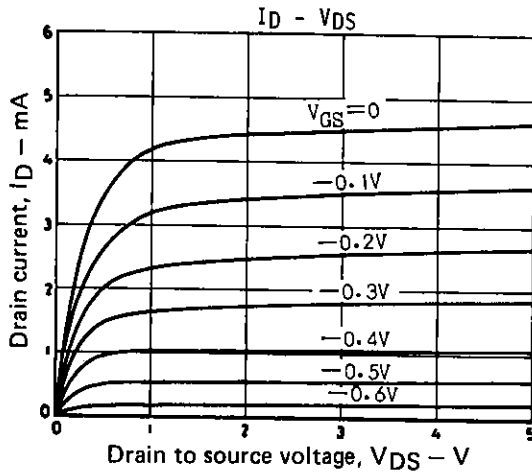
			min	typ	max	unit
Gate to drain breakdown voltage	$V_{(BR) GSS}$	$I_G = -10\mu\text{A}$	-20			V
Common source gate cutoff current	I_{GSS}	$V_{GS} = -10\text{V}, V_{DS} = 0\text{V}$			-1.0	nA
Gate to source cutoff voltage	$V_{GS}(\text{off})$	$V_{DS} = 5\text{V}, I_D = 10\mu\text{A}$		-0.8	-2.0	V
Drain current	I_{DSS}	$V_{DS} = 5\text{V}, V_{GS} = 0\text{V}$	1.2*		12.0*	mA
Forward transfer admittance	$ y_{fs} $	$V_{DS} = 5\text{V}, V_{GS} = 0\text{V}, f = 1\text{kHz}$	5.0	10		mS
Input capacitance	C_{iss}	$V_{DS} = 5\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		7.0		pF
Reverse transfer capacitance	C_{rss}	$V_{DS} = 5\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		1.8		pF
Noise figure	NF	$V_{DS} = 5\text{V}, I_D = 1\text{mA},$ $R_g = 1\text{k}\Omega, f = 1\text{kHz}$		1.5		dB

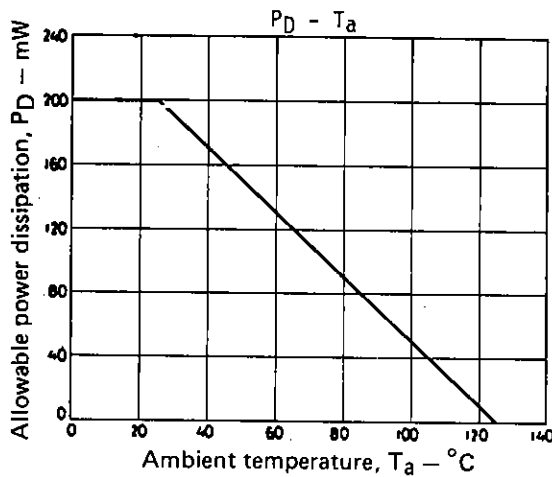
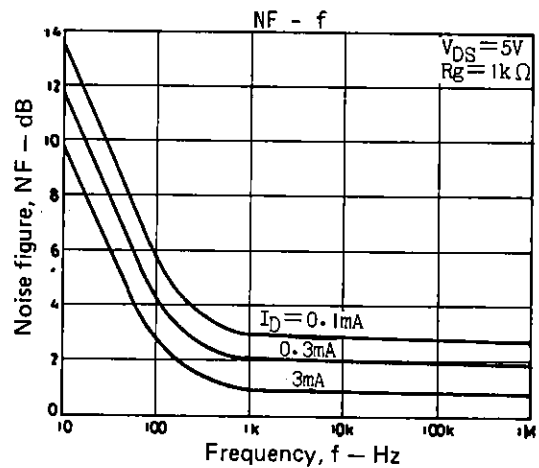
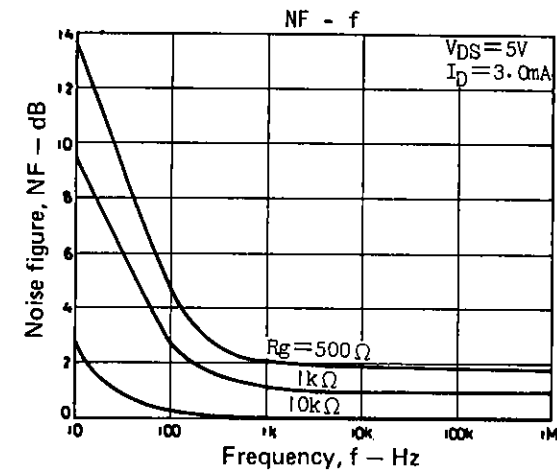
*: The 2SK404 is classified by $V_{DS} = 5\text{V}$ as follows (unit: mA):

1.2	D	3.0	2.5	E	6.0	5.0	F	12.0
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Package Dimensions 2034
(unit: mm)D: Drain
G: Gate
S: Source

SANYO: SPA





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