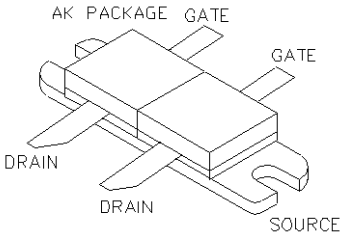
polyfet rf devices

LK702

General Description

Silicon VDMOS and LDMOS transistors designed specifically for broadband RF applications. Suitable for Militry Radios, Cellular and Paging Amplifier Base Stations, Broadcast FM/AM, MRI, Laser Driver and others.

"Polyfet"™ process features low feedback and output capacitances resulting in high F_t transistors with high input impedance and high efficiency.



SILICON GATE ENHANCEMENT MODE

RF POWER LDMOS TRANSISTOR

100.0 Watts Push - Pull

Package Style AK

HIGH EFFICIENCY, LINEAR
HIGH GAIN, LOW NOISE

ABSOLUTE MAXIMUM RATINGS (T = 25 °C)

Total Device Dissipation	Junction to Case Thermal Resistance	Maximum Junction Temperature	Storage Temperature	DC Drain Current	Drain to Gate Voltage	Drain to Source Voltage	Gate to Source Voltage
290 Watts	0.60 °C/W	200 °C	-65 °C to 150 °C	17.5 A	70V	70V	20 V

RF CHARACTERISTICS (100.0 WATTS OUTPUT)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Gps	Common Source Power Gain	14			dB	Idq = 0.80 A, Vds = 28.0 V, F = 500 MHz
η	Drain Efficiency		55		%	Idq = 0.80 A, Vds = 28.0 V, F = 500 MHz
VSWR	Load Mismatch Tolerance			20:1	Relative	Idq = 0.80 A, Vds = 28.0 V, F = 500 MHz

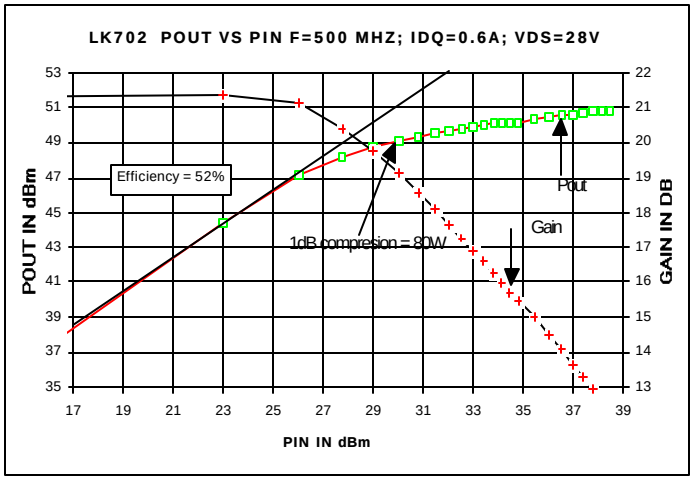
ELECTRICAL CHARACTERISTICS (EACH SIDE)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Bvdss	Drain Breakdown Voltage	65			V	Ids = 0.40 mA, Vgs = 0V
Idss	Zero Bias Drain Current			4.0	mA	Vds = 28.0 V, Vgs = 0V
Igss	Gate Leakage Current			1	uA	Vds = 0V Vgs = 30V
Vgs	Gate Bias for Drain Current	1		7	V	Ids = 0.40 A, Vgs = Vds
gM	Forward Transconductance		3.2		Mho	Vds = 10V, Vgs = 5V
Rdson	Saturation Resistance		0.45		Ohm	Vgs = 20V, Ids = 2.00 A
Idsat	Saturation Current		20.00		Amp	Vgs = 20V, Vds = 10V
Ciss	Common Source Input Capacitance		120.0		pF	Vds = 28.0 Vgs = 0V, F = 1 MHz
Crss	Common Source Feedback Capacitance		3.2		pF	Vds = 28.0 Vgs = 0V, F = 1 MHz
Coss	Common Source Output Capacitance		60.0		pF	Vds = 28.0 Vgs = 0V, F = 1 MHz

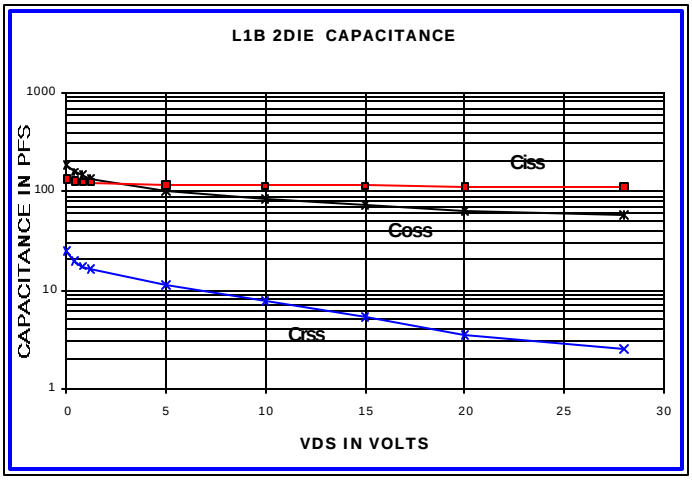
1110 Avenida Acaso, Camarillo, Ca 93012 Tel:(805) 484-4210 FAX: (805) 484-3393 EMAIL:Sales@polyfet.com URL:www.polyfet.com

LK702

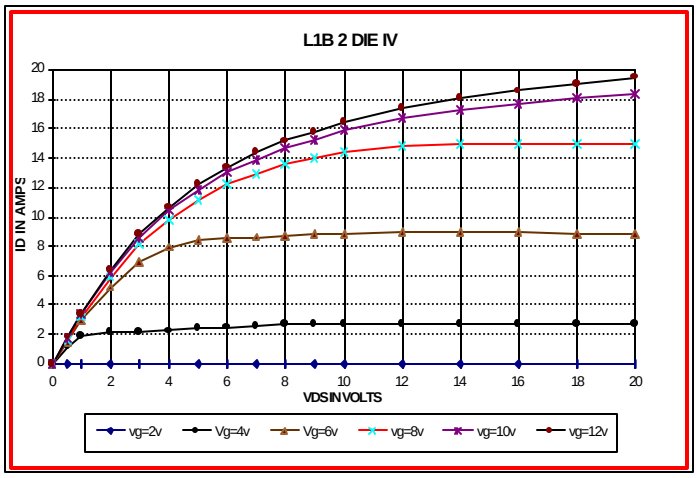
POUT VS PIN GRAPH



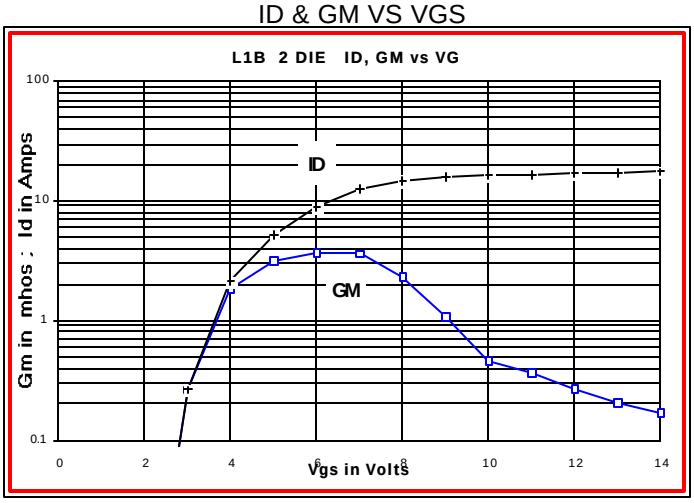
CAPACITANCE VS VOLTAGE



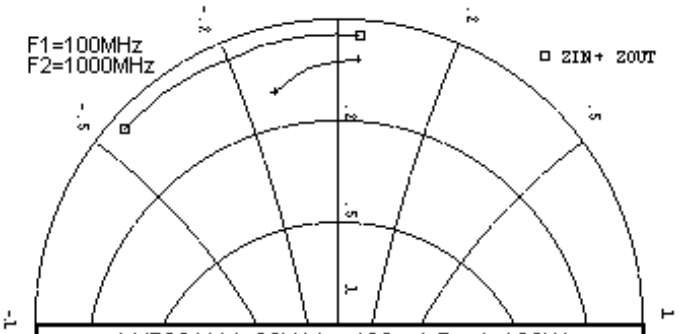
IV CURVE



ID & GM VS VGS

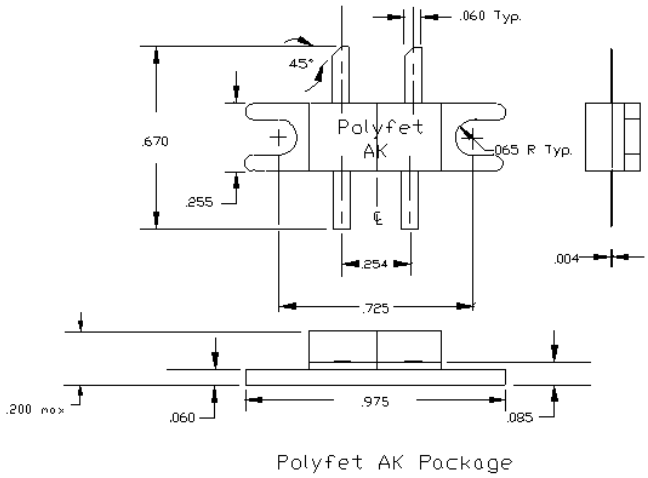


Zin Zout



LK702 Vdd=28V Idq=400mA Pout=100W					
Freq(MHz)	100	250	500	750	1000
Zin	1.4	1.4	1.2	1.2	1.2
	-j22	-j8	-j2.3	j0	j2
Zout	6	4.2	3.8	3.6	3.4
	-j6.6	-j3.2	-j0.8	j0.5	j2

PACKAGE DIMENSIONS IN INCHES



Polyfet AK Package

Tolerance .XX +/-0.01 .XXX +/-.005 inches

POLYFET RF DEVICES REVISION 04/30/2001

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