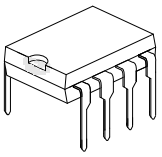
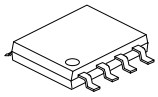
UCSR3651S

CMOS IC

HIGH PRECISION CC/CV PRIMARY-SIDE PWM POWER SWITCH



DIP-7



SOP-8

■ DESCRIPTION

The UTC **UCSR3651S** is a primary control switch mode charger and adapter applications. The controlled variable is transferred by an auxiliary winding from the secondary to the primary side. The device integrates PWM controller to enhance the performance of discontinuous conduction mode (DCM) flyback converters.

The UTC **UCSR3651S** operates in primary-side sensing and regulation. Opto-coupler and TL431 could be eliminated. It also provides off-time modulation to linearly decrease PWM frequency under light-load conditions so that low standby power can be achieved.

The UTC **UCSR3651S** achieves high precision CV/CC regulation and high power efficiency. It offers comprehensive protection coverage with auto-recovery features including Cycle-by-cycle current limiting, V_{DD} over voltage protection, V_{DD} clamp, OTP, leading edge blanking, V_{DD} under voltage lockout, etc.

■ FEATURES

* High Voltage startup circuit
* Built-in 600V Power MOSFET
* ±5% constant voltage regulation at universal AC input
* High precision constant current regulation at universal AC input
* Primary side control without TL431 and opto-coupler
* Programmable CV and CC regulation
* Programmable cable compensation in CV mode
* Flyback topology in DCM operation
* Frequency hopping to reduce system EMI
* Built-in leading edge blanking
* Built-in primary winding inductance compensation
* Power on soft start
* Gate Output Maximum Voltage Clamped at 15V
* Cycle-by-cycle current limiting
* Over voltage protection
* V_{DD} under-voltage lockout

■ ORDERING INFORMATION

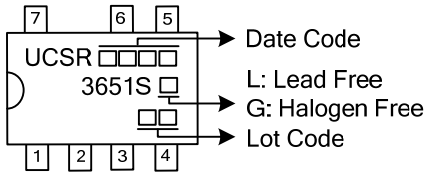
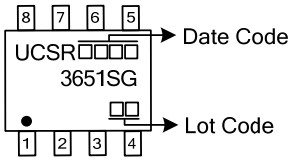
Ordering Number		Package	Packing
Lead Free	Halogen Free		
UCSR3651SL-D07-T	UCSR3651SG-D07-T	DIP-7	Tube
-	UCSR3651SG-S08-T	SOP-8	Tape Reel

UCSR3651SL-D07-T
(1)Packing Type
(2)Package Type
(3)Green Package

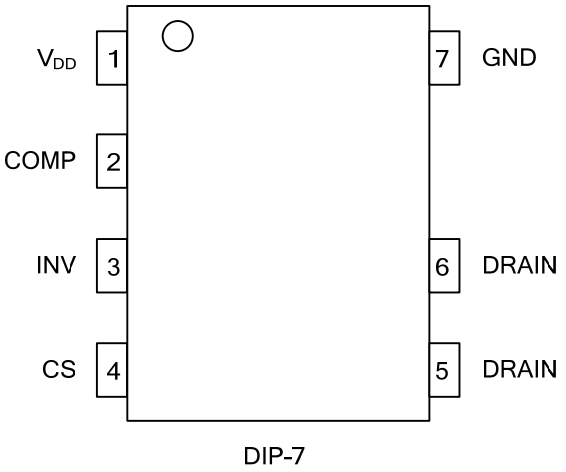
(1) T: Tube, R: Tape Reel
(2) D07: DIP-7, S08: SOP-8
(3) L: Lead Free, G: Halogen Free and Lead Free

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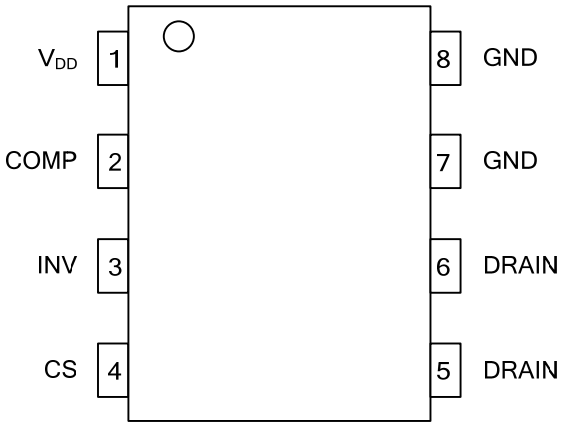
■ MARKING

DIP-7	SOP-8
	

■ PIN CONFIGURATION



DIP-7

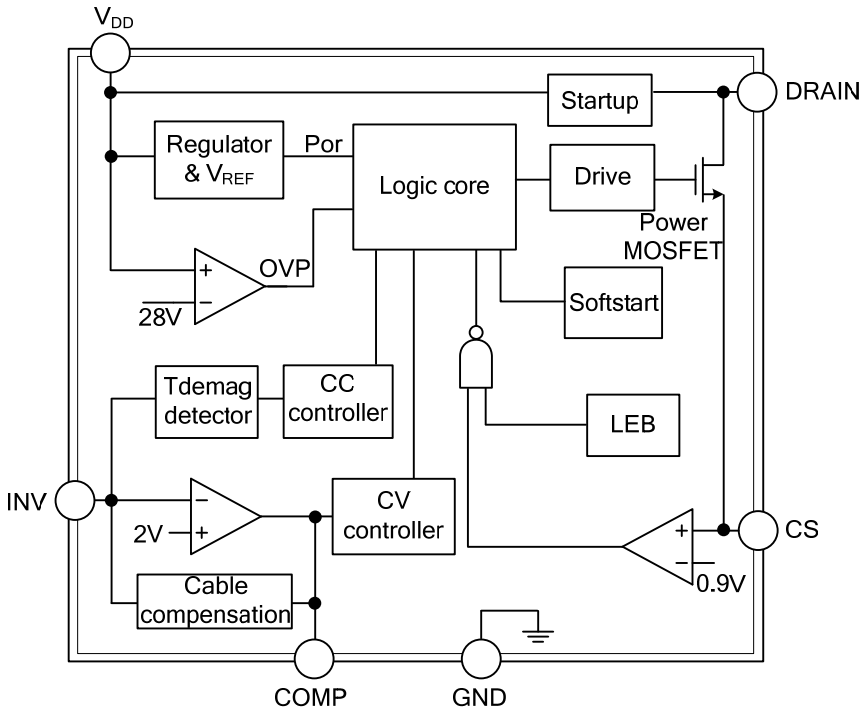


SOP-8

■ PIN DESCRIPTION

PIN NO.		PIN NAME	DESCRIPTION
DIP-7	SOP-8		
1	1	V_{DD}	Power supply
2	2	COMP	Loop compensation
3	3	INV	The voltage feedback from the auxiliary winding
4	4	CS	Current sense input
5, 6	5, 6	DRAIN	Power MOSFET Drain Pin
7	7, 8	GND	Ground

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Drain Voltage (Off State)	B_{VDSS}	-0.3~Bvdss	V
V_{DD} Zener Clamp Continuous Current	I_{DD}	10	mA
COMP Voltage	V_{COMP}	-0.3~7	V
CS Input Voltage	V_{CS}	-0.3~7	V
INV Input Voltage	V_{INV}	-0.3~7	V
Operating Junction Temperature	T_J	-20~150	°C
Storage Temperature	T_{STG}	-55~150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	DIP-7	θ_{JA}	75	°C/W
	SOP-8		90	°C/W

■ OUTPUT POWER TABLE

PARAMETER		RATINGS	UNIT
Adapter (Note 1)	230VAC±15%	10	W
	85-264VAC	8	W

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, V_{DD}=15V, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
STARTUP SECTION						
Startup Voltage	V_{ST}	$V_{DD}<V_{TH(ON)}$	25			V
Startup Current	I_{ST}	$V_{DD}<V_{TH(ON)}$		700		μA
SUPPLY SECTION						
Turn-on Threshold Voltage	$V_{TH(ON)}$		13.5	14.5	16	V
Turn-off Threshold Voltage	$V_{TH(OFF)}$		8.2	9	10.5	V
V_{DD} OVP Protection	V_{DD_OVP}			28		V
Operating Current	I_{DD-OP}			1.6	2	mA
Green-Mode Operating Current	$I_{DD-GREEN}$			1.3		mA
OSCILLATOR SECTION						
Maximum Frequency	f_{OSC}		55	65	75	kHz
Minimum Frequency at CCM	$f_{OSC-CM-MIN}$			17		kHz
Minimum Frequency at No-load	$f_{OSC-N-MIN}$			300		Hz
Frequency Hopping Range	$\frac{\Delta f}{f_{req}}$			±5.0		%
Frequency Hopping Period	t_{FHR}			3		ms
INV SECTION						
Reference Voltage for EA	V_{VR}		1.97	2.00	2.03	V
Reference Voltage for CCM	V_{VR_CCM}			0.5		V
Max. Cable Compensation Current	I_{CABLE_MAX}			37		μA
CURRENT SENSE SECTION						
Current Sense Threshold Voltage	V_{CS}		870	900	930	mV
OCP Propagation Delay	t_{D_OC}			90	200	nS
Leading Edge Blank Time	t_{LEB}			900		nS
Soft Start Time	t_{SS}			10		mS
POWER MOSFET SECTION						
MOSFET DS Breakdown Voltage	B_{VDSS}		600			V
On Resistance	$R_{DS(ON)}$			3		Ω
OTP SECTION						
Threshold Temperature of OTP (Note 2)	T_{OTP}			+140		°C

Notes: 1. Maximum practical continuous power in an Adapter design with sufficient drain pattern as a heat sink, at 50°C ambient.
2. When the over-temperature protection is activated, the power system enters auto restart mode and output is disabled.

■ OPERATION DESCRIPTION

The UTC **UCSR3651S** is a primary control switch mode charger and adapter applications. It operates in primary-side sensing and regulation. Opto-coupler and TL431 could be eliminated. Proprietary built-in CV and CC control can achieve high precision CV/CC performance. The device integrates PWM controller to enhance the performance of discontinuous conduction mode (DCM) flyback converters.

Startup control

The V_{DD} pin of UTC **UCSR3651S** is connected to the line input through a resistor. A large value startup resistor can be used to minimize the power loss in application because the start current of UTC **UCSR3651S** is very low. When the V_{DD} voltage reaches $V_{TH(ON)}$, the internal startup circuit is disabled and the IC turns on.

Operating Current

The Operating current of UTC **UCSR3651S** is as low as 1.6mA. Good efficiency and very low standby power can be achieved.

Constant Voltage Operation

The output voltage is defined by the transmission ratio between the secondary and auxiliary winding. The UTC **UCSR3651S** captures the auxiliary winding feedback voltage at INV pin and operates in constant-voltage (CV) mode to regulate the output voltage. The auxiliary voltage reflects the output voltage is given by:

$$V_{AUX} = \frac{N_A}{N_S} \times (V_O + \Delta V) \quad (1)$$

Where △V indicates the drop voltage of the output diode.

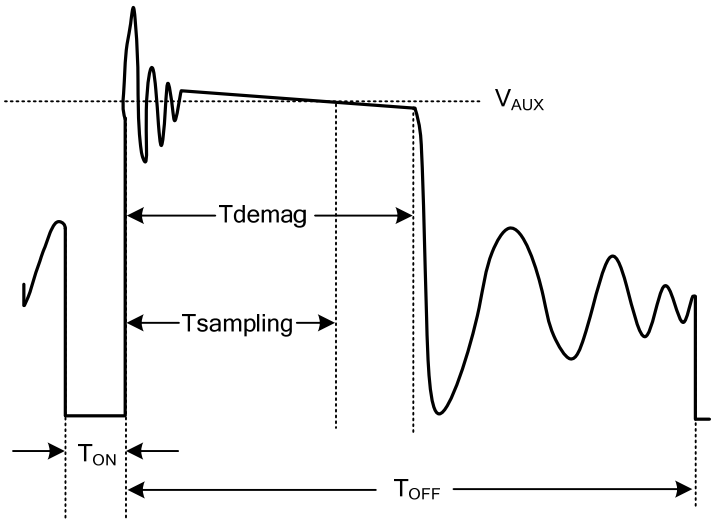


Figure 2. Auxiliary voltage waveform

Via a resistor divider connected between the auxiliary winding and INV, the V_{AUX} is sampled at the Tsampling end and it is hold until the next sampling. The sampled voltage is compared with 2.0V reference voltage and the error is amplified. The error amplifier output reflects the load condition and controls the T_{OFF} time and the Ipk to regulate the output voltage, thus constant output voltage can be achieved.

■ OPERATION DESCRIPTION (Cont.)

Constant Current Operation

When the sampled voltage is below 2.0V reference voltage and the error amplifier output reaches its maximum, thus UTC **UCSR3651S** operates in constant-current (CC) mode. The CC point and maximum output power can be externally adjusted by external current sense resistor Rcs. The larger Rcs, the smaller CC point is, and the smaller output power becomes.

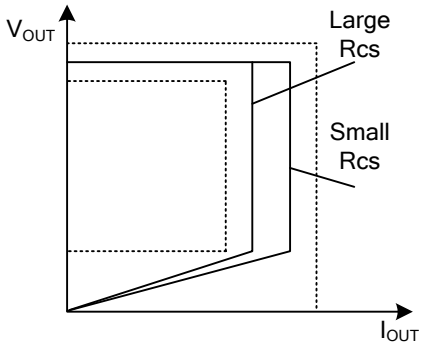


Figure 3. Adjustable output power by changing R_{CS}

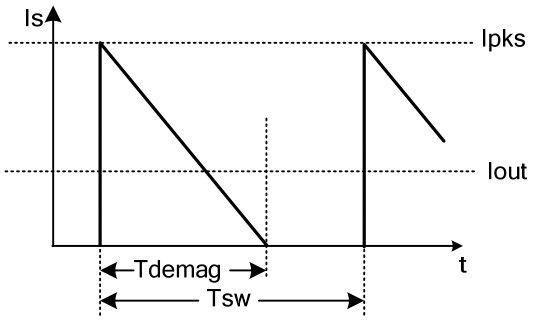


Figure 4. Secondary current waveform

In CC operation, the CC loop control function of UTC **UCSR3651S** will keep a fixed proportion between secondary inductance de-magnetization time (Tdemag) and switching cycle time (Tsw). The fixed proportion is

$$\frac{Tdemag}{Tsw} = \frac{1}{2} \qquad (2)$$

Thus the output current is given by:

$$I_{OUT} = \frac{1}{2} \times \frac{N_P}{N_S} \times I_{PK} \times \frac{Tdemag}{Tsw} = \frac{1}{4} \times \frac{N_P}{N_S} \times I_{PK} \qquad (3)$$

Programmable Cable Drop Compensation

UTC **UCSR3651S** has a built-in cable voltage drop compensation to achieve good load regulation. An offset voltage is generated at INV pin by an internal current flowing into the resistor divider. The current is inversely proportional to the voltage of COMP pin. As a result, it is inversely proportional to the output load current. The voltage drop across the cable is compensated by this offset voltage at INV pin. It can also be programmed by adjusting the resistance of the divider to compensate the drop for various cable lines used.

■ OPERATION DESCRIPTION (Cont.)

Current Sensing and Leading Edge Blanking

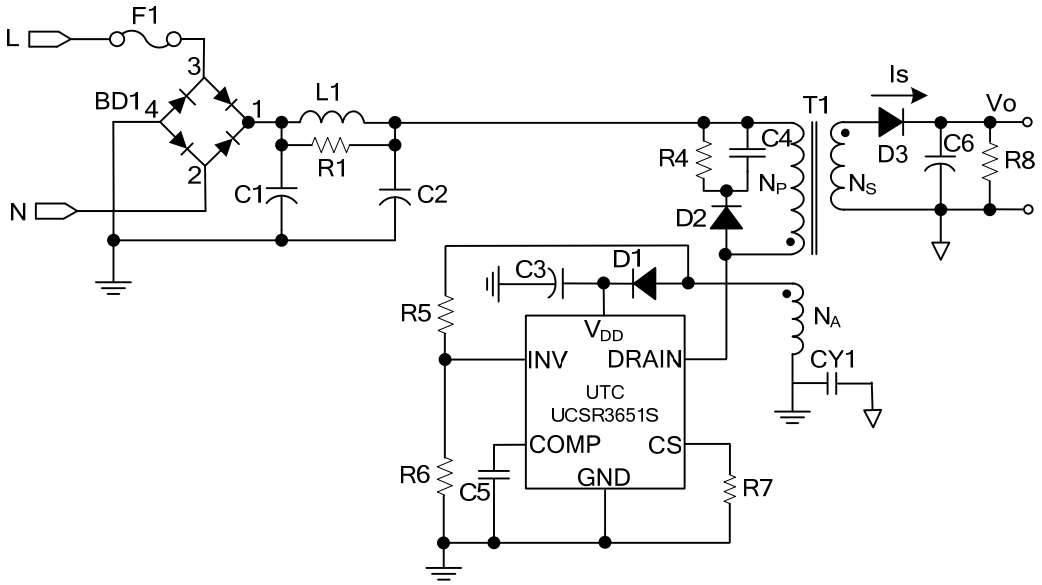
Cycle-by-cycle current limiting is offered in UTC **UCSR3651S**. The switch current is detected by a sense resistor into the CS pin. When the power switch is turned on, a turn-on spike will occur on this resistor. A 900ns leading-edge blanking is built in to avoid false-termination of the switching pulse so that the external RC filtering is no longer needed.

Protection Control

Good power supply system reliability is achieved with its comprehensive protection features including V_{DD} over-voltage protection, V_{DD} Clamp, GATE Clamp, Power on soft start, Cycle-by-cycle current limiting, short circuit protection, leading edge blanking, OTP and UVLO, etc.

V_{DD} is supplied by transformer auxiliary winding output. The output of UTC **UCSR3651S** is shutdown when V_{DD} drops below $V_{TH(OFF)}$ and the power converter enters power on start-up sequence thereafter.

■ TYPICAL APPLICATION CIRCUIT (5V, 1A)



BOM

Reference	Component	Reference	Component
BD1	BD 1A/600V	R1	R 2.4KΩ 1206 ±5%
L1	1m H 6x8mm	R4	R 200KΩ 1206 ±5%
C1	EC 4.7u F 400V 105°C	R5	R 18KΩ 0805 ±1%
C2	EC 4.7u F 400V 105°C	R6	R 3.3KΩ 0805 ±1%
C3	EC 4.7u F 50V 105°C	R7	R 1.8Ω 1206 ±5%
C4	CC 1n F 1000V 1206	R8	R 3.3KΩ 1206 ±5%
C5	CC 0.1u F 50V 0805	T1	EE-16
C6	EC 1000u F 10V 105°C	F1	FUSE 1A 250VAC
D1,D2	Diode UTC 1N4007G	CY1	Y/C 1000p F 250V
D3	Diode UTC SB340		

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