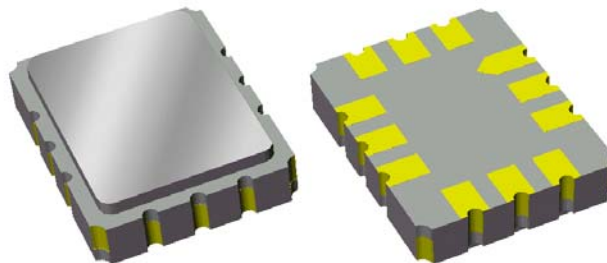


Data Sheet

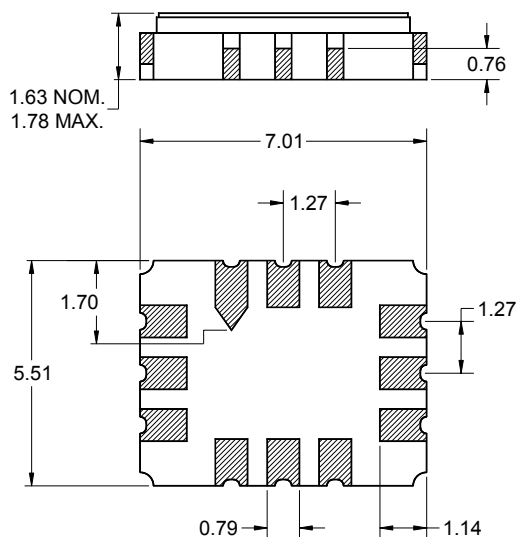
Features

- For Wireless LAN applications
- Usable bandwidth of 18 MHz
- High Attenuation
- Single-ended operation
- Ceramic Surface Mount Package (SMP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



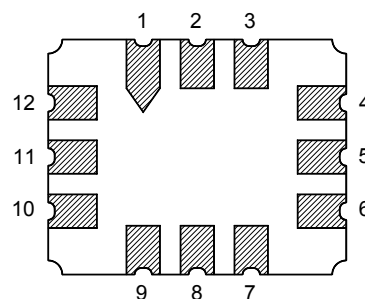
Package

Surface Mount 7.01 x 5.51 x 1.63 mm



Pin Configuration

Bottom View



Pin No.	Description
4	Output
10	Input
1,2,3,5,6	Case Ground
7,8,9,11,12	Case Ground

Dimensions shown are nominal in millimeters
 All tolerances are ± 0.15 mm except overall
 length and width ± 0.13 mm

Body: Al_2O_3 ceramic
 Lid: Kovar, Ni plated
 Terminations: Au plating 0.5 - 1.0 μ m,
 over a 2 - 6 μ m Ni plating

Data Sheet

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -40 to +85 °C

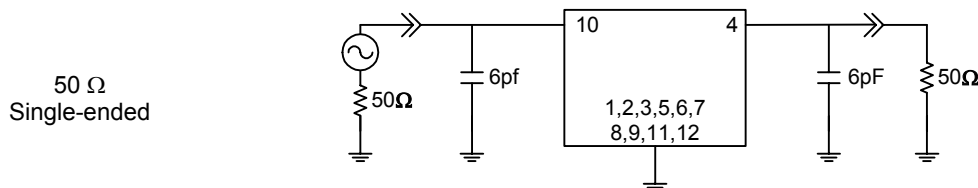
Parameter ⁽³⁾	Minimum	Typical	Maximum	Unit
Center Frequency ⁽⁴⁾	969.20	970.060	970.80	MHz
Insertion Loss at f_0	-	24.7	27	dB
Lower 3 dB Bandedge ⁽⁵⁾	-	959.2	961	MHz
Upper 3 dB Bandedge	979	980.7	-	MHz
Lower 35 dB Bandedge ⁽⁵⁾	954	955.9	-	MHz
Upper 35 dB Bandedge	-	984.3	986	MHz
Passband Ripple (over 3 dB bandwidth)	-	0.58	1.0	dB p-p
Passband Ripple (over any 300 kHz bandwidth)	-	0.15	0.3	dB p-p
Passband Amplitude Tilt	-	0.2	1.5	dB
Phase Ripple (over 3 dB bandwidth)	-	3	8.0	deg
Phase Ripple (over any 300 kHz bandwidth)	-	0.5	2.0	deg
Group Delay	-	0.5	0.6	μsec
Group Delay Variation (over 3 dB bandwidth)	-	25	100	nsec
Relative Attenuation ⁽⁵⁾				
10 - 945 MHz	35	40	-	dB
995 - 1400 MHz	35	40	-	dB
Adjacent Channel Rejection (Integrated Power) ⁽⁵⁾				
941.6 - 958.4 MHz	15	20	-	dB
981.6 - 998.4 MHz	15	20	-	dB
Source Impedance ⁽⁶⁾	-	50	-	Ω
Load Impedance ⁽⁶⁾	-	50	-	Ω

Notes:

1. All specifications are based on the test circuit shown below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Evaluated as the mean of the 3dB frequencies relative to minimum insertion loss
5. Referenced to Minimum Insertion Loss
6. This is the optimum impedance in order to achieve the performance shown

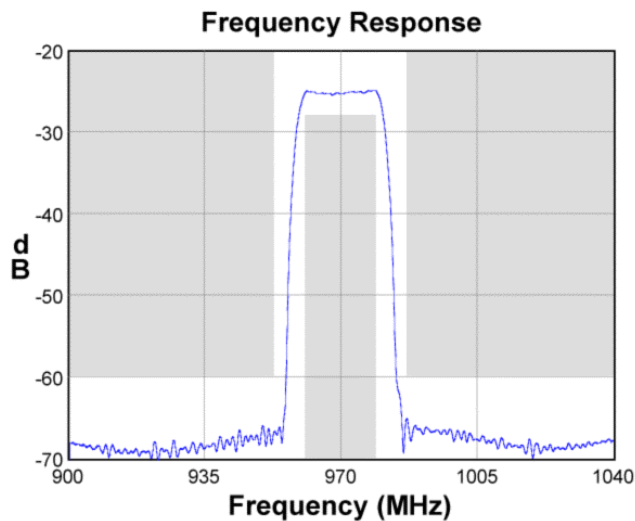
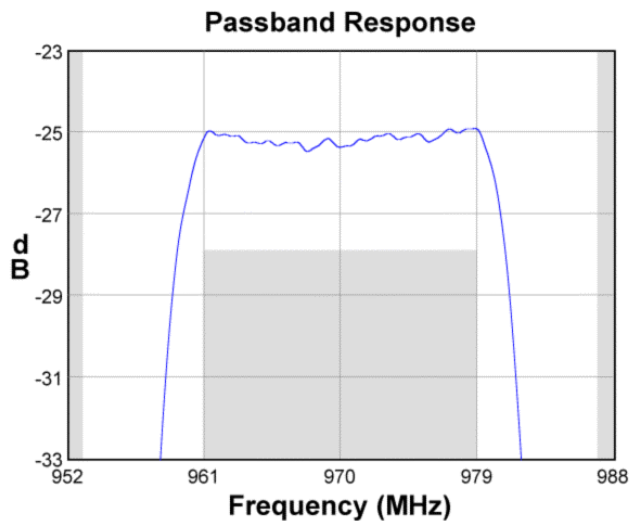
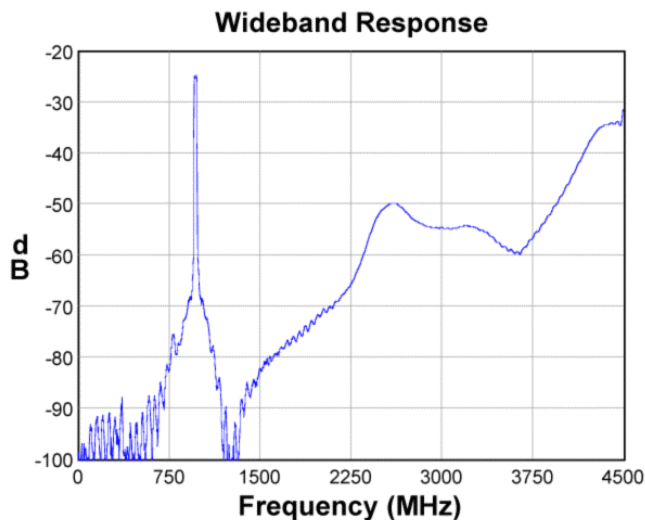
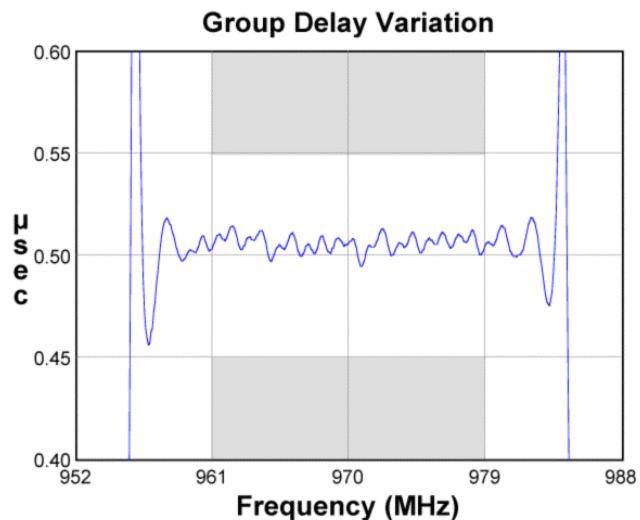
Test Circuit:

Actual matching values may vary due to PCB layout and parasitics

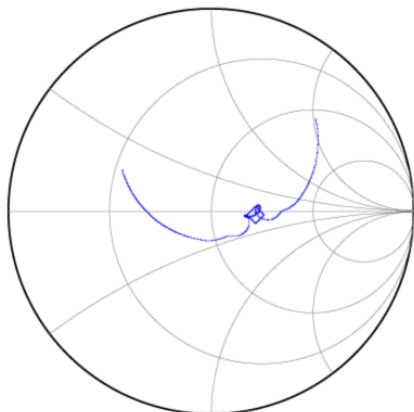


Data Sheet

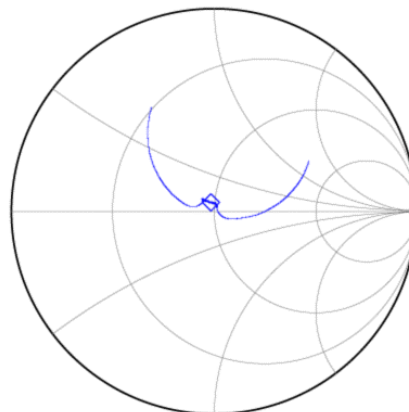
Typical Performance (at +25°C)



Input Smith Chart



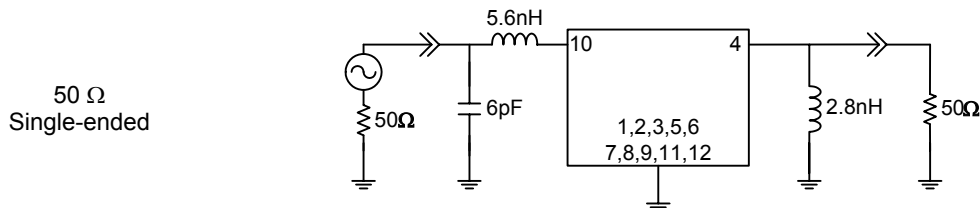
Output Smith Chart



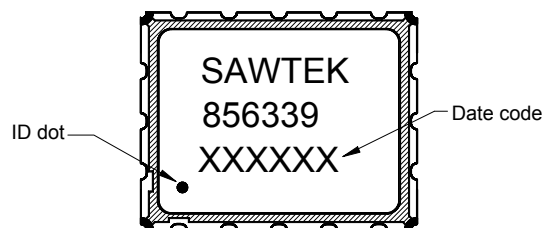
Data Sheet

Matching Schematics

Actual matching values may vary due to PCB layout and parasitics

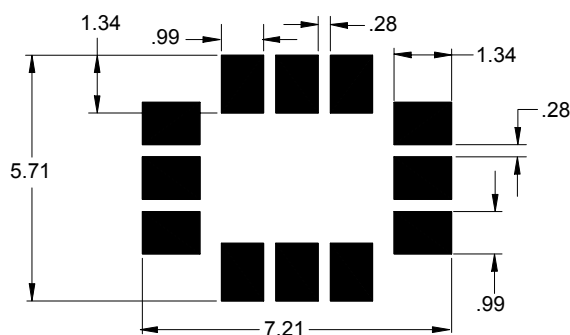


Marking



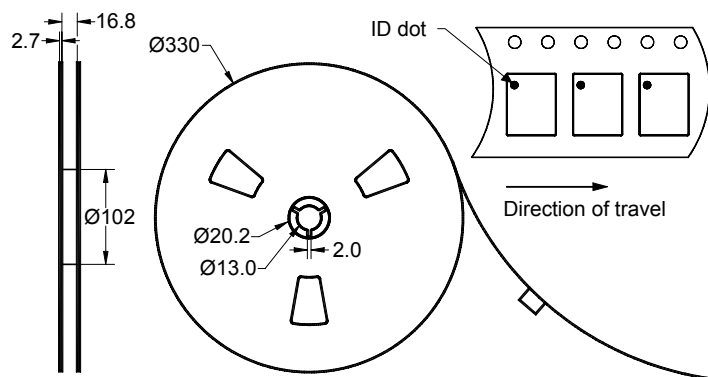
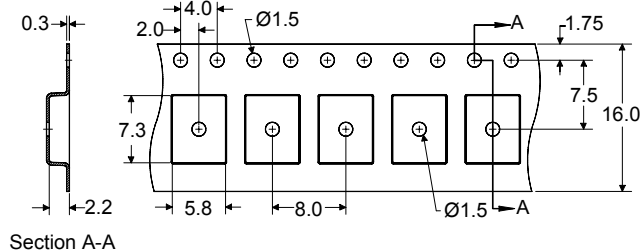
The date code consists of: day of the current year (Julian, 3 digits), last digit of the year (1 digit) and hour (2 digits)

PCB Footprint



This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel



Dimensions shown are nominal in millimeters
Packaging quantity: 3000 units/reel

Data Sheet

Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T _{stg}	-55	+85	°C

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

Solderability

- Compatible with JEDEC J-STD-020C **Pb**-free process, **260°C** peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)[Qualification Flowchart](#)[Soldering Profile](#)[S-Parameters](#)[RoHS information](#)[Other Technical Information](#)

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