

Product Specification

RoHS-6 Compliant 10Gb/s 40km Datacom SFP+ Transceiver

FTLX1671D3BCL

PRODUCT FEATURES

- Hot-pluggable SFP+ footprint
- Supports 9.95 to 10.3Gb/s bit rates
- Power dissipation < 1.5W;
Typical <1W
- RoHS-6 compliant (lead-free)
- Commercial temperature range
0°C to 70°C
- Single 3.3V power supply
- Maximum link length of 40km
- 1550nm cooled CML™ technology
- Receiver limiting electrical interface
- Duplex LC connector
- Built-in digital diagnostic functions



APPLICATIONS

- 10GBASE-ER/EW 10G Ethernet

Finisar's FTLX1671D3BCL 10Gb/s Enhanced Small Form Factor Pluggable SFP+ transceivers are designed for use in 10-Gigabit Ethernet links up to 40km over single mode fiber. They are compliant with SFF-8431¹, SFF-8432² and IEEE 802.3ae 10GBASE-ER/EW³. The FTLX1671D3BCL incorporates advanced CML™ technology, and is a "limiting module", i.e., it employs a limiting receiver. Host board designers using an EDC PHY IC should follow the IC manufacturer's recommended settings for interoperating the host-board EDC PHY with a limiting receiver SFP+ module. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472⁴. The transceiver is RoHS compliant and lead free per Directive 2002/95/EC⁵, and Finisar Application Note AN-2038⁶.

PRODUCT SELECTION

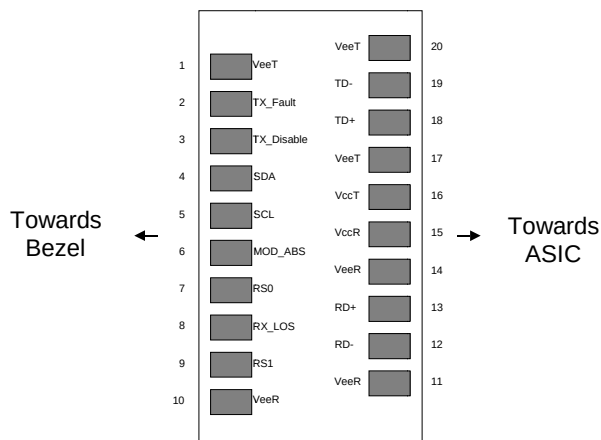
FTLX1671D3BCL

I. Pin Descriptions

Pin	Symbol	Name/Description	Ref.
1	V _{EET}	Transmitter Ground	1
2	T _{FAULT}	Transmitter Fault	2
3	T _{DIS}	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line	2
5	SCL	2-wire Serial Interface Clock Line	2
6	MOD_ABS	Module Absent. Grounded within the module	2
7	RS0	Rate Select 0.	4
8	RX_LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	RS1	Rate Select 1.	4
10	V _{EER}	Receiver Ground	1
11	V _{EER}	Receiver Ground	1
12	RD-	Receiver Inverted DATA out. AC Coupled.	
13	RD+	Receiver Non-inverted DATA out. AC Coupled.	
14	V _{EER}	Receiver Ground	1
15	V _{CCR}	Receiver Power Supply	
16	V _{CCT}	Transmitter Power Supply	
17	V _{EET}	Transmitter Ground	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V _{EET}	Transmitter Ground	1

Notes:

1. Circuit ground is internally isolated from chassis ground.
2. T_{FAULT} is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to V_{cc} + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
3. Laser output disabled on T_{DIS} >2.0V or open, enabled on T_{DIS} <0.8V.
4. Internally pulled down per SFF-8431 Rev 4.1.
5. LOS is open collector output. Should be pulled up with 4.7k – 10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

**Figure 1. Diagram of Host Board Connector Block Pin Numbers and Names.**

II. Absolute Maximum Ratings

Exceeding the limits below may damage the transceiver module permanently.

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Maximum Supply Voltage	V _{CC}	-0.5		4.0	V	
Storage Temperature	T _S	-40		85	°C	
Relative Humidity	RH	0		85	%	1

1. Non-condensing.

III. Electrical Characteristics (T_{OP} = 0 to 70 °C, V_{CC} = 3.14 to 3.46 Volts)

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Supply Voltage	V _{CC}	3.14		3.46	V	
Supply Current	I _{CC}		270	400	mA	
Transmitter						
Input differential impedance	R _{in}		100		Ω	1
Differential data input swing	V _{in,pp}	180		700	mV	
Transmit Disable Voltage	V _D	2		V _{CC}	V	
Transmit Enable Voltage	V _{EN}	V _{EE}		V _{EE} + 0.8	V	
Receiver						
Differential data output swing	V _{out,pp}	300		850	mV	2,6
Output rise time and fall time	T _r , T _f	28			ps	3
LOS Fault	V _{LOS fault}	2		V _{CCHOST}	V	4
LOS Normal	V _{LOS norm}	V _{EE}		V _{EE} +0.8	V	4
Power Supply Noise Tolerance	V _{CC} T/V _{CC} R	Per SFF-8431 Rev 4.1			mV _{pp}	5

Notes:

1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
2. Into 100Ω differential termination.
3. 20 – 80%. Measured with Module Compliance Test Board and OMA test pattern. Use of four 1's and four 0's sequence in the PRBS 9 is an acceptable alternative.
4. LOS is an open collector output. Should be pulled up with 4.7kΩ – 10kΩ on the host board. Normal operation is logic 0; loss of signal is logic 1.
5. See Section 2.8.4 of SFF-8431 Rev 4.1.
6. The FTLX1671D3BCL is a “limiting module”, i.e., it employs a limiting receiver. Host board designers using an EDC PHY IC should follow the IC manufacturer's recommended settings for interoperating the host-board EDC PHY with a limiting receiver SFP+ module.

IV. Optical Characteristics (T_{OP} = 0 to 70 °C, V_{CC3} = 3.14 to 3.46 Volts)

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Transmitter						
Optical Modulation Amplitude (OMA) minus TDP	P _{OMA-TDP}	-2.1			dBm	
Average Launch Power	P _{Ave}	-4.7		+4.0	dBm	1
Optical Wavelength	λ	1530		1565	nm	
Side-Mode Suppression Ratio	SMSR	30			dB	
Optical Extinction Ratio	ER	3.0			dB	
Transmitter and Dispersion Penalty	TDP			3.0	dB	
Average Launch power of OFF transmitter	P _{OFF}			-30	dBm	
Tx Jitter	T _{Xj}	Per 802.3ae requirements				
Relative Intensity Noise	RIN			-128	dB/Hz	

Receiver						
Receiver Sensitivity (OMA) @ 10.3 Gb/s	R _{SENS1}			-14.1	dBm	2
Stressed Receiver Sensitivity (OMA) @ 10.3 Gb/s	R _{SENS2}			-11.3	dBm	3
Average Receive Power	P _{AVE}	-15.8		-1.0	dBm	4
Damage Threshold				+4	dBm	
Optical Center Wavelength	λ_C	1260		1600	nm	
Receiver Reflectance	R _{FX}			-26	dB	
LOS De-Assert	LOS _D			-19	dBm	
LOS Assert	LOS _A	-28			dBm	
LOS Hysteresis		0.5			dB	

Notes:

1. Average launch power (min) is informative and not the principal indicator of signal strength. A transmitter with launch power below this value cannot be compliant; however, a value above this does not ensure compliance.
2. Valid between 1530 and 1565 nm. Measured with worst ER; BER<10⁻¹²; 2³¹ – 1 PRBS.
3. Valid between 1530 and 1565 nm. Per IEEE 802.3ae.
4. Average power figures are informative only, per IEEE 802.3ae.

V. General Specifications

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Bit Rate	BR	9.95		10.3	Gb/s	1
Bit Error Ratio	BER			10 ⁻¹²		2
Max. Supported Link Length	L _{MAX}		40		km	3

Notes:

1. 10GBASE-ER, 10GBASE-EW.
2. Tested with a 2³¹ – 1 PRBS.
3. SMF-28 fiber, 1565nm wavelength.

Power Level I/Power Level II Power-up Sequence

The FTLX1671D3BCL is a Power Level II device, as the maximum power dissipation is above 1W at high temperature. Please refer to SFF-8431 and Finisar Application Note AN-2076 for details of the host responsibilities regarding the Power Level I/Power Level II power-up sequence to ensure proper transceiver operation.

Timing Parameters

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Time to initialize cooled module	t _{start_up_cooled}		60	90	s	1

Notes:

1. More details on Timing Parameters can be found in SFF-8431. Maximum Tx_Disable negate time t_{on} = 3 seconds.

VI. Environmental Specifications

Finisar FTLX1671D3BCL transceivers have an operating temperature range from 0°C to +70°C case temperature.

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Case Operating Temperature	T _{op}	0		70	°C	
Storage Temperature	T _{sto}	-40		85	°C	

VII. Regulatory Compliance

Finisar transceivers are Class 1 Laser Products and comply with US FDA regulations. These products are certified by TÜV and CSA to meet the Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950. Copies of certificates are available at Finisar Corporation upon request.

VIII. Digital Diagnostic Functions

Finisar FTLX1671D3BCL SFP+ transceivers support the 2-wire serial communication protocol as defined in the SFP MSA¹. It is very closely related to the E²PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Finisar SFP+ transceivers provide an enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E²PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement. The complete interface is described in Finisar Application Note AN-2030: "Digital Diagnostics Monitoring Interface for SFP Optical Transceivers".

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E²PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod

Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

For more information, please see the SFP MSA documentation^{1,5} and Finisar Application Note AN-2030.

Please note that evaluation board FDB-1027 is available with Finisar ModDEMO software that allows simple to use communication over the 2-wire serial interface.

IX. Digital Diagnostic Specifications

FTLX1671D3BCL transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Accuracy						
Internally measured transceiver temperature	$\Delta DD_{\text{Temperature}}$			3	°C	
Internally measured transceiver supply voltage	$\Delta DD_{\text{Voltage}}$			3	%	
Measured TX bias current	ΔDD_{Bias}			10	%	1
Measured TX output power	$\Delta DD_{\text{Tx-Power}}$			2	dB	
Measured RX received average optical power	$\Delta DD_{\text{Rx-Power}}$			2	dB	
Dynamic Range for Rated Accuracy						
Internally measured transceiver temperature	$DD_{\text{Temperature}}$	0		70	°C	
Internally measured transceiver supply voltage	DD_{Voltage}	3.1		3.5	V	
Measured TX bias current	DD_{Bias}	0		100	mA	
Measured TX output power	$DD_{\text{Tx-Power}}$	-4.7		+4.0	dBm	
Measured RX received average optical power	$DD_{\text{Rx-Power}}$	-15.8		-1.0	dBm	

Notes:

1. Accuracy of measured Tx bias current is 10% of the actual bias current from the laser driver to the laser.

X. Mechanical Specifications

Finisar FTLX1671D3BCL SFP+ transceivers are compatible with the SFF-8432 specification for improved pluggable form factor, and shown here for reference purposes only. Bail color is red.

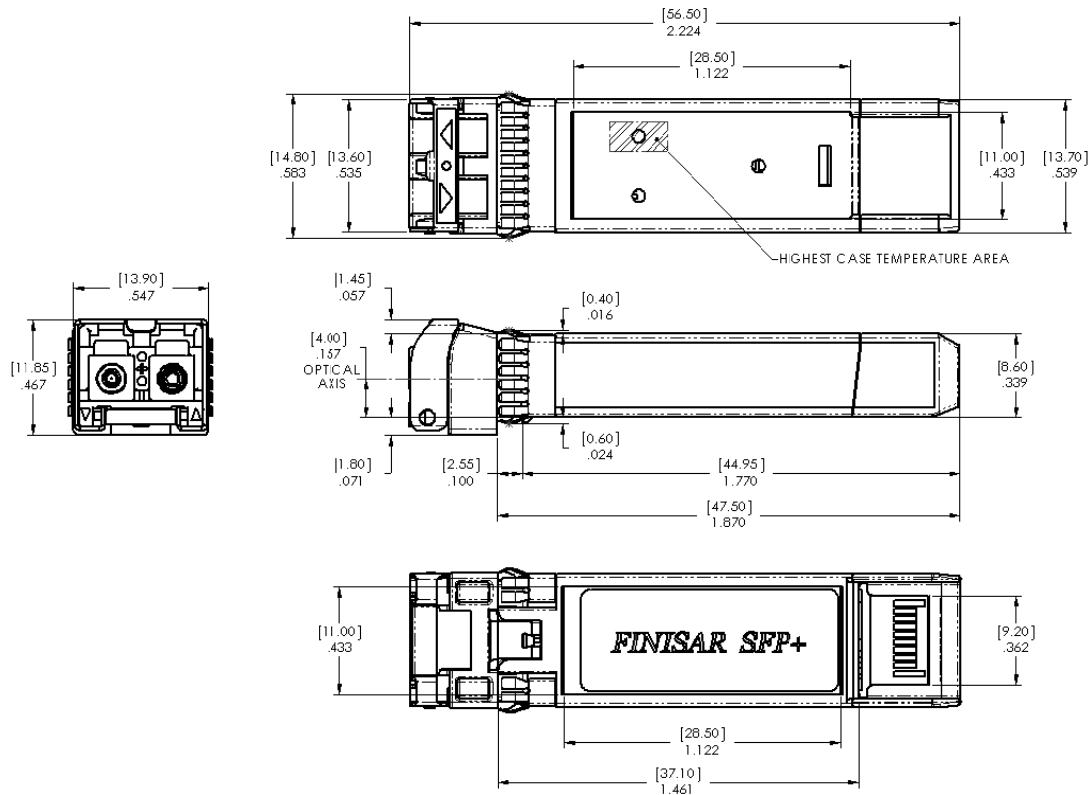


Figure 2. FTLX1671D3BCL Mechanical Dimensions

XI. PCB Layout and Bezel Recommendations

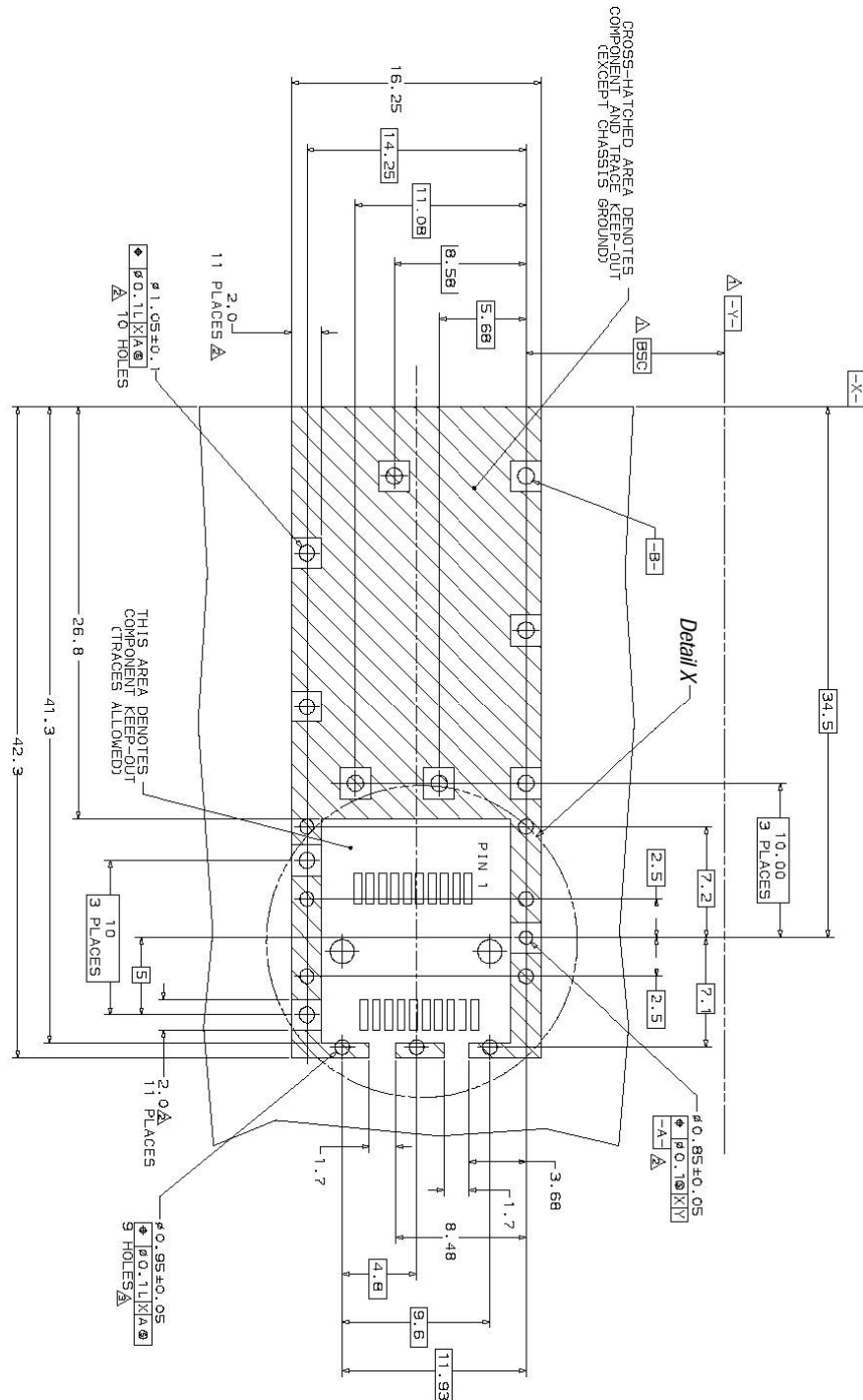
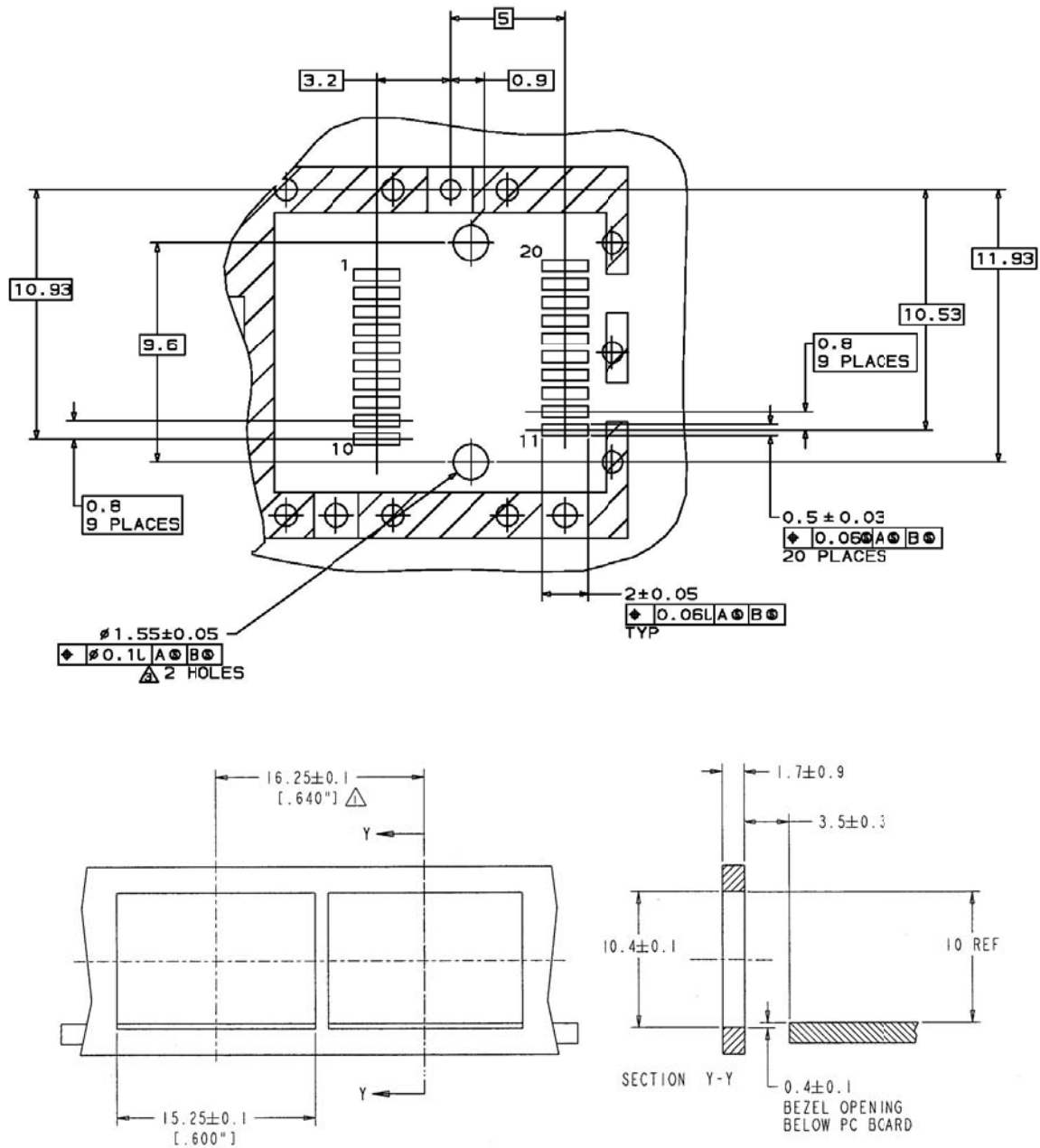


Figure 3

- △ Datum and Basic Dimension Established by Customer
- △ Pads and Vias are Chassis Ground, 11 Places
- △ Through Holes are Unplated

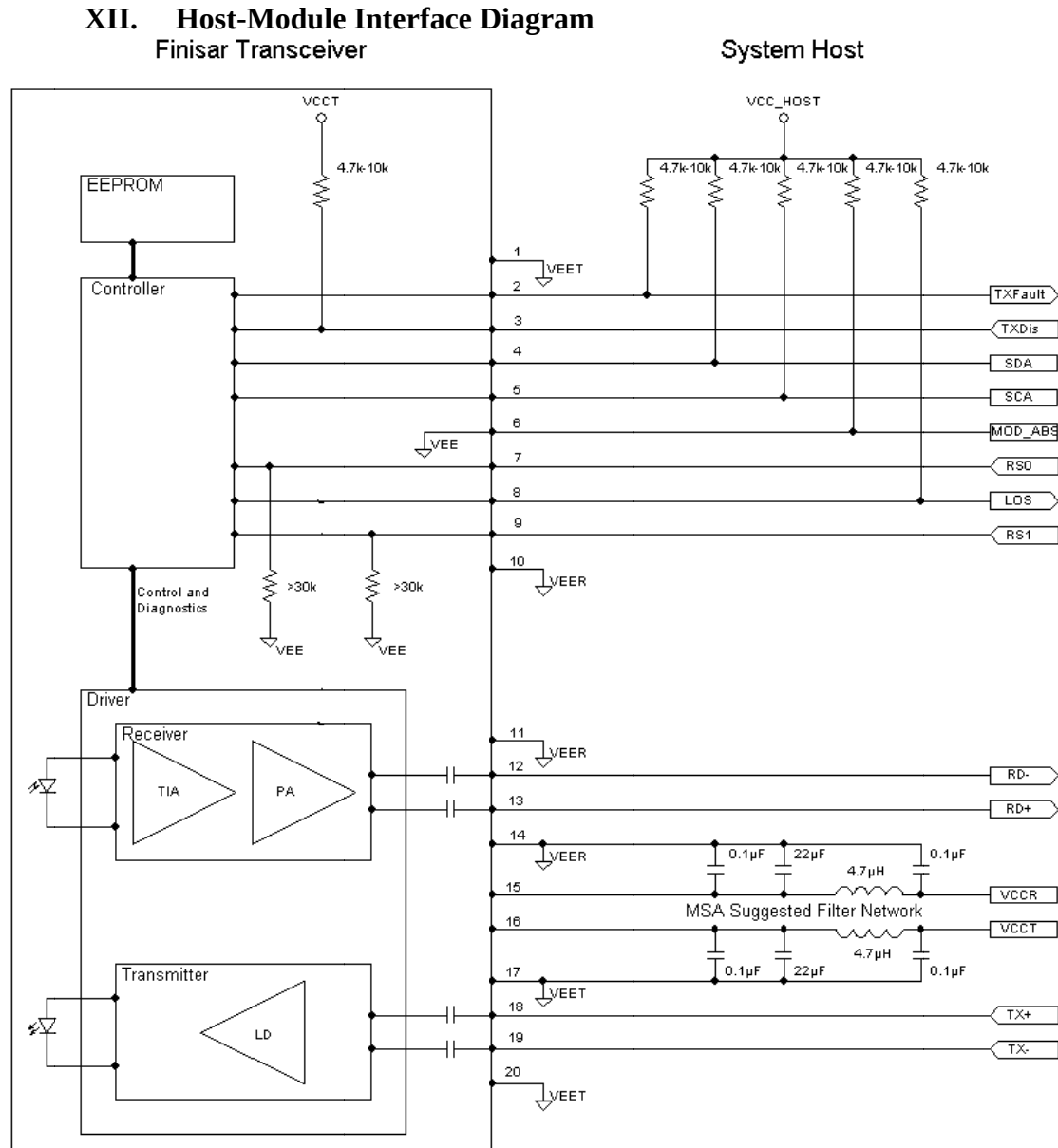


NOTES:

△ MINIMUM PITCH ILLUSTRATED, ENGLISH DIMENSIONS ARE FOR REFERENCE ONLY

2. NOT RECOMMENDED FOR PCI EXPANSION CARD APPLICATIONS

Figure 4

**Figure 5**

XIII. References

1. “Specifications for Enhanced 8.5 and 10 Gigabit Small Form Factor Pluggable Module ‘SFP+ ’”, SFF Document Number SFF-8431, Revision 4.1, July 6, 2009. Although SFF-8431 does not explicitly refer to 10GBASE-ER/EW, it is intended for the FTLX1671D3BCL product to adhere to references to electrical SFI specifications similar to 10GBASE-LR/LW.
2. “Improved Pluggable Formfactor”, SFF Document Number SFF-8432, Revision 4.2, April 18, 2007.
3. IEEE Std 802.3ae, Clause 52, PMD Type 10GBASE-ER. IEEE Standards Department.
4. “Digital Diagnostics Monitoring Interface for Optical Transceivers”. SFF Document Number SFF-8472, Revision 10.1, March 1, 2007.
5. Directive 2002/95/EC of the European Council Parliament and of the Council, “on the restriction of the use of certain hazardous substances in electrical and electronic equipment”. January 27, 2003.
6. “Application Note AN-2038: Finisar Implementation Of RoHS Compliant Transceivers”, Finisar Corporation, January 21, 2005.

XIV. For More Information

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