

J270 – J271 / SST270 – SST271

FEATURES

- Surface Mount

APPLICATIONS

- P-Channel Amplifier

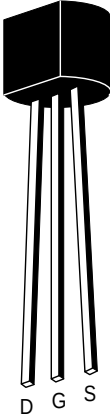
DESCRIPTION

The J270/SST270 Series is an all-purpose amplifier for designs requiring P-channel operation. These devices feature high gain, low noise and tight $V_{GS(OFF)}$ limits for simple circuit design. They are available in low-cost SOT-23 and TO-92 packages and are fully compatible with automatic insertion techniques.

ORDERING INFORMATION

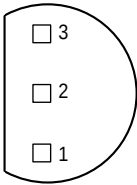
Part	Package	Temperature Range
J270-271	Plastic TO-92	-55°C to +135°C
SST270-271	Plastic SOT-23	-55°C to +135°C

PIN CONFIGURATION



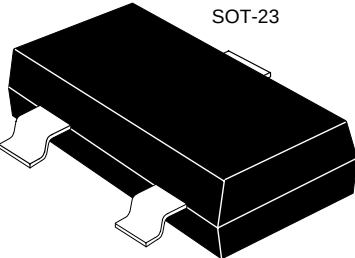
TO-92

D G S



1 DRAIN
2 GATE
3 SOURCE

BOTTOM VIEW



SOT-23

1 GATE
2 SOURCE
3 DRAIN

TOP VIEW

PRODUCT MARKING (SOT-23)	
SST270	P20
SST271	P21

5508

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}\text{C}$ unless otherwise noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMIT	UNIT
Gate-Drain Voltage	V_{GD}	30	V
Gate-Source Voltage	V_{GS}	30	V
Gate Current	I_G	-50	mA
Power Dissipation	P_D	350	mW
Power Derating		2.8	mW/ $^{\circ}\text{C}$
Operating Junction Temperature	T_J	-55 to 150	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-55 to 150	$^{\circ}\text{C}$
Lead Temperature (1/16" from case for 10 seconds)	T_L	300	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	PARAMETER	TYP ¹	270		271		UNIT	TEST CONDITIONS
			MIN	MAX	MIN	MAX		
STATIC								
V _{(BR)GSS}	Gate-Source Breakdown Voltage	45	30		30		V	I _G = 1μA, V _{DS} = 0V
V _{GS(OFF)}	Gate-Source Cutoff Voltage		0.5	2.0	1.5	4.5		V _{DS} = -15V, I _D = -1nA
I _{DSS}	Saturation Drain Current ²		-2	-15	-6	-50	mA	V _{DS} = -15V, V _{GS} = 0V
I _{GSS}	Gate Reverse Current	10		200		200	pA	V _{GS} = 20V, V _{DS} = 0V
		5					nA	T _A = 125°C
I _G	Gate Operating Current	10					pA	V _{DG} = -15V, I _D = -1mA
V _{GS(F)}	Gate-Source Forward Voltage	-0.7					V	I _G = -1mA, V _{DS} = 0V
DYNAMIC								
g _{fs}	Common-Source Forward Transconductance		6	15	8	18	mS	V _{DS} = -15V, V _{GS} = 0V f = 1kHz
g _{os}	Common-Source Output Conductance			200		500	μS	
C _{iss}	Common-Source Input Capacitance	20					pF	V _{DS} = -15V, V _{GS} = 0V f = 1MHz
C _{rss}	Common-Source Reverse Transfer Capacitance	4						
e _n	Equivalent Input Noise Voltage	20					$\frac{nV}{\sqrt{Hz}}$	V _{DS} = -10V, V _{GS} = 0V f = 1kHz

NOTES: 1. For design aid only, not subject to production testing.
2. Pulse test; $PW = 300\mu\text{s}$, duty cycle $\leq 3\%$.