# AR/ARS50X SERIES

## HIGH CURRENT PLASTIC SILICON RECTIFIER

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## AR/ARS50005 THRU AR/ARS5010

### HIGH CURRENT PLASTIC SILICON RECTIFIER



REVERSE VOLTAGE: 50 to 1000 VOLTS FORWARD CURRENT: 50.0 AMPERE

#### **FEATURES**

· Plastic material has Underwriters Laboratory Flammability Classification 94V-0

- · Low cost construction utilizing void-free molded plastic technique
- · Low cost
- · Diffused junction
- · High surge current capability
- · Low leakage
- · High temperature soldering guaranteed: 250°C for 10 seconds

#### **MECHANICAL DATA**

Case: Molded plastic, RA/RAS

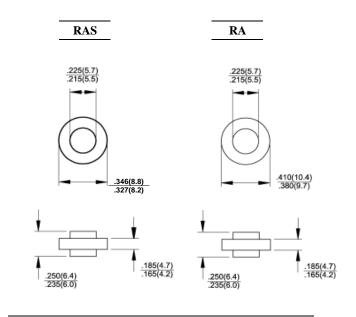
Epoxy: UL 94V-O rate flame retardant

Lead: Axial leads, solderable per MIL-STD-202,

method 208 guaranteed

Polarity: Color ring denotes cathode end

Mounting position: Any Weight: 0.07ounce, 1.8gram



**Dimensions in inches and (millimeters)** 

### Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

	Cb ala	AR50005	AR5001	AR5002	AR5004	AR5006	AR5008	AR5010	Units
	Symbols	ARS50005	ARS5001	ARS5002	ARS5004	ARS5006	ARS5008	ARS5010	
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	$V_{RMS}$	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at $T_C$ =135 $^{\circ}$ C	I <sub>(AV)</sub>	50							Amp
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	$I_{\mathrm{FSM}}$	500							Amp
Maximum Forward Voltage at 50.0A DC and 25 ℃	$V_{\mathrm{F}}$	1.1							Volts
Maximum Reverse Current at $T_C$ =25°C at Rated DC Blocking Voltage $T_C$ =100°C	$I_R$	5.0 250							uAmp
Typical Junction Capacitance (Note 1)	$C_{J}$	300							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JC}$	1							°C/W
Maximum Reverse Recovery Time (Note 3)	$T_{RR}$	3							uS
Operating and Storage Temperature Range	T <sub>J</sub> , Tstg	-55 to +175							್

#### NOTES:

- 1- Measured at 1 MH<sub>Z</sub> and applied reverse voltage of 4.0 VDC.
- 2- Reverse Recovery Test Conditions:  $I_F$ =.5A,  $I_R$ =1A,  $I_{RR}$ =.25A.
- 3- Thermal Resistance from Junction to Case, Singe Side Cooled.

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#### RATINGS AND CHARACTERISTIC CURVES

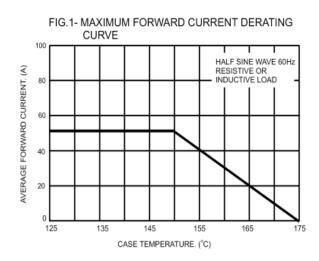


FIG.2- MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

1000

8.3ms Single Half Sine Wave JEDEC Method

1000

100

NUMBER OF CYCLES AT 60Hz

FIG.3- TYPICAL FORWARD CHARACTERISTICS

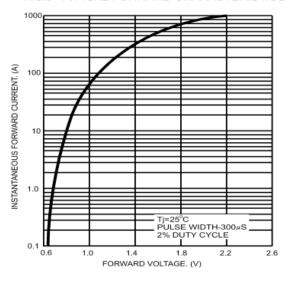


FIG.4- TYPICAL REVERSE CHARACTERISTICS

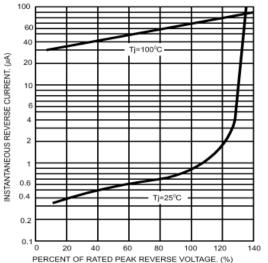


FIG.5- TYPICAL JUNCTION CAPACITANCE

