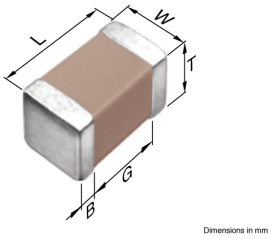
Creation Date : April 25, 2017 (GMT)

Multilayer Ceramic Chip Capacitors

C1608JB1H334K080AB

RoHS | REACH SVHC-Free | Halogen Free | Lead Free

TDK item description	C1608JB1H334KT****
Applications	Commercial Grade
Feature	General (Up to 50V)
Series	C1608 [EIA 0603]
Status	Production (Not Recommended for New Design)



Size	
Length(L)	1.60mm ±0.10mm
Width(W)	0.80mm ±0.10mm
Thickness(T)	0.80mm ±0.10mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.70mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PB)	0.80mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.60mm to 0.80mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	330nF ±10%
Rated Voltage	50VDC
Temperature Characteristic	JB(±10%)
Dissipation Factor (Max.)	5%
Insulation Resistance (Min.)	1515MΩ

Other	
Soldering Method	Wave (Flow) Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

! Images are for reference only and show exemplary products.
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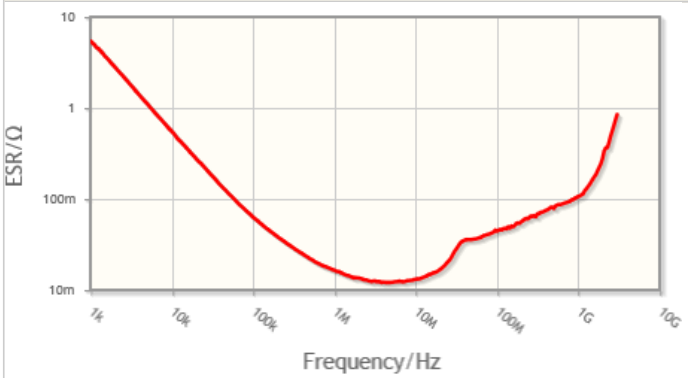
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



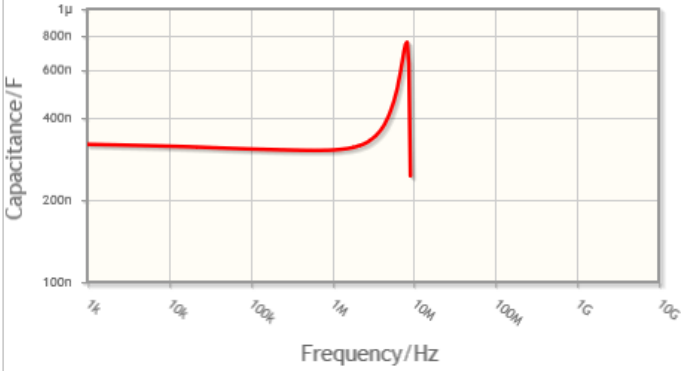
C1608JB1H334K080AB

ESR



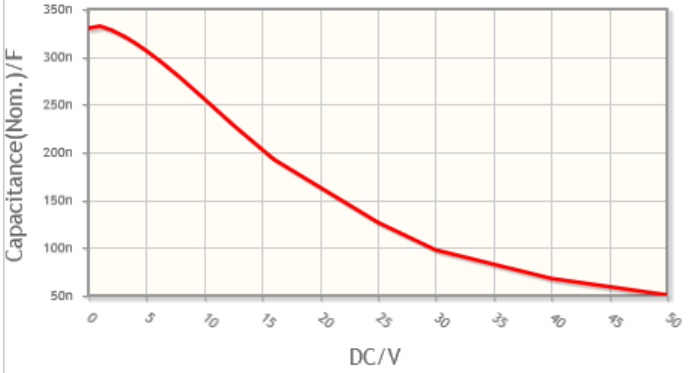
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Capacitance



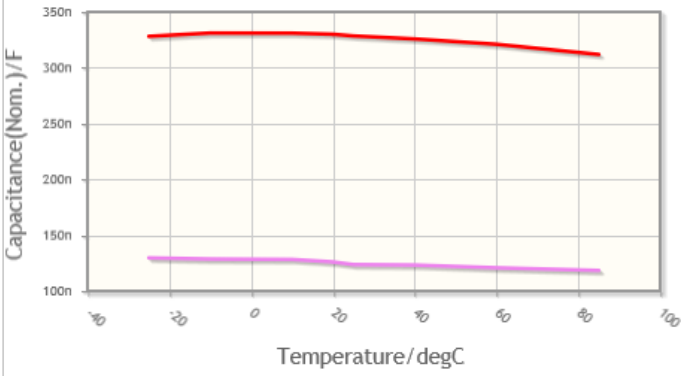
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DC Bias Characteristic



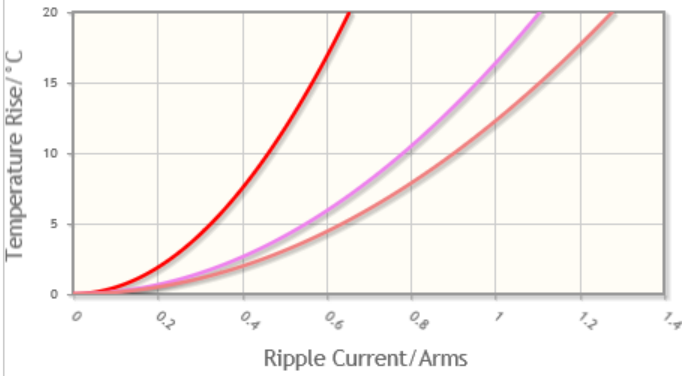
C1608JB1H334K080AB

Temperature Characteristic



C1608JB1H334K080AB(No Bias) C1608JB1H334K080AB(DC Bias = 25V)

Ripple Temperature Rising



C1608JB1H334K080AB(100kHz) C1608JB1H334K080AB(500kHz) C1608JB1H334K080AB(1MHz)

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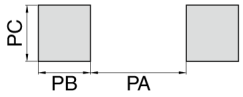
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Associated Images

Land Pattern (Terminal Connection)



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