

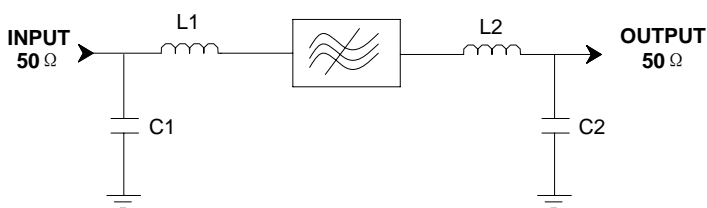
Specifications

Parameter	Unit	Minimum	Typical	Maximum	
Center Frequency	MHz	-	211.2	-	
Insertion Loss($f_0 \pm 9.42\text{MHz}$)	dB	-	10	13	
1 dB Bandwidth	MHz	18.84	20	-	
3 dB Bandwidth	MHz	-	21.34	-	
Passband Variation($f_0 \pm 9.42\text{MHz}$)	dB	-	0.6	1.3	
Absolute Delay	usec	-	0.7	-	
Phase Linearity($f_0 \pm 9.5\text{MHz}$)	deg	-	11	15	
Group Delay Variation($f_0 \pm 9.5\text{MHz}$)	nsec	-	55	100	
Input VSWR($f_0 \pm 9.42\text{MHz}$)	-	-	1.3	2	
Output VSWR($f_0 \pm 9.42\text{MHz}$)	-	-	1.9	2.2	
Ultimate Rejection	1~100MHz	dB	50	58	-
	100~168MHz	dB	55	60	-
	168~196.5MHz	dB	20	41	-
	250~270MHz	dB	50	56	-
	270~350MHz	dB	55	61	-
	350~2000MHz	dB	45	48	-
Material Temperature coefficient	KHz/°C	-19.85			
Ambient Temperature	°C	25			
Package Size	SMD7.0*5.0				

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



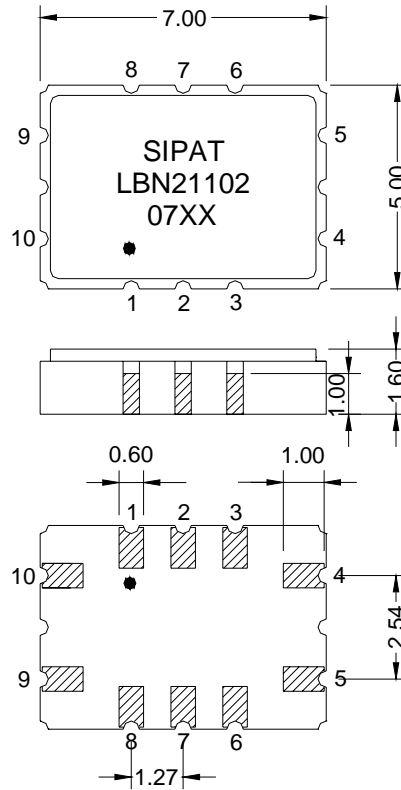
L1=56nH L2=47nH
C1=C2=22pF
Source/Load Impedance=50 ohm
Notes - Component values may change depending on board layout.



SIPAT Co., Ltd.
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#14 Nanping Huayuan Road,
Chongqing, China, 400060

Part Number	LBN21102	
Rev. Date	2007-06-07	
Ver.	1.0	Page 1/4

Package Dimension



- 1) ∴ Pad Number 1 Index
- 2) SIPAT: Manufacturer name
- 3) LBN21102: Part Number
- 4) 07XX: Date Code

Input	9
Output	4
Ground	1.2.3.5. 6.7.8.10

Package: SMD7.0*5.0

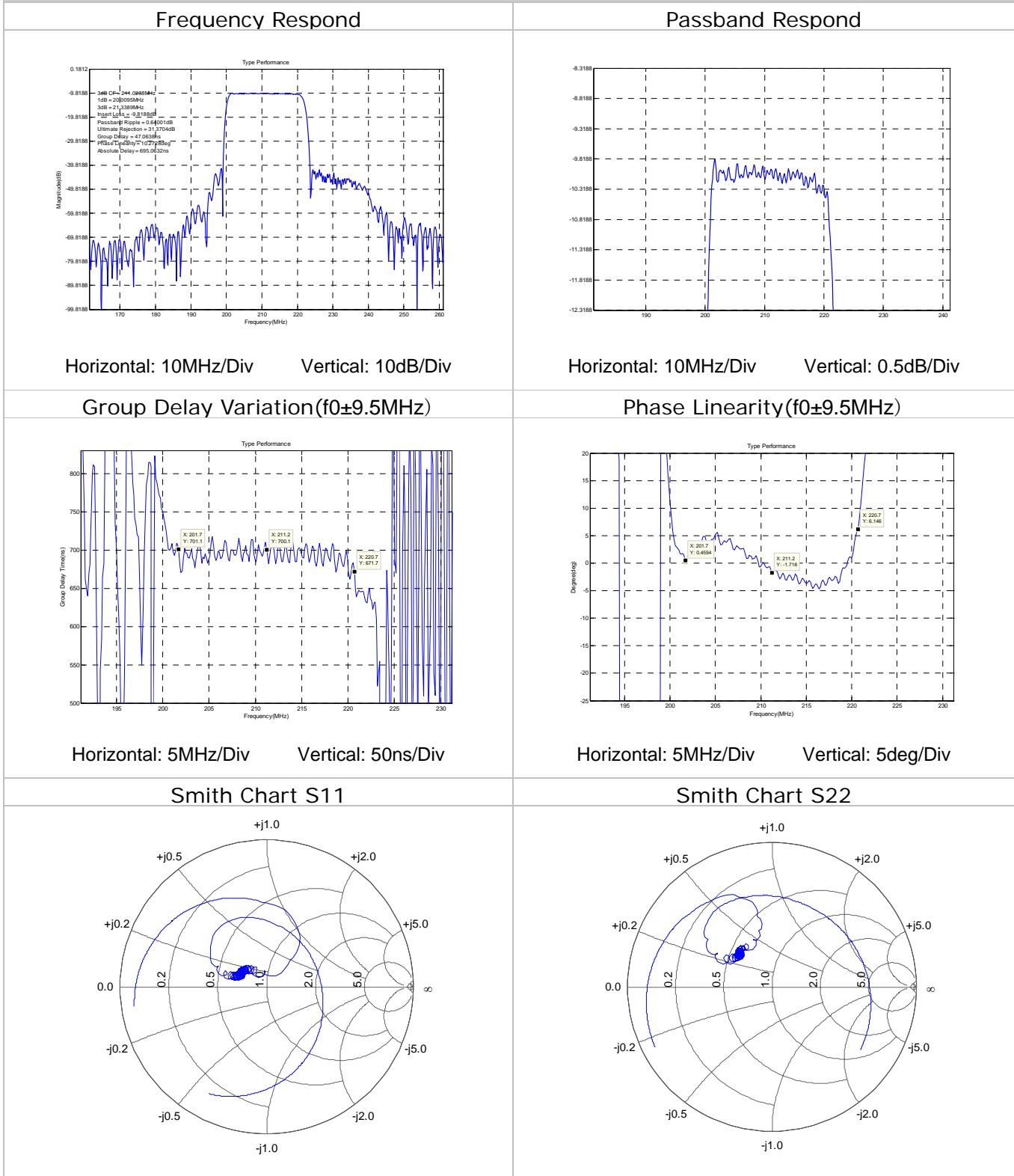
Unit: mm



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Typical Performance




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Maximum Ratings

Operating Temperature Range	°C	[-40°C;+85°C]
Storage Temperature Range	°C	[-40°C;+105°C]
DC Voltage	V	0
Input Power	dBm	10 max
ESD Class		1

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