



Ultrafast Rectifier

MUR1020CT
MURB1020CT
MURB1020CT-1

Features

- Ultrafast Recovery Time
- Low Forward Voltage Drop
- Low Leakage Current
- 175°C Operating Junction Temperature

$$t_{rr} = 25\text{ns}$$

$$I_{F(AV)} = 10\text{Amp}$$

$$V_R = 200\text{V}$$

Description/Applications

International Rectifier's MUR.. series are the state of the art Ultra fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultra fast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

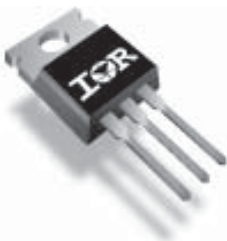
These devices are intended for use in the output rectification stage of SMPS, UPS, DC-DC converters as well as free-wheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

Absolute Maximum Ratings

Parameters	Max	Units
V_{RRM} Peak Repetitive Peak Reverse Voltage	200	V
$I_{F(AV)}$ Average Rectified Forward Current	5	A
Per Leg		
Total Device, (Rated V_R), $T_C = 149^\circ\text{C}$	10	
Total Device		
I_{FSM} Non Repetitive Peak Surge Current	50	A
Per Leg		
Per Leg		
I_{FM} Peak Repetitive Forward Current	10	A
(Rated V_R , Square wave, 20 KHz), $T_C = 149^\circ\text{C}$		
T_J, T_{STG} Operating Junction and Storage Temperatures	- 65 to 175	$^\circ\text{C}$

Case Styles

MUR1020CT	MURB1020CT	MURB1020CT-1
		
TO-220AB	D ² PAK	TO-262

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Parameters	Min	Typ	Max	Units	Test Conditions
V_{BR} , V_r Breakdown Voltage, Blocking Voltage	200	-	-	V	$I_R = 100\mu\text{A}$
V_F Forward Voltage	-	0.87	0.99	V	$I_F = 5\text{A}$, $T_J = 125^\circ\text{C}$
	-	1.02	1.20	V	$I_F = 10\text{A}$, $T_J = 125^\circ\text{C}$
	-	1.12	1.25	V	$I_F = 10\text{A}$, $T_J = 25^\circ\text{C}$
I_R Reverse Leakage Current	-	-	10	μA	$V_R = V_R$ Rated
	-	-	250	μA	$T_J = 150^\circ\text{C}$, $V_R = V_R$ Rated
C_T Junction Capacitance	-	8	-	pF	$V_R = 200\text{V}$
L_S Series Inductance	-	8.0	-	nH	Measured lead to lead 5mm from package body

Dynamic Recovery Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Parameters	Min	Typ	Max	Units	Test Conditions
t_{rr} Reverse Recovery Time	-	-	35	ns	$I_F = 1.0\text{A}$, $di_F/dt = 50\text{A}/\mu\text{s}$, $V_R = 30\text{V}$
	-	-	25		$I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $I_{REC} = 0.25\text{A}$
	-	24	-		$T_J = 25^\circ\text{C}$
	-	35	-		$T_J = 125^\circ\text{C}$
I_{RRM} Peak Recovery Current	-	3.3	-	A	$T_J = 25^\circ\text{C}$
	-	5.0	-		$T_J = 125^\circ\text{C}$
Q_{rr} Reverse Recovery Charge	-	33	-	nC	$T_J = 25^\circ\text{C}$
	-	76	-		$T_J = 125^\circ\text{C}$

Thermal - Mechanical Characteristics

Parameters	Min	Typ	Max	Units
T_J Max. Junction Temperature Range	-	-	- 65 to 175	$^\circ\text{C}$
T_{Stg} Max. Storage Temperature Range	-	-	- 65 to 175	
R_{thJC} Thermal Resistance, Junction to Case Per Leg	-	-	5	$^\circ\text{C}/\text{W}$
R_{thJA} Thermal Resistance, Junction to Ambient Per Leg	-	-	50	
R_{thCS} ① Thermal Resistance, Case to Heatsink	-	0.5	-	
Wt Weight	-	2.0	-	g
	-	0.07	-	(oz)
Mounting Torque	6.0	-	12	Kg-cm
	5.0	-	10	lbf.in

① Mounting Surface, Flat, Smooth and Greased

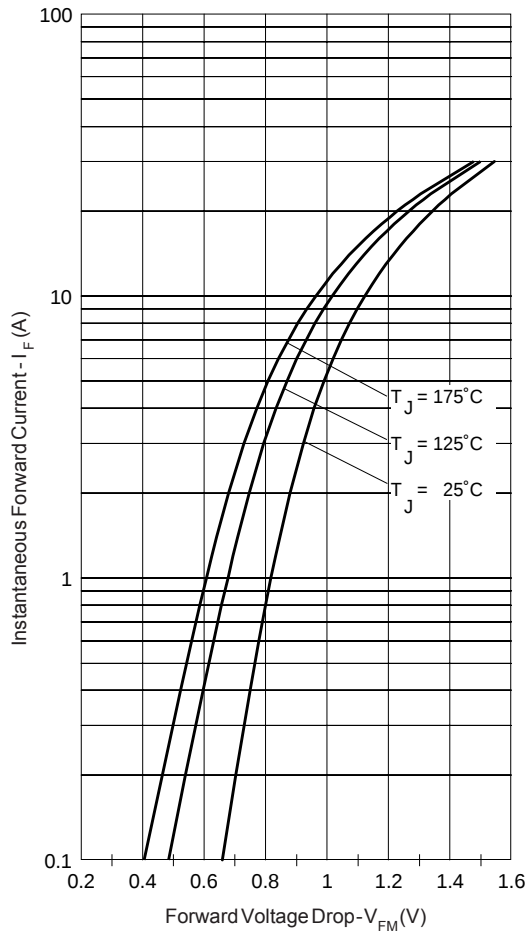


Fig. 1 - Typical Forward Voltage Drop Characteristics

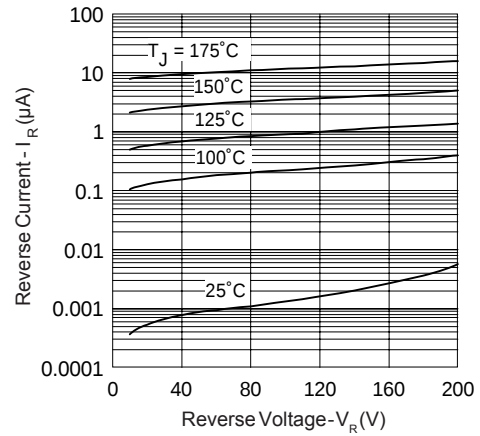


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage

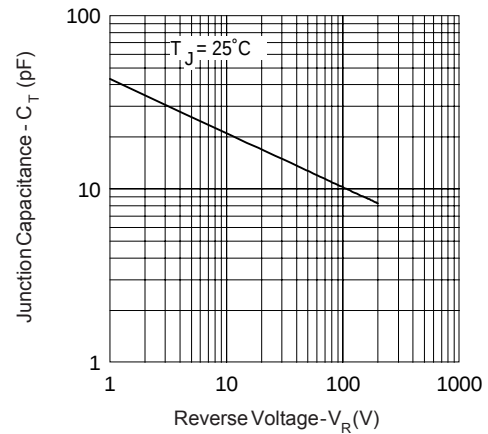


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage

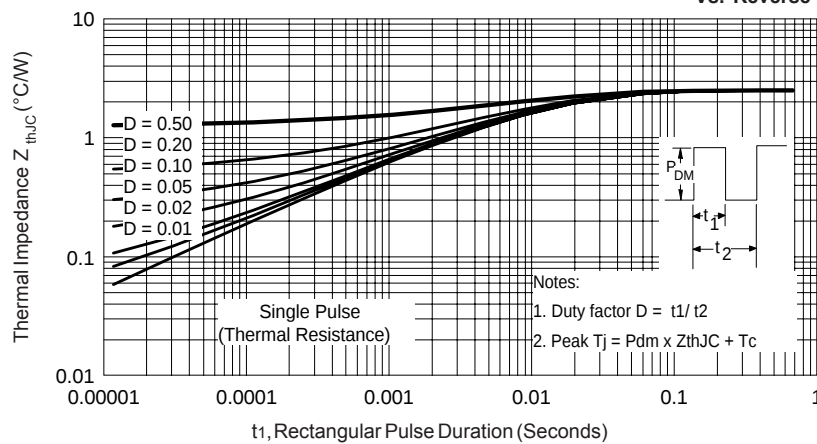


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

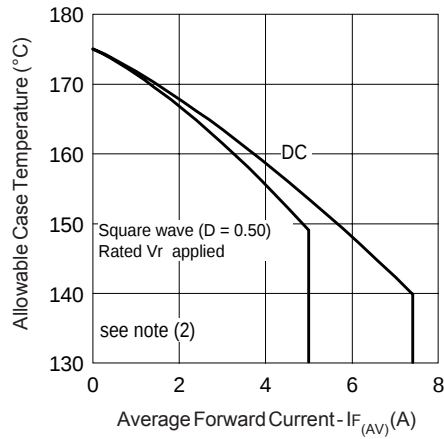


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current

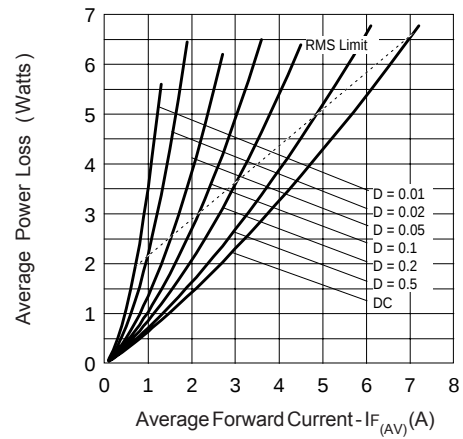


Fig. 6 - Forward Power Loss Characteristics

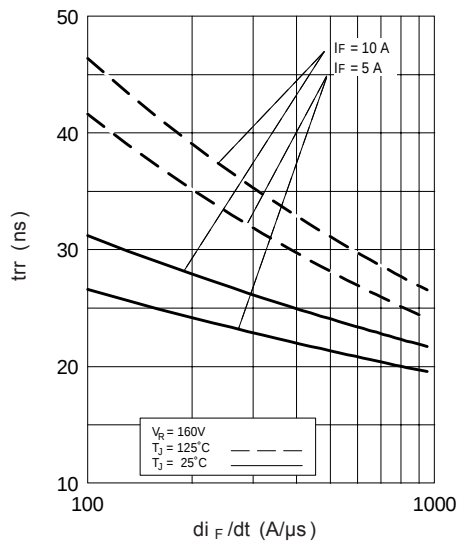


Fig. 7 - Typical Reverse Recovery vs. di_F/dt

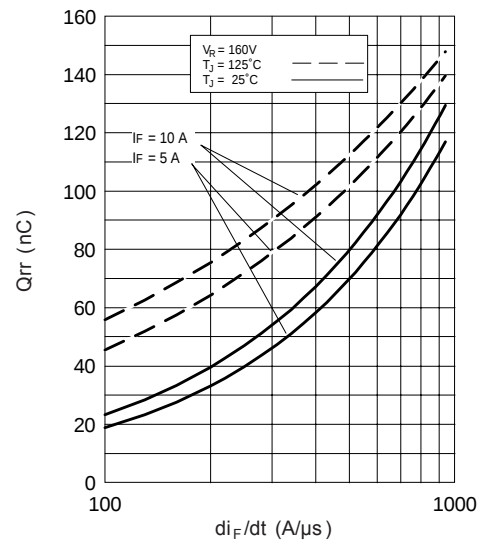


Fig. 8 - Typical Stored Charge vs. di_F/dt

(2) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward Power Loss = $I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$ (see Fig. 6);

Pd_{REV} = Inverse Power Loss = $V_{R1} \times I_R (1 - D)$; $I_R @ V_{R1}$ = rated V_R

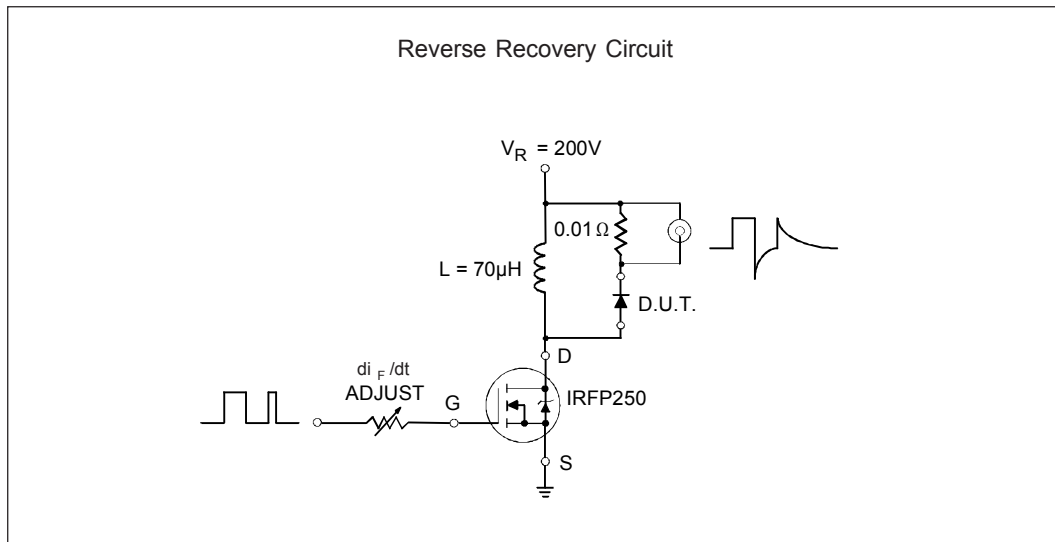


Fig. 9- Reverse Recovery Parameter Test Circuit

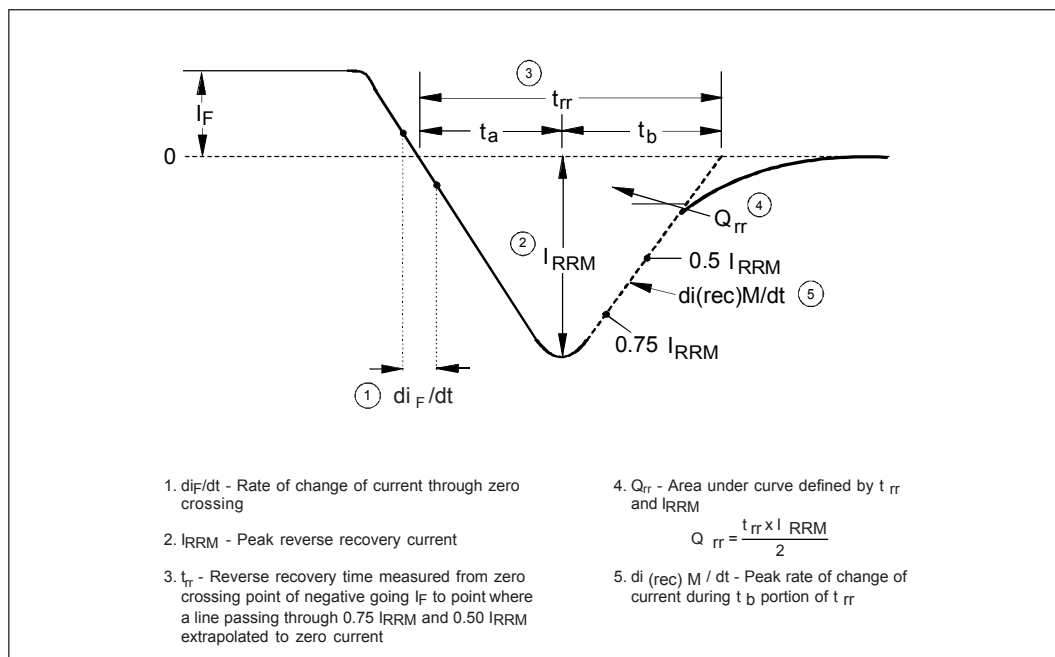
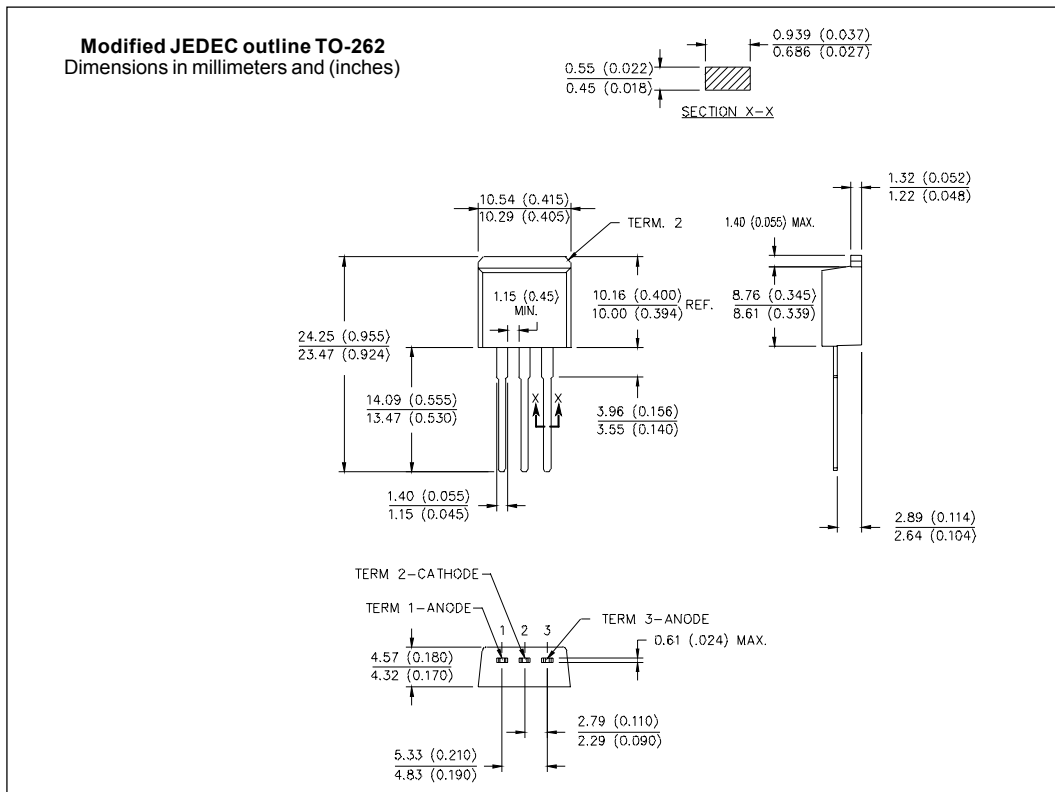
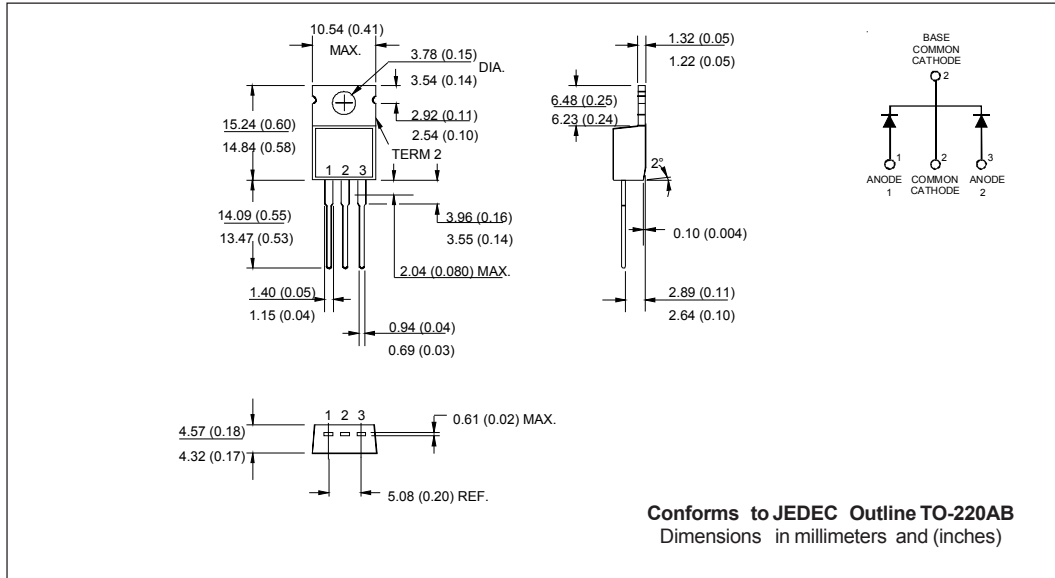
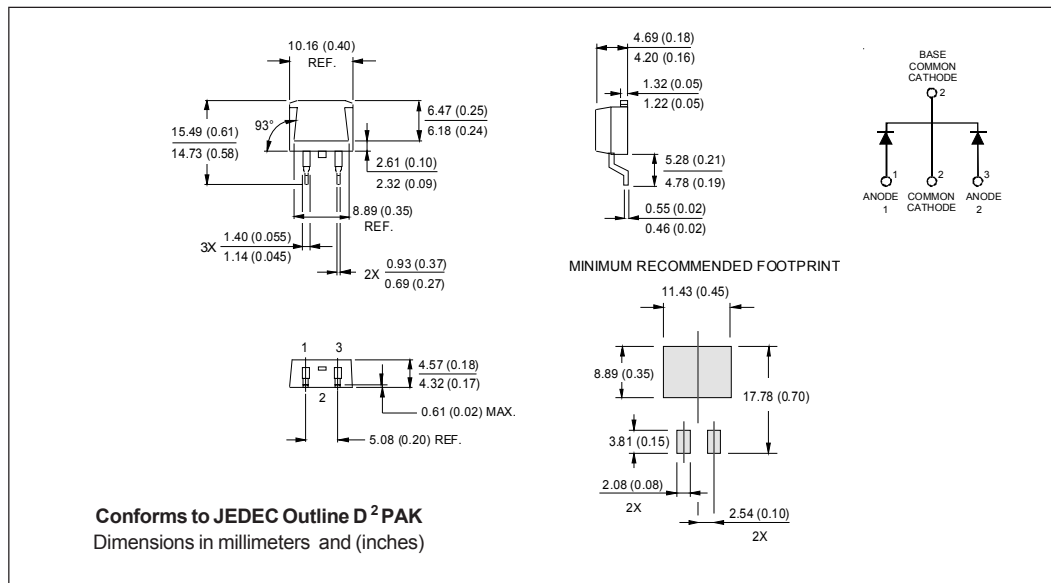


Fig. 10 - Reverse Recovery Waveform and Definitions

Outline Table



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Ordering Information Table

Device Code					
MUR	B	10	20	CT	-1
①	②	③	④	⑤	⑥
1	- Ultrafast MUR Series				
2	- B = D ² Pak / TO-262 None = TO-220AB				
3	- Current Rating (10 = 10A)				
4	- Voltage Rating (20 = 200V)				
5	- CT = Center Tap (Dual) TO-220 / D ² PAK / TO-262				
6	- "-1" = TO-262 Option				

MUR1020CT, MURB1020CT, MURB1020CT-1

Bulletin PD-20738 rev. B 12/03

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Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.

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