

UTC UNISONIC TECHNOLOGIES CO., LTD

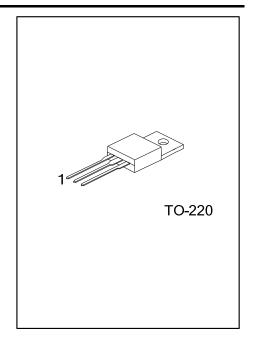
BTB24A **Preliminary TRIAC**

25A TRIACS

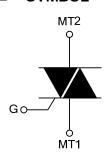
DESCRIPTION

The UTC BTB24A is a 25A triacs which can be operated in 3 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances.

The UTC BTB24A is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

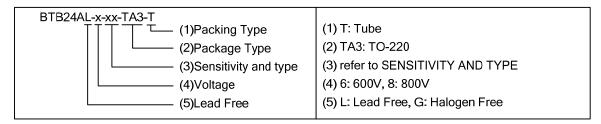


SYMBOL



ORDERING INFORMATION

Ordering	Dooksass	Pin	Assignn	Daaldaa		
Lead Free	Halogen Free	Package	1	2	3	Packing
BTB24AL-x-xx-TA3-T	BTB24AG-x-xx-TA3-T	TO-220	MT1	MT2	G	Tube

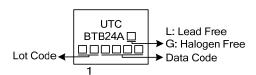


SENSITIVITY AND TYPE

PART NUMBER VOLTAGE 600V 800V	VOL1	ΓAGE	OFNOITIVITY	TYPF		
	SENSITIVITY	TYPE				
BW	0	0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		

①: Available

MARKING



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full	MS On-State Current (Full Sine Wave) T _C =75°C		I _{T(RMS)}	25	Α
Non Repetitive Surge Peak On-State Current (Full	F=50 Hz	t=20ms	l	250	Α
Cycle, T _J initial=25°C)	F=60 Hz	t=16.7ms	I _{TSM}	260	Α
I ² t Value for Fusing	t _P =10ms		l ² t	340	A^2s
Critical Rate of Rise of On-State Current I _G =2xI _{GT} , tr≤100ns	F=120 Hz	T _J =125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	T _J =25°C	V _{DSM} /V _{RSM}	V _{DRM} /V _{RRM} +100	V
Peak Gate Current	t _P =20µs	T _J =125°C	I_{GM}	4	Α
Average Gate Power Dissipation T _J =125°C		$P_{G(AV)}$	1	W	
Operating Junction Temperature			T_J	-40~+125	°C
Storage Junction Temperature			T _{STG}	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	θ_{JC}	0.8	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J =25°C unless otherwise specified.)

DADAMETED	CVMDOL	TEST CONDITIONS -		CW			BW			UNIT
PARAMETER	SYMBOL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
SNUBBERLESS TYPE (3 QUADRANTS)										
Gate Trigger Current (Note 1)	I_{GT}	V _D =12V,	1-11-111			35			50	mA
Gate Trigger Voltage	V_{GT}	$R_L=33\Omega$	1-11-111			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	- -	0.2			0.2			V
Holding Current (Note 2)	l _Η	I _T =500mA				50			75	mA
Latching Current	IL	I _G =1.2I _{GT}	1-111			70			80	mA
Latering Current	'L	IG-1.ZIG	II			80			100	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , Gate Open, T _J =125°C		500			1000			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dl/dt)c	Without Snubber, T _J =125°C		13			22			A/ms

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	V_{TM}	I _{TM} =35A, t _P =380μs	TJ=25°C			1.55	V
Threshold Voltage (Note 2)	V_{TO}		T _J =125°C			0.85	V
Dynamic Resistance (Note 2)	R_D		T _J =125°C			16	mΩ
Repetitive Peak Off-State	I_{DRM})/ -\/	TJ=25°C			5	μΑ
Current	I _{RRM}	$V_{DRM}=V_{RRM}$	T _J =125°C			3	mA

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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