

General Description

The WSF30P60 is the highest performance trench P-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF30P60 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		10s	Steady State	
V_{DS}	Drain-Source Voltage	-30		V
V_{GS}	Gate-Source Voltage	± 20		V
$I_D@T_C=25^{\circ}C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-60		A
$I_D@T_C=100^{\circ}C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-50		A
$I_D@T_A=25^{\circ}C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-15	-10	A
$I_D@T_A=70^{\circ}C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-12	-8.2	A
I_{DM}	Pulsed Drain Current ²	-140		A
EAS	Single Pulse Avalanche Energy ³	273		mJ
I_{AS}	Avalanche Current	-50		A
$P_D@T_C=25^{\circ}C$	Total Power Dissipation ⁴	45		W
$P_D@T_A=25^{\circ}C$	Total Power Dissipation ⁴	5	3.5	W
T_{STG}	Storage Temperature Range	-55 to 150		$^{\circ}C$
T_J	Operating Junction Temperature Range	-55 to 150		$^{\circ}C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	62	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹ (t ≤ 10s)	---	25	$^{\circ}C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	2.8	$^{\circ}C/W$

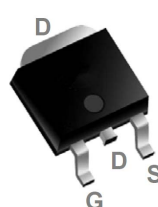
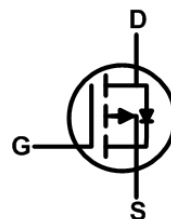
Product Summary

BVDSS	RDSON	ID
-30V	12mΩ	-60A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

TO-252 Pin Configuration



Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=-1mA$	---	-0.0232	---	V/ $^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=-10V, I_D=-30A$	---	12	15	m Ω
		$V_{GS}=-4.5V, I_D=-15A$	---	20	25	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.0	-1.5	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	4.6	---	mV/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-24V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-24V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	-5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=-5V, I_D=-30A$	---	30	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	9	18	Ω
Q_g	Total Gate Charge (-4.5V)	$V_{DS}=-15V, V_{GS}=-4.5V, I_D=-15A$	---	22	30.8	nC
Q_{gs}	Gate-Source Charge		---	8.7	12.2	
Q_{gd}	Gate-Drain Charge		---	7.2	10	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-15V, V_{GS}=-10V, R_G=3.3\Omega$ $I_D=-15A$	---	8	16	ns
T_r	Rise Time		---	73.7	132	
$T_{d(off)}$	Turn-Off Delay Time		---	61.8	123	
T_f	Fall Time		---	24.4	48	
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V, f=1MHz$	---	2215	3100	pF
C_{oss}	Output Capacitance		---	310	434	
C_{rss}	Reverse Transfer Capacitance		---	237	330	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	$V_{DD}=-25V, L=0.1mH, I_{AS}=-24A$	63	---	---	mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,6}	$V_G=V_D=0V$, Force Current	---	---	-60	A
I_{SM}	Pulsed Source Current ^{2,6}		---	---	-140	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=-1A, T_J=25^\circ\text{C}$	---	---	-1	V
t_{rr}	Reverse Recovery Time	$I_F=-15A, dI/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	19	---	nS
Q_{rr}	Reverse Recovery Charge		---	9	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, $t < 10\text{sec}$.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-24A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

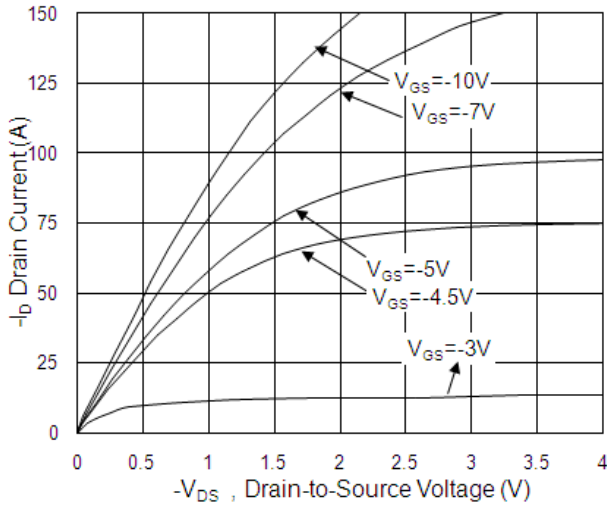


Fig.1 Typical Output Characteristics

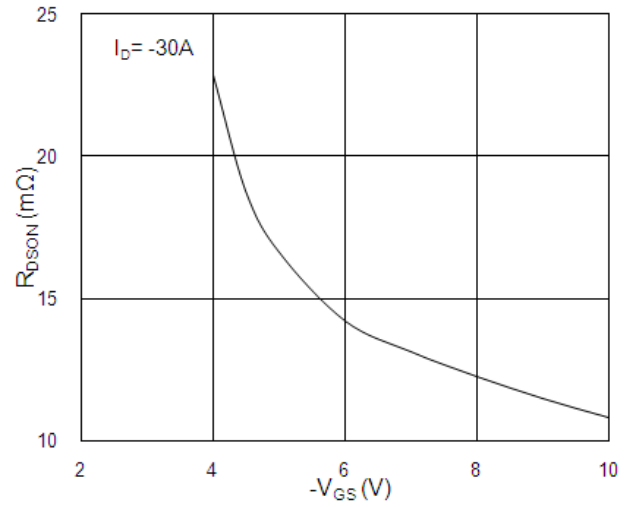


Fig.2 On-Resistance vs. G-S Voltage

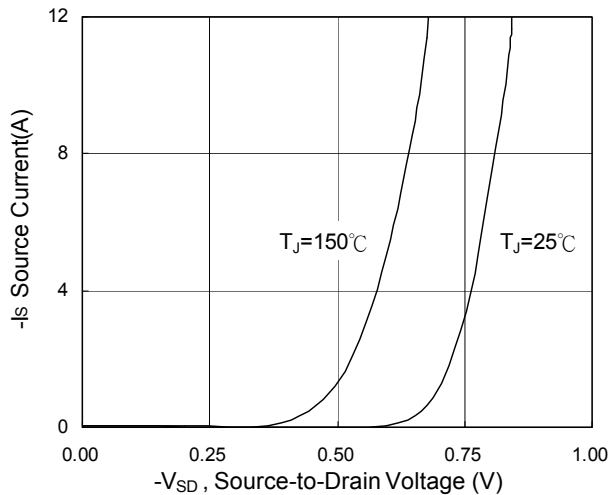


Fig.3 Forward Characteristics of Reverse

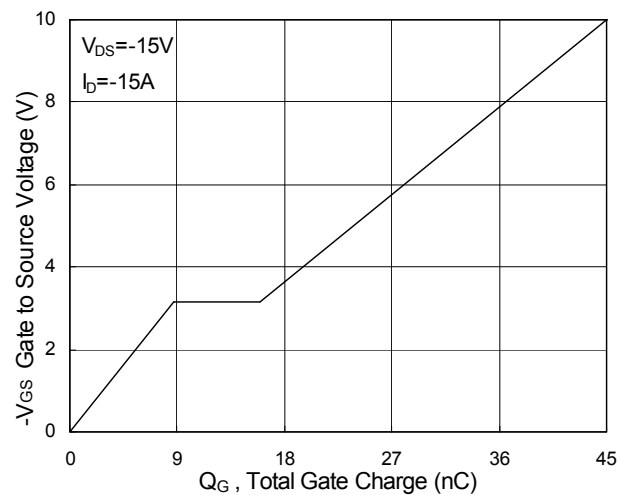


Fig.4 Gate-charge Characteristics

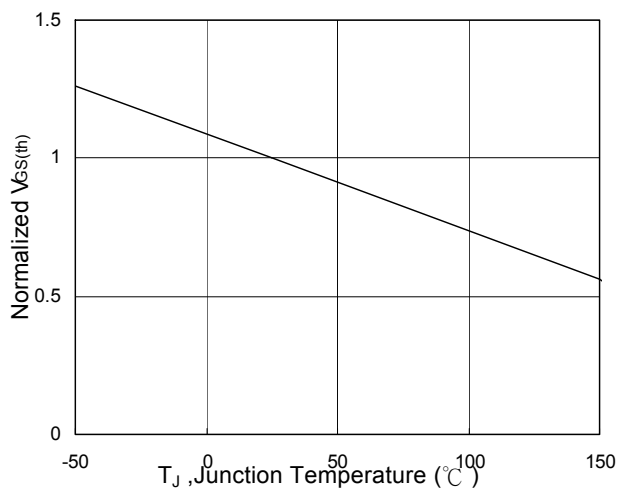


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

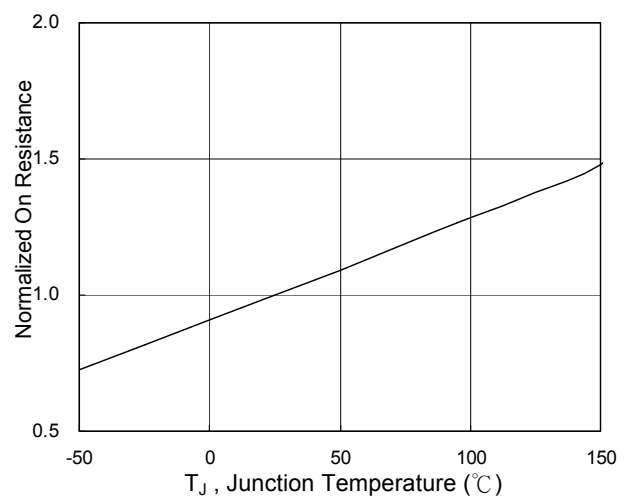
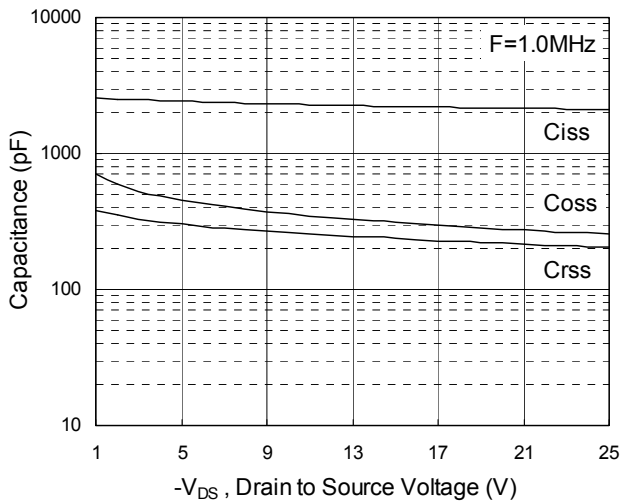
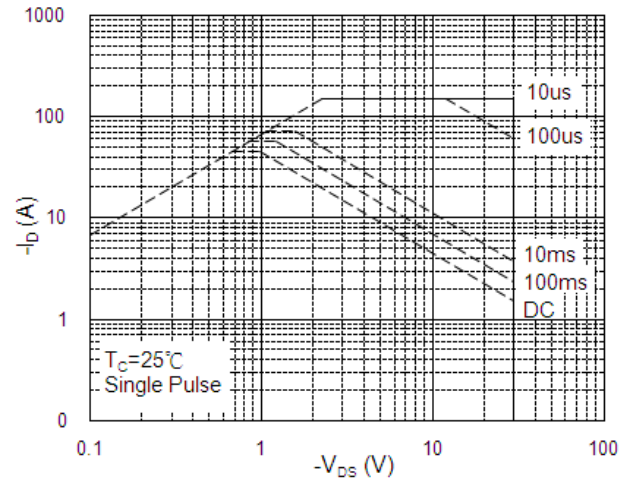
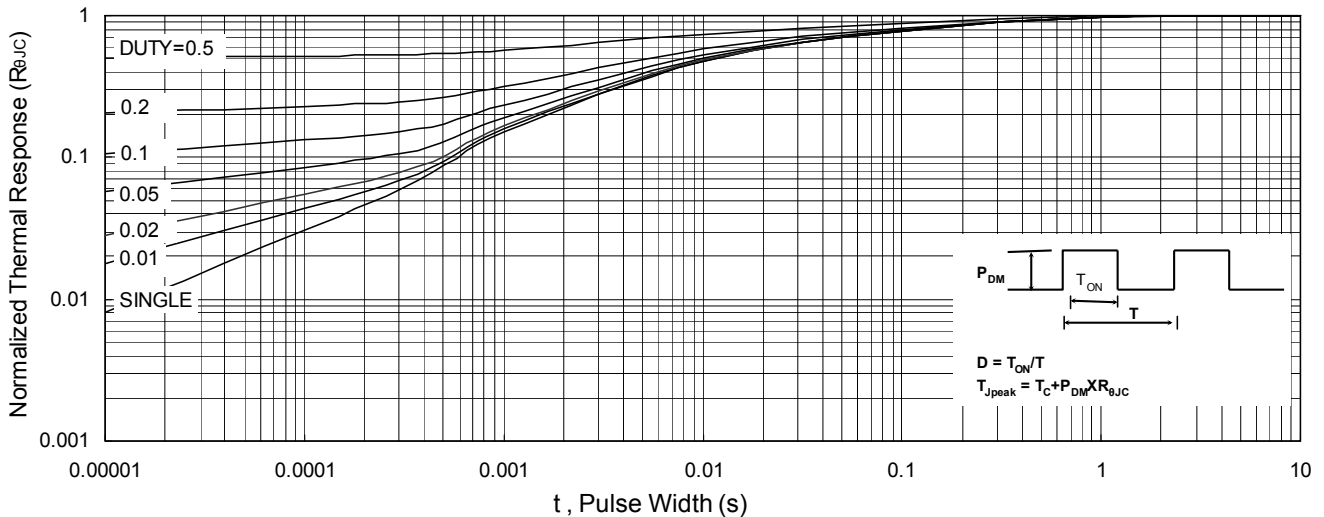
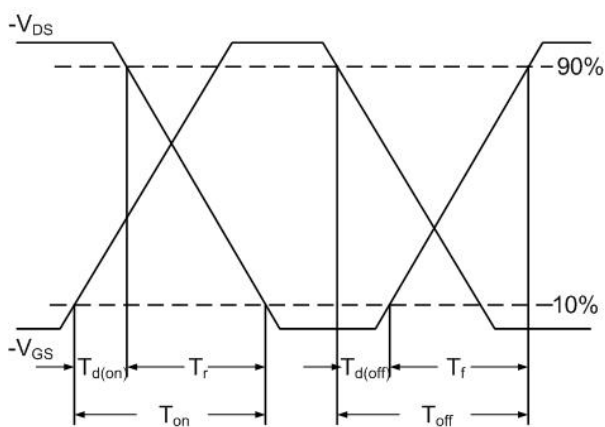
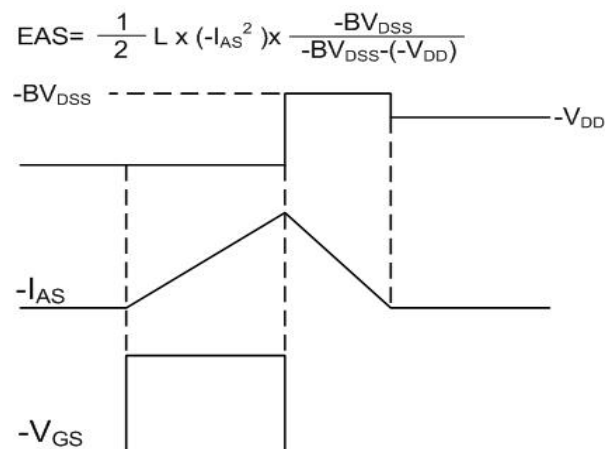


Fig.6 Normalized $R_{DS(on)}$ vs. T_J



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