

# High ESD Protected, +125°C, 40Mbps, 3.3V, Full Fail-Safe, RS-485/RS-422 Transceivers

## ISL3179E, ISL3180E

Intersil's [ISL3179E](#) and [ISL3180E](#) are high ESD Protected (see [Table 2](#)), 3.3V powered, single transceivers that meet both the RS-485 and RS-422 standards for balanced communication. Each device has low bus currents (+220μA/-150μA), so it presents a "1/5 unit load" to the RS-485 bus. This allows up to 160 transceivers on the network without violating the RS-485 specification's 32 unit load maximum, and without using repeaters.

Receiver (Rx) inputs feature a "full fail-safe" design, which ensures a logic high Rx output if Rx inputs are floating, shorted, or terminated but undriven.

The ISL3180E is configured for full duplex applications. The ISL3179E half duplex version multiplexes the Rx inputs and Tx outputs to allow a transceiver with an output disable function in 8 Ld packages.

Hot plug circuitry ensures that the Tx and Rx outputs remain in a high impedance state while the power supply stabilizes.

## Applications

- Motor controller/position encoder systems
- Factory automation
- Field bus networks
- Security networks
- Building environmental control systems
- Industrial/process control networks

## Features

- High ESD protection on RS-485 I/O pins
  - ISL3179E. . . . . ±16.5kV IEC61000
  - ISL3180E . . . . . ±12kV HBM
  - Class 3 HBM level on all other pins (ISL3179E) . . . . . >9kV
- Specified for +125°C operation
- High data rates. . . . . up to 40Mbps
- 5V tolerant logic inputs
- 1/5 unit load allows up to 160 devices on the bus
- Full fail-safe (open, shorted, terminated/undriven) receiver
- Hot plug - Tx and Rx outputs remain three-state during power-up
- Low quiescent current. . . . . 4mA (max)
- Low current shutdown mode . . . . . 1μA (max)
- -7V to +12V common-mode input voltage range
- Three-state Rx and Tx outputs
- 16/16.5ns (max) Tx/Rx propagation delays; 1.5ns (max) skew
- Operates from a single +3.3V supply (10% tolerance)
- Current limiting and thermal shutdown for driver overload protection
- Pb-free (RoHS compliant)

TABLE 1. KEY DIFFERENCES BETWEEN HIGH-SPEED INTERFACE FAMILY OF PARTS

| PART NUMBER | FULL/HALF DUPLEX | VCC (V) | VOD (V) | DATA RATE (Mbps) |
|-------------|------------------|---------|---------|------------------|
| ISL3179E    | Half             | 3.3     | 1.5     | 40               |
| ISL3180E    | Full             | 3.3     | 1.5     | 40               |
| ISL3159E    | Half             | 5       | 2.1     | 40               |
| ISL3259E    | Half             | 5       | 2.1     | 100              |

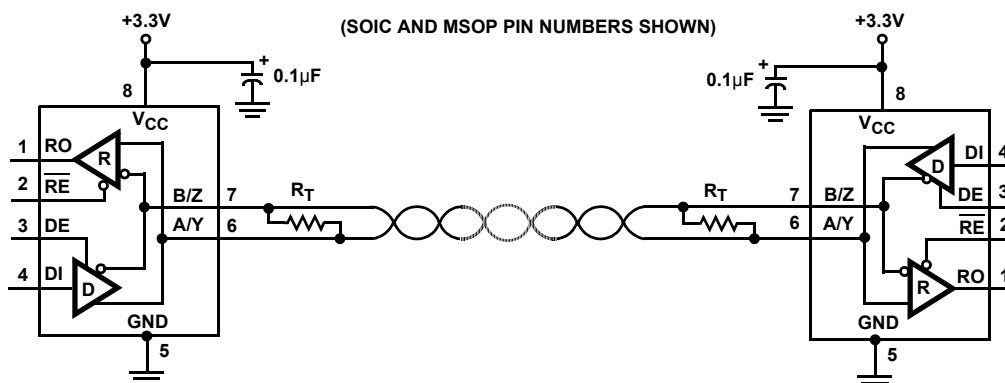


FIGURE 1. TYPICAL OPERATING CIRCUIT - ISL3179E

# ISL3179E, ISL3180E

## Ordering Information

| PART NUMBER<br>( <a href="#">Note 1</a> , <a href="#">2</a> , <a href="#">3</a> ) | PART MARKING | TEMP. RANGE<br>(°C) | PACKAGE<br>(RoHS Compliant) | PKG. DWG. # |
|-----------------------------------------------------------------------------------|--------------|---------------------|-----------------------------|-------------|
| ISL3179EFBZ                                                                       | 3179 EFBZ    | -40 to +125         | 8 Ld SOIC                   | M8.15       |
| ISL3179EFUZ                                                                       | 179FZ        | -40 to +125         | 8 Ld MSOP                   | M8.118      |
| ISL3179EFRZ                                                                       | 79FZ         | -40 to +125         | 10 Ld DFN                   | L10.3x3C    |
| ISL3179EIBZ                                                                       | 3179 EIBZ    | -40 to +85          | 8 Ld SOIC                   | M8.15       |
| ISL3179EIUZ                                                                       | 179IZ        | -40 to +85          | 8 Ld MSOP                   | M8.118      |
| ISL3179EIRZ                                                                       | 79IZ         | -40 to +85          | 10 Ld DFN                   | L10.3x3C    |
| ISL3180EIBZ                                                                       | ISL3180 EIBZ | -40 to +85          | 14 Ld SOIC                  | M14.15      |

### NOTES:

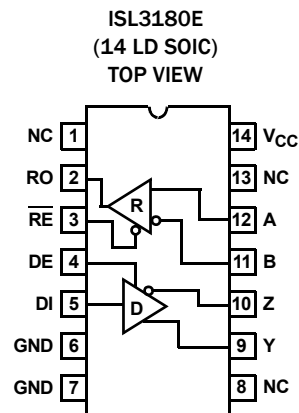
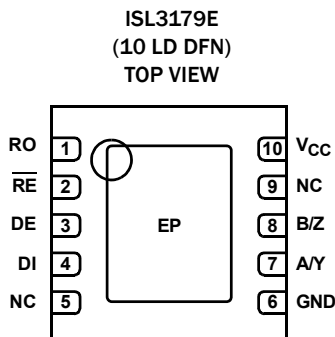
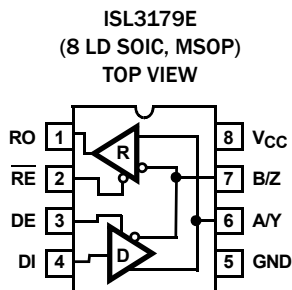
1. Add "-T\*" suffix for tape and reel. Please refer to [TB347](#) for details on reel specifications.
2. Intersil Pb-free plastic packaged products employ special Pb-free material sets, molding compounds/die attach materials, and 100% matte tin plate plus anneal (e3 termination finish, which is RoHS compliant and compatible with both SnPb and Pb-free soldering operations). Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.
3. For Moisture Sensitivity Level (MSL), please see device information page for [ISL3179E](#), [ISL3180E](#). For more information on MSL please see techbrief [TB363](#).

TABLE 2. SUMMARY OF FEATURES

| PART NUMBER | HALF/FULL DUPLEX | DATA RATE (Mbps) | RS-485 PIN ESD LEVEL | HOT PLUG? | RX/TX ENABLE? | QUIESCENT I <sub>CC</sub> (mA) | LOW POWER SHUTDOWN? | PIN COUNT |
|-------------|------------------|------------------|----------------------|-----------|---------------|--------------------------------|---------------------|-----------|
| ISL3179E    | HALF             | 40               | 16.5kV IEC61000      | YES       | YES           | 2.6                            | YES                 | 8, 10     |
| ISL3180E    | FULL             | 40               | 12kV HBM             | YES       | YES           | 2.6                            | YES                 | 14        |

# ISL3179E, ISL3180E

## Pin Configurations



## Truth Table

| TRANSMITTING    |    |    |         |         |
|-----------------|----|----|---------|---------|
| INPUTS          |    |    | OUTPUTS |         |
| $\overline{RE}$ | DE | DI | B/Z     | A/Y     |
| X               | 1  | 1  | 0       | 1       |
| X               | 1  | 0  | 1       | 0       |
| 0               | 0  | X  | High-Z  | High-Z  |
| 1               | 0  | X  | High-Z* | High-Z* |

NOTE: \*Shutdown Mode

## Truth Table

| RECEIVING       |    |                     |         |
|-----------------|----|---------------------|---------|
| INPUTS          |    |                     | OUTPUT  |
| $\overline{RE}$ | DE | A-B                 | RO      |
| 0               | 0  | $\geq -0.05V$       | 1       |
| 0               | 0  | $\leq -0.2V$        | 0       |
| 0               | 0  | Inputs Open/Shorted | 1       |
| 1               | 1  | X                   | High-Z  |
| 1               | 0  | X                   | High-Z* |

NOTE: \*Shutdown Mode

## Pin Descriptions

| PIN             | FUNCTION                                                                                                                                                                                                                                   |
|-----------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| RO              | Receiver output: If $A-B \geq -50mV$ , RO is high; If $A-B \leq -200mV$ , RO is low; RO = High if A and B are unconnected (floating) or shorted, or connected to a terminated bus that is undriven.                                        |
| $\overline{RE}$ | Receiver output enable. RO is enabled when $\overline{RE}$ is low; RO is high impedance when $\overline{RE}$ is high. If the Rx enable function isn't required, connect $\overline{RE}$ directly to GND.                                   |
| DE              | Driver output enable. The driver outputs, Y and Z, are enabled by bringing DE high, and they are high impedance when DE is low. If the Tx enable function isn't required, connect DE to $V_{CC}$ through a $1k\Omega$ or greater resistor. |
| DI              | Driver input. A low on DI forces output Y low and output Z high. Similarly, a high on DI forces output Y high and output Z low.                                                                                                            |
| GND             | Ground connection. This is also the potential of the DFN's exposed metal pad.                                                                                                                                                              |
| A/Y             | $\pm 16.5kV$ IEC61000 ESD protected RS-485/RS-422 level, noninverting receiver input and noninverting driver output. Pin is an input (A) if DE = 0; pin is an output (Y) if DE = 1. ISL3179E only.                                         |
| B/Z             | $\pm 16.5kV$ IEC61000 ESD protected RS-485/RS-422 level, inverting receiver input and inverting driver output. Pin is an input (B) if DE = 0; pin is an output (Z) if DE = 1. ISL3179E only.                                               |
| A               | $\pm 12kV$ HBM ESD protected RS-485/RS-422 level, noninverting receiver input. ISL3180E only.                                                                                                                                              |
| B               | $\pm 12kV$ HBM ESD protected RS-485/RS-422 level, inverting receiver input. ISL3180E only.                                                                                                                                                 |
| Y               | $\pm 12kV$ HBM ESD protected RS-485/RS-422 level, noninverting driver output. ISL3180E only.                                                                                                                                               |
| Z               | $\pm 12kV$ HBM ESD protected RS-485/RS-422 level, inverting driver output. ISL3180E only.                                                                                                                                                  |
| $V_{CC}$        | System power supply input (3.0V to 3.6V).                                                                                                                                                                                                  |
| NC              | No internal connection.                                                                                                                                                                                                                    |
| EP              | The exposed metal pad on the bottom of the DFN; connect to GND.                                                                                                                                                                            |

## Typical Operating Circuits

(SOIC AND MSOP PIN NUMBERS SHOWN)

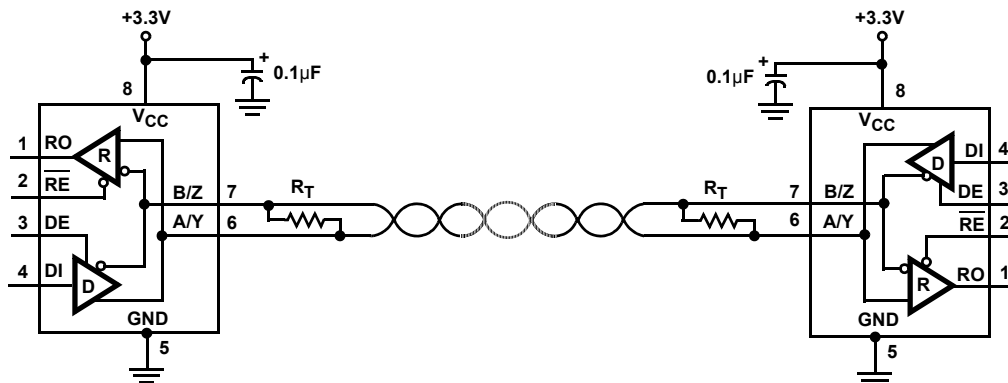


FIGURE 2. ISL3179E

(PIN NUMBERS FOR SOIC)

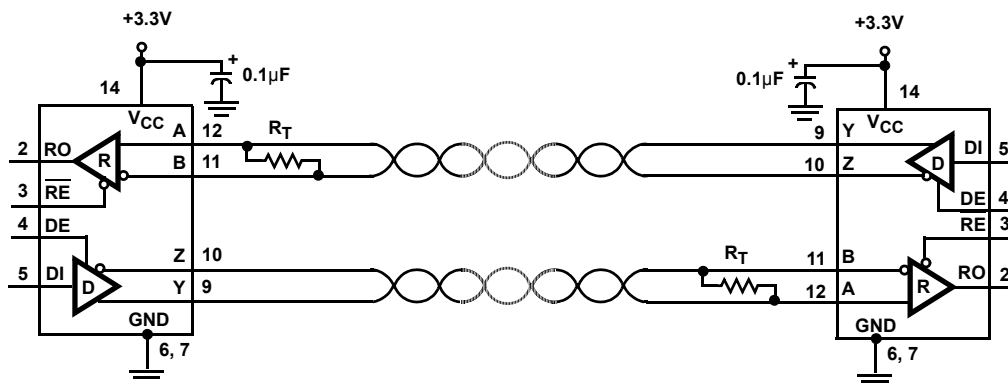


FIGURE 3. ISL3180E

# ISL3179E, ISL3180E

## Absolute Maximum Ratings

|                           |                                                               |
|---------------------------|---------------------------------------------------------------|
| V <sub>CC</sub> to Ground | 7V                                                            |
| Input Voltages            |                                                               |
| DI, DE, $\overline{RE}$   | -0.3V to 7V                                                   |
| Input/Output Voltages     |                                                               |
| A, B, Y, Z, A/Y, B/Z      | -9V to +13V                                                   |
| RO                        | -0.3V to (V <sub>CC</sub> + 0.3V)                             |
| Short-circuit Duration    |                                                               |
| Y, Z                      | Continuous                                                    |
| ESD Rating                | see Electrical Specifications table on <a href="#">page 6</a> |

## Thermal Information

| Thermal Resistance (Typical)                     | $\theta_{JA}$ (°C/W)      | $\theta_{JC}$ (°C/W) |
|--------------------------------------------------|---------------------------|----------------------|
| 8 Ld SOIC Package ( <a href="#">Note 4</a> )     | 160                       | N/A                  |
| 14 Ld SOIC Package ( <a href="#">Note 4</a> )    | 91                        | N/A                  |
| 8 Ld MSOP Package ( <a href="#">Note 4</a> )     | 132.5                     | N/A                  |
| 10 Ld DFN Package ( <a href="#">Notes 5, 6</a> ) | 46                        | 3.5                  |
| Maximum Junction Temperature (Plastic Package)   | +150°C                    |                      |
| Maximum Storage Temperature Range                | -65°C to +150°C           |                      |
| Pb-free Reflow Profile                           | see <a href="#">TB493</a> |                      |

## Operating Conditions

|                      |                 |
|----------------------|-----------------|
| Temperature Range    |                 |
| ISL3179EF            | -40°C to +125°C |
| ISL3179EI, ISL3180EI | -40°C to +85°C  |

**CAUTION:** Do not operate at or near the maximum ratings listed for extended periods of time. Exposure to such conditions may adversely impact product reliability and result in failures not covered by warranty.

### NOTES:

- $\theta_{JA}$  is measured with the component mounted on a high effective thermal conductivity test board in free air. See Tech Brief [TB379](#) for details.
- For  $\theta_{JC}$ , the “case temp” location is the center of the exposed metal pad on the package underside.
- $\theta_{JA}$  is measured in free air with the component mounted on a high effective thermal conductivity test board with “direct attach” features. See Tech Brief [TB379](#)

## Electrical Specifications

Test Conditions: V<sub>CC</sub> = 3.0V to 3.6V; Typicals are at V<sub>CC</sub> = 3.3V, T<sub>A</sub> = +25°C. **Boldface limits apply across the operating temperature range.** ([Note 7](#))

| PARAMETER                                                                                   | SYMBOL            | TEST CONDITIONS                                                                                              | TEMP (°C) | MIN ( <a href="#">Note 17</a> ) | TYP  | MAX ( <a href="#">Note 17</a> ) | UNIT |
|---------------------------------------------------------------------------------------------|-------------------|--------------------------------------------------------------------------------------------------------------|-----------|---------------------------------|------|---------------------------------|------|
| <b>DC CHARACTERISTICS</b>                                                                   |                   |                                                                                                              |           |                                 |      |                                 |      |
| Driver Differential V <sub>OUT</sub>                                                        | V <sub>OD</sub>   | R <sub>L</sub> = 100Ω (RS-422) ( <a href="#">Figure 4A</a> ), ( <a href="#">Note 16</a> )                    | Full      | <b>2</b>                        | 2.3  | -                               | V    |
|                                                                                             |                   | R <sub>L</sub> = 54Ω (RS-485) ( <a href="#">Figure 4A</a> )                                                  | Full      | <b>1.5</b>                      | 2.1  | <b>V<sub>CC</sub></b>           | V    |
|                                                                                             |                   | No Load                                                                                                      | Full      | -                               | -    | <b>V<sub>CC</sub></b>           |      |
|                                                                                             |                   | R <sub>L</sub> = 60Ω, -7V ≤ V <sub>CM</sub> ≤ 12V ( <a href="#">Figure 4B</a> ), ( <a href="#">Note 16</a> ) | Full      | <b>1.5</b>                      | 2    | -                               | V    |
| Change in Magnitude of Driver Differential V <sub>OUT</sub> for Complementary Output States | ΔV <sub>OD</sub>  | R <sub>L</sub> = 54Ω or 100Ω ( <a href="#">Figure 4A</a> )                                                   | Full      | -                               | 0.01 | <b>0.2</b>                      | V    |
| Driver Common-mode V <sub>OUT</sub>                                                         | V <sub>OC</sub>   | R <sub>L</sub> = 54Ω or 100Ω ( <a href="#">Figure 4A</a> )                                                   | Full      | -                               | 2    | <b>2.5</b>                      | V    |
| Change in Magnitude of Driver Common-mode V <sub>OUT</sub> for Complementary Output States  | ΔV <sub>OC</sub>  | R <sub>L</sub> = 54Ω or 100Ω ( <a href="#">Figure 4A</a> )                                                   | Full      | -                               | 0.02 | <b>0.2</b>                      | V    |
| Logic Input High Voltage                                                                    | V <sub>IH</sub>   | DI, DE, $\overline{RE}$                                                                                      | Full      | <b>2</b>                        | -    | -                               | V    |
| Logic Input Low Voltage                                                                     | V <sub>IL</sub>   | DI, DE, $\overline{RE}$                                                                                      | Full      | -                               | -    | <b>0.8</b>                      | V    |
| Logic Input Current                                                                         | I <sub>IN1</sub>  | DI = DE = $\overline{RE}$ = 0V or V <sub>CC</sub>                                                            | Full      | <b>-2</b>                       | -    | <b>2</b>                        | μA   |
| Input Current (A, B, A/Y, B/Z)                                                              | I <sub>IN2</sub>  | DE = 0V, V <sub>CC</sub> = 0V or 3.6V                                                                        |           |                                 |      |                                 |      |
|                                                                                             |                   | V <sub>IN</sub> = 12V                                                                                        | Full      | -                               | -    | <b>220</b>                      | μA   |
|                                                                                             |                   | V <sub>IN</sub> = -7V                                                                                        | Full      | <b>-160</b>                     | -    | -                               | μA   |
| Y or Z Output Leakage Current                                                               | I <sub>OZ</sub>   | DE = 0V, -7V ≤ V <sub>Y</sub> or V <sub>Z</sub> ≤ 12V, ISL3180E Only                                         | Full      | <b>-40</b>                      | -    | <b>40</b>                       | μA   |
| Driver Short-circuit Current, V <sub>O</sub> = High or Low                                  | I <sub>OSD1</sub> | DE = V <sub>CC</sub> , -7V ≤ V <sub>Y</sub> or V <sub>Z</sub> ≤ 12V ( <a href="#">Note 9</a> )               | Full      | -                               | -    | <b>±250</b>                     | mA   |
| Receiver Differential Threshold Voltage                                                     | V <sub>TH</sub>   | -7V ≤ V <sub>CM</sub> ≤ 12V                                                                                  | Full      | <b>-200</b>                     | -    | <b>-50</b>                      | mV   |
| Receiver Input Hysteresis                                                                   | ΔV <sub>TH</sub>  | V <sub>CM</sub> = 0V                                                                                         | 25        | -                               | 28   | -                               | mV   |
| Receiver Output High Voltage                                                                | V <sub>OH</sub>   | I <sub>O</sub> = -12mA, V <sub>ID</sub> = -50mV                                                              | Full      | <b>V<sub>CC</sub> - 0.5</b>     | -    | -                               | V    |

# ISL3179E, ISL3180E

**Electrical Specifications** Test Conditions:  $V_{CC} = 3.0V$  to  $3.6V$ ; Typicals are at  $V_{CC} = 3.3V$ ,  $T_A = +25^\circ C$ . **Boldface limits apply across the operating temperature range. (Note 7) (Continued)**

| PARAMETER                                               | SYMBOL                              | TEST CONDITIONS                                                                                                                   | TEMP<br>(°C) | MIN<br>( <a href="#">Note 17</a> ) | TYP   | MAX<br>( <a href="#">Note 17</a> ) | UNIT |
|---------------------------------------------------------|-------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------|--------------|------------------------------------|-------|------------------------------------|------|
| Receiver Output Low Voltage                             | V <sub>OL</sub>                     | I <sub>O</sub> = +10mA, V <sub>ID</sub> = -200mV                                                                                  | Full         | -                                  | -     | <b>0.4</b>                         | V    |
| Receiver Output Low Current                             | I <sub>OL</sub>                     | V <sub>OL</sub> = 1V, V <sub>ID</sub> = -200mV                                                                                    | Full         | <b>25</b>                          | -     | -                                  | mA   |
| Three-state (high impedance)<br>Receiver Output Current | I <sub>OZR</sub>                    | 0.4V ≤ V <sub>O</sub> ≤ 2.4V                                                                                                      | Full         | <b>-1</b>                          | 0.015 | <b>1</b>                           | μA   |
| Receiver Input Resistance                               | R <sub>IN</sub>                     | -7V ≤ V <sub>CM</sub> ≤ 12V                                                                                                       | Full         | <b>54</b>                          | 80    | -                                  | kΩ   |
| Receiver Short-circuit Current                          | I <sub>OSR</sub>                    | 0V ≤ V <sub>O</sub> ≤ V <sub>CC</sub>                                                                                             | Full         | <b>±20</b>                         | -     | <b>±110</b>                        | mA   |
| SUPPLY CURRENT                                          |                                     |                                                                                                                                   |              |                                    |       |                                    |      |
| No-load Supply Current ( <a href="#">Note 8</a> )       | I <sub>CC</sub>                     | DI = DE = 0V or V <sub>CC</sub>                                                                                                   | Full         | -                                  | 2.6   | <b>4</b>                           | mA   |
| Shutdown Supply Current                                 | I <sub>SHDN</sub>                   | DE = 0V, $\overline{RE}$ = V <sub>CC</sub> , DI = 0V or V <sub>CC</sub>                                                           | Full         | -                                  | 0.05  | <b>1</b>                           | μA   |
| ESD PERFORMANCE                                         |                                     |                                                                                                                                   |              |                                    |       |                                    |      |
| RS-485 Pins (A/Y, B/Z)<br>ISL3179E Only                 |                                     | IEC61000-4-2, Air-gap Discharge Method                                                                                            | 25           | -                                  | ±16.5 | -                                  | kV   |
|                                                         |                                     | IEC61000-4-2, Contact Discharge Method                                                                                            | 25           | -                                  | ±9    | -                                  | kV   |
|                                                         |                                     | Human Body Model, from bus pins to GND                                                                                            | 25           | -                                  | ±16.5 | -                                  | kV   |
| All Pins<br>ISL3179E Only                               |                                     | Human Body Model, per JEDEC                                                                                                       | 25           | -                                  | >±9   | -                                  | kV   |
|                                                         |                                     | Machine Model, per JEDEC                                                                                                          | 25           | -                                  | >±400 | -                                  | V    |
| RS-485 Pins (A, B, Y, Z)<br>ISL3180E Only               |                                     | IEC61000-4-2, Air-gap Discharge Method                                                                                            | 25           | -                                  | ±4    | -                                  | kV   |
|                                                         |                                     | IEC61000-4-2, Contact Discharge Method                                                                                            | 25           | -                                  | ±5    | -                                  | kV   |
|                                                         |                                     | Human Body Model, from bus pins to GND                                                                                            | 25           | -                                  | ±12   | -                                  | kV   |
| All Pins<br>ISL3180E Only                               |                                     | Human Body Model, per JEDEC                                                                                                       | 25           | -                                  | ±3    | -                                  | kV   |
|                                                         |                                     | Machine Model, per JEDEC                                                                                                          | 25           | -                                  | ±150  | -                                  | V    |
| DRIVER SWITCHING CHARACTERISTICS                        |                                     |                                                                                                                                   |              |                                    |       |                                    |      |
| Maximum Data Rate                                       | f <sub>MAX</sub>                    | V <sub>OD</sub> ≥ ±1.5V, R <sub>D</sub> = 54Ω, C <sub>L</sub> = 100pF ( <a href="#">Figure 7</a> )                                | Full         | <b>40</b>                          | 60    | -                                  | Mbps |
| Driver Differential Output Delay                        | t <sub>DD</sub>                     | R <sub>D</sub> = 54Ω, C <sub>D</sub> = 50pF ( <a href="#">Figure 5</a> )                                                          | Full         | -                                  | 11    | <b>16</b>                          | ns   |
| Prop Delay Part-to-part Skew                            | t <sub>SKP-P</sub>                  | R <sub>D</sub> = 54Ω, C <sub>D</sub> = 50pF ( <a href="#">Figure 5</a> ), ( <a href="#">Note 15</a> )                             | Full         | -                                  | -     | <b>4</b>                           | ns   |
| Driver Differential Output Skew                         | t <sub>SKEW</sub>                   | R <sub>D</sub> = 54Ω, C <sub>D</sub> = 50pF ( <a href="#">Figure 5</a> )                                                          | Full         | -                                  | 0     | <b>1.5</b>                         | ns   |
| Driver Differential Rise or Fall Time                   | t <sub>R</sub> , t <sub>F</sub>     | R <sub>D</sub> = 54Ω, C <sub>D</sub> = 50pF ( <a href="#">Figure 5</a> )                                                          | Full         | -                                  | 4     | <b>7</b>                           | ns   |
| Driver Enable to Output High                            | t <sub>ZH</sub>                     | R <sub>L</sub> = 110Ω, C <sub>L</sub> = 50pF, SW = GND ( <a href="#">Figure 6</a> ), ( <a href="#">Note 10</a> )                  | Full         | -                                  | 18    | <b>25</b>                          | ns   |
| Driver Enable to Output Low                             | t <sub>ZL</sub>                     | R <sub>L</sub> = 110Ω, C <sub>L</sub> = 50pF, SW = V <sub>CC</sub> ( <a href="#">Figure 6</a> ), ( <a href="#">Note 10</a> )      | Full         | -                                  | 16    | <b>25</b>                          | ns   |
| Driver Disable from Output High                         | t <sub>HZ</sub>                     | R <sub>L</sub> = 110Ω, C <sub>L</sub> = 50pF, SW = GND ( <a href="#">Figure 6</a> )                                               | Full         | -                                  | 15    | <b>25</b>                          | ns   |
| Driver Disable from Output Low                          | t <sub>LZ</sub>                     | R <sub>L</sub> = 110Ω, C <sub>L</sub> = 50pF, SW = V <sub>CC</sub> ( <a href="#">Figure 6</a> )                                   | Full         | -                                  | 18    | <b>25</b>                          | ns   |
| Time to Shutdown                                        | t <sub>SHDN</sub>                   | ( <a href="#">Note 12</a> )                                                                                                       | Full         | <b>60</b>                          | -     | <b>600</b>                         | ns   |
| Driver Enable from Shutdown to Output High              | t <sub>ZH(SHDN)</sub>               | R <sub>L</sub> = 110Ω, C <sub>L</sub> = 50pF, SW = GND ( <a href="#">Figure 6</a> ), ( <a href="#">Notes 12, 13</a> )             | Full         | -                                  | -     | <b>1000</b>                        | ns   |
| Driver Enable from Shutdown to Output Low               | t <sub>ZL(SHDN)</sub>               | R <sub>L</sub> = 110Ω, C <sub>L</sub> = 50pF, SW = V <sub>CC</sub> ( <a href="#">Figure 6</a> ), ( <a href="#">Notes 12, 13</a> ) | Full         | -                                  | -     | <b>1000</b>                        | ns   |
| RECEIVER SWITCHING CHARACTERISTICS                      |                                     |                                                                                                                                   |              |                                    |       |                                    |      |
| Maximum Data Rate                                       | f <sub>MAX</sub>                    | V <sub>ID</sub> = ±1.5V                                                                                                           | Full         | <b>40</b>                          | 60    | -                                  | Mbps |
| Receiver Input to Output Delay                          | t <sub>PLH</sub> , t <sub>PHL</sub> | <a href="#">Figure 8</a>                                                                                                          | Full         | -                                  | 10    | <b>16.5</b>                        | ns   |
| Prop Delay Part-to-part Skew                            | t <sub>SKP-P</sub>                  | <a href="#">Figure 8, Note 15</a>                                                                                                 | Full         | -                                  | -     | <b>4</b>                           | ns   |

# ISL3179E, ISL3180E

**Electrical Specifications** Test Conditions:  $V_{CC} = 3.0V$  to  $3.6V$ ; Typicals are at  $V_{CC} = 3.3V$ ,  $T_A = +25^\circ C$ . **Boldface limits apply across the operating temperature range. (Note 7) (Continued)**

| PARAMETER                                    | SYMBOL         | TEST CONDITIONS                                                            | TEMP<br>( $^\circ C$ ) | MIN<br>(Note 17) | TYP | MAX<br>(Note 17) | UNIT |
|----------------------------------------------|----------------|----------------------------------------------------------------------------|------------------------|------------------|-----|------------------|------|
| Receiver Skew   $t_{PLH} - t_{PHL}$          | $t_{SKD}$      | Figure 8                                                                   | Full                   | -                | 0   | <b>1.5</b>       | ns   |
| Receiver Enable to Output High               | $t_{ZH}$       | $R_L = 1k\Omega$ , $C_L = 15pF$ , SW = GND (Figure 9), (Note 11)           | Full                   | -                | 10  | <b>15</b>        | ns   |
| Receiver Enable to Output Low                | $t_{ZL}$       | $R_L = 1k\Omega$ , $C_L = 15pF$ , SW = $V_{CC}$ (Figure 9), (Note 11)      | Full                   | -                | 11  | <b>15</b>        | ns   |
| Receiver Disable from Output High            | $t_{HZ}$       | $R_L = 1k\Omega$ , $C_L = 15pF$ , SW = GND (Figure 9)                      | Full                   | -                | 10  | <b>15</b>        | ns   |
| Receiver Disable from Output Low             | $t_{LZ}$       | $R_L = 1k\Omega$ , $C_L = 15pF$ , SW = $V_{CC}$ (Figure 9)                 | Full                   | -                | 10  | <b>15</b>        | ns   |
| Time to Shutdown                             | $t_{SHDN}$     | (Note 12)                                                                  | Full                   | <b>60</b>        | -   | <b>600</b>       | ns   |
| Receiver Enable from Shutdown to Output High | $t_{ZH}(SHDN)$ | $R_L = 1k\Omega$ , $C_L = 15pF$ , SW = GND (Figure 9), (Notes 12, 14)      | Full                   | -                | -   | <b>1000</b>      | ns   |
| Receiver Enable from Shutdown to Output Low  | $t_{ZL}(SHDN)$ | $R_L = 1k\Omega$ , $C_L = 15pF$ , SW = $V_{CC}$ (Figure 9), (Notes 12, 14) | Full                   | -                | -   | <b>1000</b>      | ns   |

## NOTES:

- All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.
- Supply current specification is valid for loaded drivers when  $DE = 0V$ .
- Applies to peak current. See "Typical Performance Curves" on page 9 for more information.
- Because of the shutdown feature, keep  $\overline{RE} = 0$  to prevent the device from entering SHDN.
- Because of the shutdown feature, the  $\overline{RE}$  signal high time must be short enough (typically  $<100ns$ ) to prevent the device from entering SHDN.
- These IC's are put into shutdown by bringing  $\overline{RE}$  high and  $DE$  low. If the inputs are in this state for less than 60ns, the parts are guaranteed not to enter shutdown. If the inputs are in this state for at least 700ns, the parts are guaranteed to have entered shutdown. See "Low Power Shutdown Mode" on page 13.
- Keep  $\overline{RE} = V_{CC}$ , and set the  $DE$  signal low time  $>700ns$  to ensure that the device enters SHDN.
- Set the  $\overline{RE}$  signal high time  $>700ns$  to ensure that the device enters SHDN.
- This is the part-to-part skew between any two units tested with identical test conditions (Temperature,  $V_{CC}$ , etc.).
- $V_{CC} = 3.3V \pm 5\%$
- Compliance to datasheet limits is assured by one or more methods: production test, characterization and/or design.

## Test Circuits and Waveforms

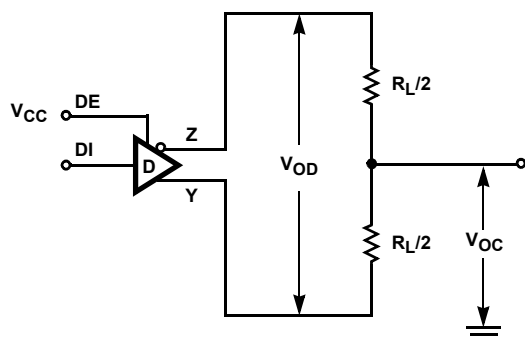


FIGURE 4A.  $V_{OD}$  AND  $V_{OC}$

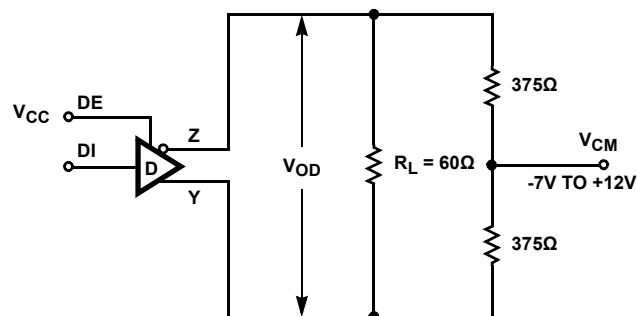


FIGURE 4B.  $V_{OD}$  WITH COMMON-MODE LOAD

FIGURE 4. DC DRIVER TEST CIRCUITS

## Test Circuits and Waveforms (Continued)

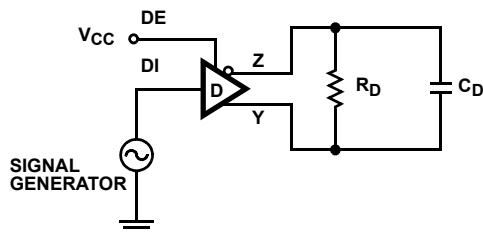


FIGURE 5A. TEST CIRCUIT

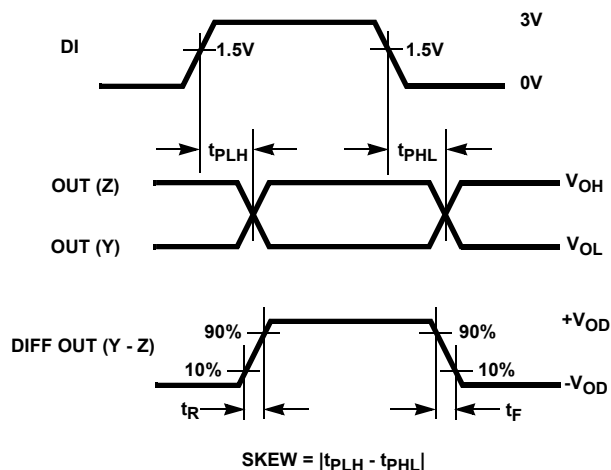
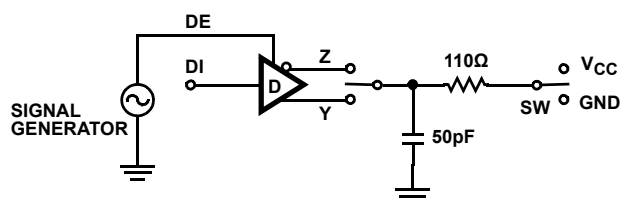


FIGURE 5B. MEASUREMENT POINTS

FIGURE 5. DRIVER PROPAGATION DELAY AND DIFFERENTIAL TRANSITION TIMES



| PARAMETER      | OUTPUT | $\overline{RE}$ | DI  | SW       |
|----------------|--------|-----------------|-----|----------|
| $t_{HZ}$       | Y/Z    | X               | 1/0 | GND      |
| $t_{LZ}$       | Y/Z    | X               | 0/1 | $V_{CC}$ |
| $t_{ZH}$       | Y/Z    | 0 (Note 10)     | 1/0 | GND      |
| $t_{ZL}$       | Y/Z    | 0 (Note 10)     | 0/1 | $V_{CC}$ |
| $t_{ZH(SHDN)}$ | Y/Z    | 1 (Note 13)     | 1/0 | GND      |
| $t_{ZL(SHDN)}$ | Y/Z    | 1 (Note 13)     | 0/1 | $V_{CC}$ |

FIGURE 6A. TEST CIRCUIT

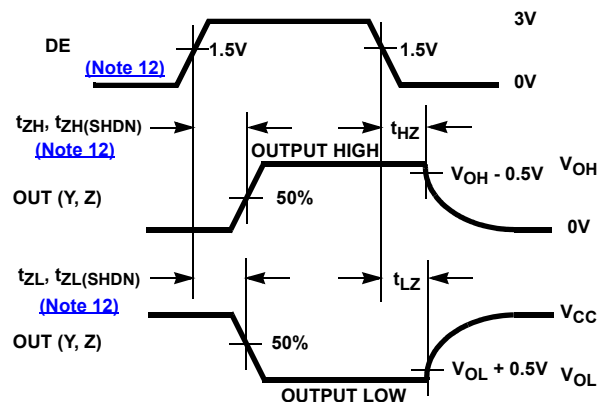


FIGURE 6B. MEASUREMENT POINTS

FIGURE 6. DRIVER ENABLE AND DISABLE TIMES

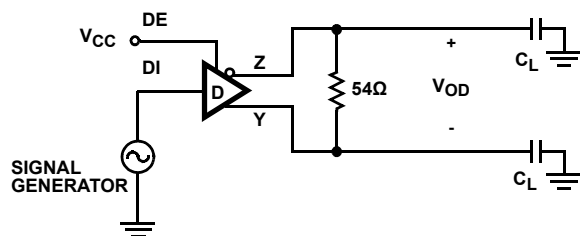


FIGURE 7A. TEST CIRCUIT

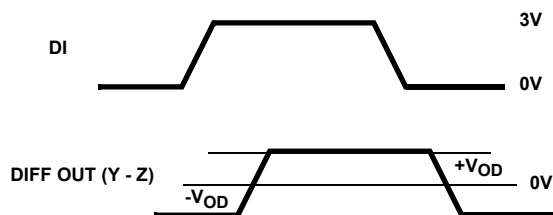


FIGURE 7B. MEASUREMENT POINTS

FIGURE 7. DRIVER DATA RATE

## Test Circuits and Waveforms (Continued)

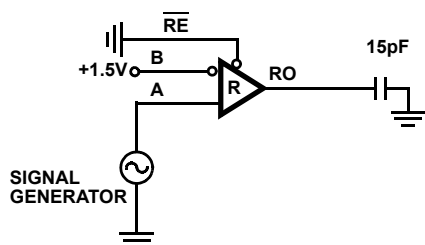


FIGURE 8A. TEST CIRCUIT

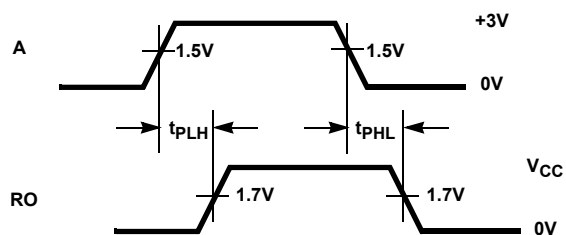
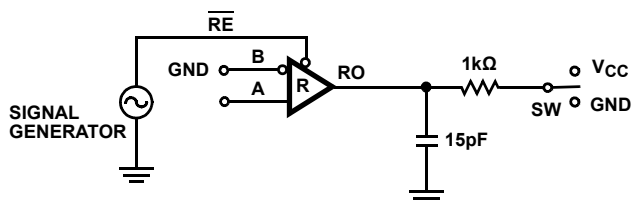


FIGURE 8B. MEASUREMENT POINTS

FIGURE 8. RECEIVER PROPAGATION DELAY



| PARAMETER                | DE | A     | SW       |
|--------------------------|----|-------|----------|
| $t_{HZ}$                 | 0  | +1.5V | GND      |
| $t_{LZ}$                 | 0  | -1.5V | $V_{CC}$ |
| $t_{ZH}$ (Note 11)       | 0  | +1.5V | GND      |
| $t_{ZL}$ (Note 11)       | 0  | -1.5V | $V_{CC}$ |
| $t_{ZH(SHDN)}$ (Note 14) | 0  | +1.5V | GND      |
| $t_{ZL(SHDN)}$ (Note 14) | 0  | -1.5V | $V_{CC}$ |

FIGURE 9A. TEST CIRCUIT

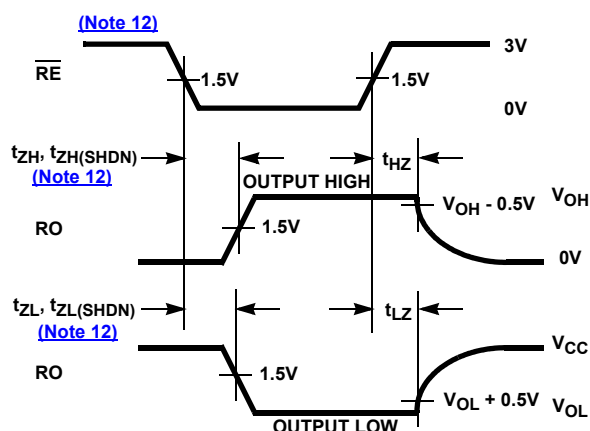


FIGURE 9B. MEASUREMENT POINTS

FIGURE 9. RECEIVER ENABLE AND DISABLE TIMES

## Typical Performance Curves $V_{CC} = 3.3V$ , $T_A = +25^\circ C$ ; Unless Otherwise Specified

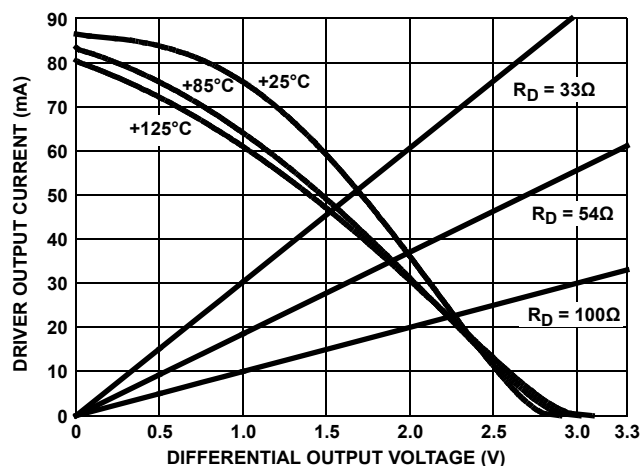


FIGURE 10. DRIVER OUTPUT CURRENT vs DIFFERENTIAL OUTPUT VOLTAGE

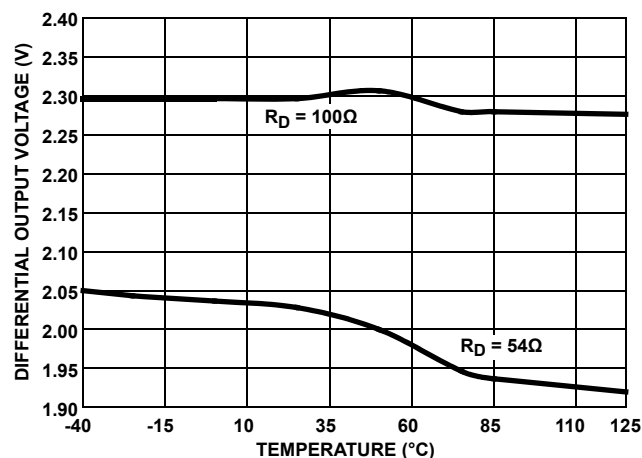


FIGURE 11. DRIVER DIFFERENTIAL OUTPUT VOLTAGE vs TEMPERATURE

## Typical Performance Curves $V_{CC} = 3.3V$ , $T_A = +25^\circ C$ ; Unless Otherwise Specified (Continued)

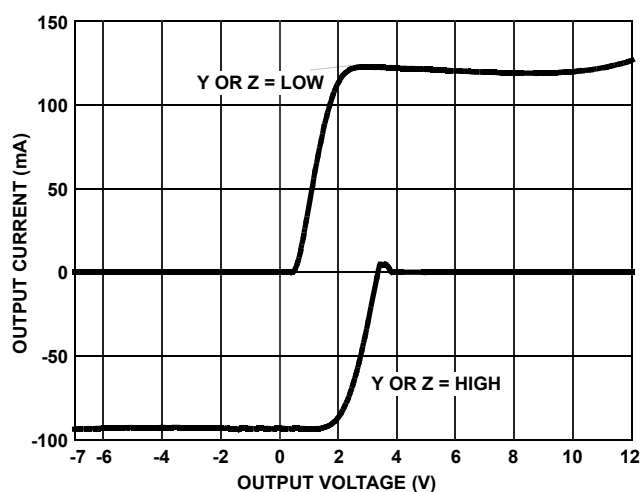


FIGURE 12. DRIVER OUTPUT CURRENT vs SHORT-CIRCUIT VOLTAGE

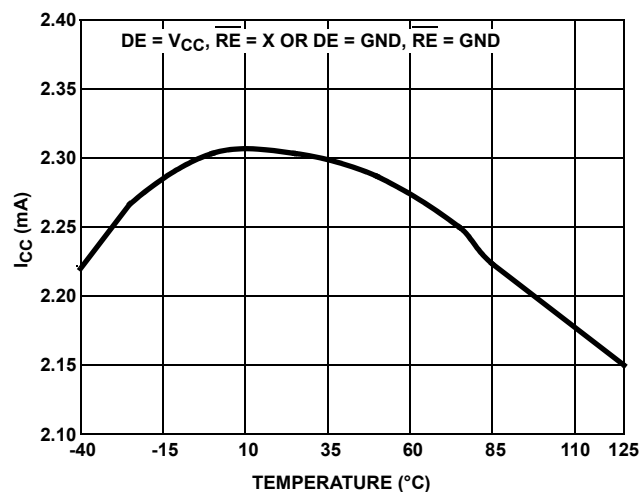


FIGURE 13. SUPPLY CURRENT vs TEMPERATURE

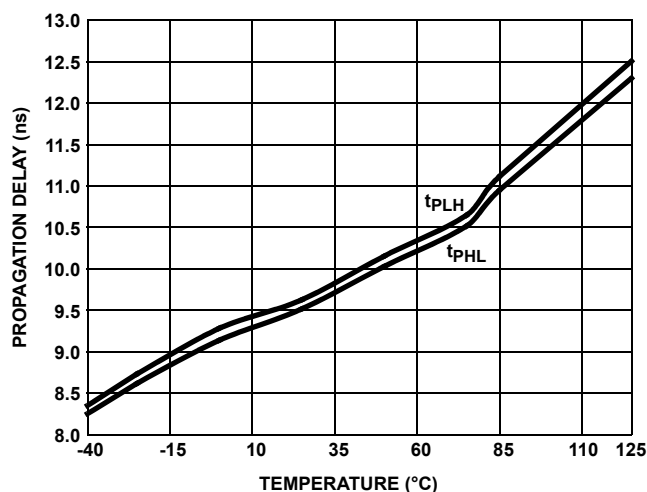


FIGURE 14. DRIVER DIFFERENTIAL PROPAGATION DELAY vs TEMPERATURE

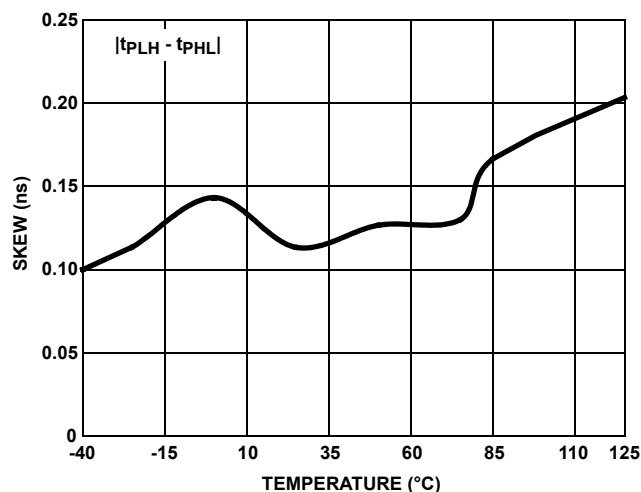


FIGURE 15. DRIVER DIFFERENTIAL SKEW vs TEMPERATURE

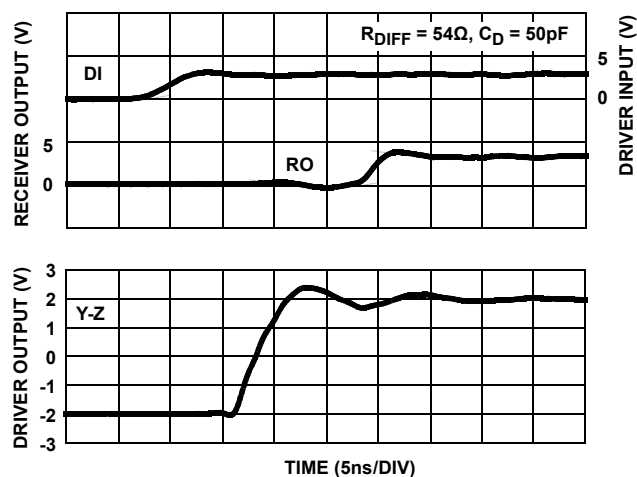


FIGURE 16. DRIVER AND RECEIVER WAVEFORMS

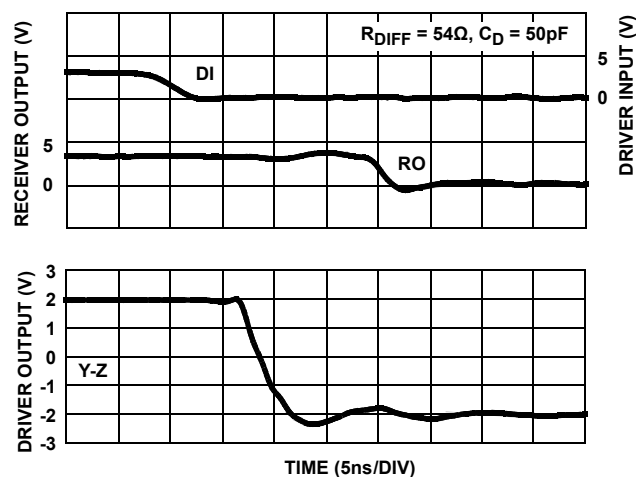


FIGURE 17. DRIVER AND RECEIVER WAVEFORMS

## Typical Performance Curves $V_{CC} = 3.3V$ , $T_A = +25^\circ C$ ; Unless Otherwise Specified (Continued)

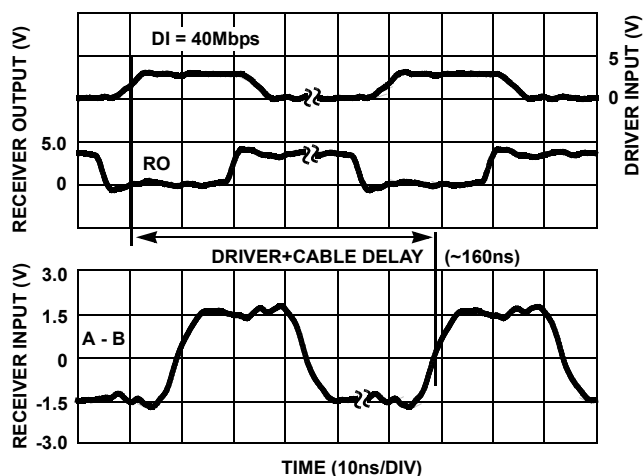


FIGURE 18. DRIVER AND RECEIVER WAVEFORMS DRIVING 100' (31m) OF CAT5 CABLE (DOUBLE TERMINATED WITH  $120\Omega$ )

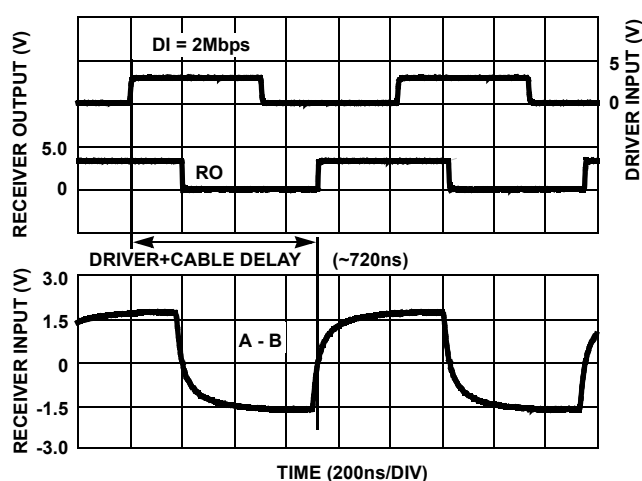


FIGURE 19. DRIVER AND RECEIVER WAVEFORMS DRIVING 500' (152m) OF CAT5 CABLE (DOUBLE TERMINATED WITH  $120\Omega$ )

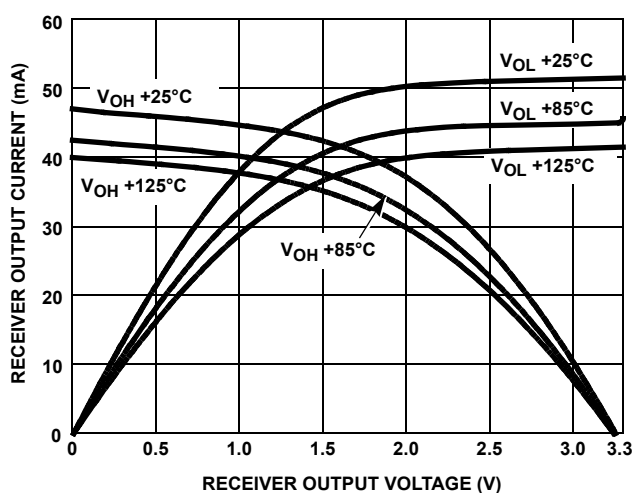


FIGURE 20. RECEIVER OUTPUT CURRENT vs RECEIVER OUTPUT VOLTAGE

## Die Characteristics

### SUBSTRATE AND DFN THERMAL PAD POTENTIAL (POWERED UP):

GND

### PROCESS:

Si Gate BiCMOS

## Application Information

RS-485 and RS-422 are differential (balanced) data transmission standards for use in long haul or noisy environments. RS-422 is a subset of RS-485, so RS-485 transceivers are also RS-422 compliant. RS-422 is a point-to-multipoint (multidrop) standard, which allows only one driver and up to 10 (assuming one unit load devices) receivers on each bus. RS-485 is a true multipoint standard, which allows up to 32 one unit load devices (any mix of drivers and receivers) on each bus. To allow for multipoint operation, the RS-485 spec requires that drivers must handle bus contention without sustaining any damage.

Another important advantage of RS-485 is the extended common-mode range (CMR), which specifies that the driver outputs and receiver inputs withstand signals that range from +12V to -7V. RS-422 and RS-485 are intended for runs as long as 4000' (~1200m), so the wide CMR is necessary to handle ground potential differences, as well as voltages induced in the cable by external fields.

### Receiver (Rx) Features

This transceiver utilizes a differential input receiver for maximum noise immunity and common-mode rejection. Input sensitivity is  $\pm 200\text{mV}$ , as required by the RS-422 and RS-485 specifications. Receiver inputs function with common-mode voltages as great as +9/-7V outside the power supplies (i.e., +12V and -7V), making them ideal for long networks, or industrial environments, where induced voltages are a realistic concern.

The receiver input resistance of 50k $\Omega$  surpasses the RS-422 specification of 4k $\Omega$ , and is 5x the RS-485 "Unit Load" (UL) requirement of 12k $\Omega$  minimum. Thus, the ISL3179E is known as a "one-fifth UL" transceiver, and there can be up to 160 devices on the RS-485 bus while still complying with the RS-485 loading specification.

The receiver is a "full fail-safe" version that guarantees a high level receiver output if the receiver inputs are unconnected (floating), shorted together, or connected to a terminated bus with all the transmitters disabled (terminated/undriven).

Rx outputs deliver large low state currents (typically 28mA at  $V_{OL} = 1\text{V}$ ) to ease the design of optically coupled isolated networks.

Receivers easily meet the 40Mbps data rate supported by the driver, and the receiver output is tri-statable via the active low  $\overline{\text{RE}}$  input.

### Driver (Tx) Features

The RS-485/RS-422 driver is a differential output device that delivers at least 1.5V across a 54 $\Omega$  load (RS-485), and at least 2V across a 100 $\Omega$  load (RS-422). The drivers feature low propagation delay skew to maximize bit width and to minimize EMI.

Outputs of the drivers are not slew rate limited, so faster output transition times allow data rates of at least 40Mbps. Driver outputs are tri-statable via the active high DE input.

For parallel applications, bit-to-bit skews between any two transmitter and receiver pairs are guaranteed to be no worse than 8ns (4ns max for any two Tx, 4ns max for any two Rx).

### ESD Protection

All pins on the ISL3179E include class 3 (>9kV) Human Body Model (HBM) ESD protection structures, but the RS-485 pins (driver outputs and receiver inputs) incorporate advanced structures allowing them to survive ESD events in excess of  $\pm 16.5\text{kV}$  HBM (ISL3179E) or  $\pm 12\text{kV}$  HBM (ISL3180E), and  $\pm 16.5\text{kV}$  (ISL3179E) or  $\pm 4\text{kV}$  (ISL3180E) IEC61000-4-2. The RS-485 pins are particularly vulnerable to ESD strikes because they typically connect to an exposed port on the exterior of the finished product. Simply touching the port pins, or connecting a cable, can cause an ESD event that might destroy unprotected ICs. These new ESD structures protect the device whether or not it is powered up, and without degrading the RS-485 common-mode range of -7V to +12V. This built-in ESD protection eliminates the need for board level protection structures (e.g., transient suppression diodes) and the associated, undesirable capacitive load they present.

### IEC61000-4-2 Testing

The IEC61000 test method applies to finished equipment, rather than to an individual IC. Therefore, the pins most likely to suffer an ESD event are those that are exposed to the outside world (the RS-485 pins in this case), and the IC is tested in its typical application configuration (power applied) rather than testing each pin-to-pin combination. The IEC61000 standard's lower current limiting resistor coupled with the larger charge storage capacitor yields a test that is much more severe than the HBM test. The extra ESD protection built into the ISL3179E's RS-485 pins allows the design of equipment meeting level 4 criteria without the need for additional board level protection on the RS-485 port.

### AIR-GAP DISCHARGE TEST METHOD

For this test method, a charged probe tip moves toward the IC pin until the voltage arcs to it. The current waveform delivered to the IC pin depends on approach speed, humidity, temperature, etc., so it is more difficult to obtain repeatable results. The ISL3179E RS-485 pins withstand  $\pm 16.5\text{kV}$  air-gap discharges, while the ISL3180E's RS-485 pins withstand  $\pm 4\text{kV}$ .

### CONTACT DISCHARGE TEST METHOD

During the contact discharge test, the probe contacts the tested pin before the probe tip is energized, thereby eliminating the variables associated with the air-gap discharge. The result is a more repeatable and predictable test, but equipment limits prevent testing devices at voltages higher than  $\pm 9\text{kV}$ . The RS-485 pins of the ISL3179E survive  $\pm 9\text{kV}$  contact discharges, while the ISL3180E's RS-485 pins withstand  $\pm 5\text{kV}$ .

### Hot Plug Function

When a piece of equipment powers up, there is a period of time where the processor or ASIC driving the RS-485 control lines (DE,  $\overline{\text{RE}}$ ) is unable to ensure that the RS-485 Tx and Rx outputs are kept disabled. If the equipment is connected to the bus, a driver activating prematurely during power-up may crash the bus. To avoid this scenario, the ISL3179E and ISL3180E incorporate a

“hot plug” function. Circuitry monitoring  $V_{CC}$  ensures that, during power-up and power-down, the Tx and Rx outputs remain disabled, regardless of the state of DE and  $\overline{RE}$ , if  $V_{CC}$  is less than  $\sim 2.4V$ . This gives the processor/ASIC a chance to stabilize and drive the RS-485 control lines to the proper states.

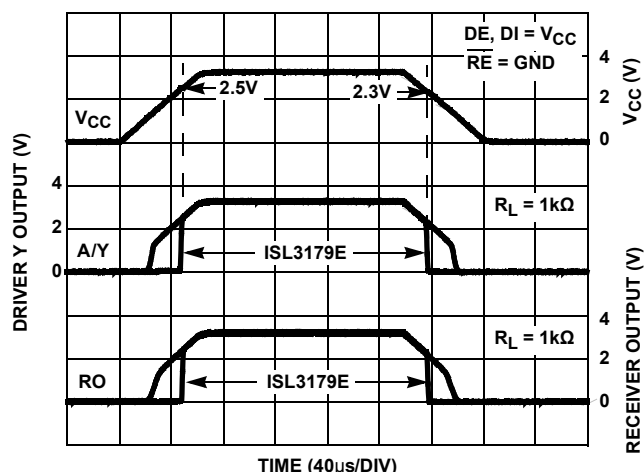


FIGURE 21. HOT PLUG PERFORMANCE (ISL3179E) vs ISL83485 WITHOUT HOT PLUG CIRCUITRY

## Data Rate, Cables, and Terminations

RS-485/RS-422 are intended for network lengths up to 4000', but the maximum system data rate decreases as the transmission length increases. Devices operating at 40Mbps are limited to lengths less than 100'.

Twisted pair is the cable of choice for RS-485/RS-422 networks. Twisted pair cables tend to pick up noise and other electromagnetically induced voltages as common-mode signals, which are effectively rejected by the differential receiver in this IC.

Proper termination is imperative to minimize reflections. In point-to-point, or point-to-multipoint (single driver on bus) networks, the main cable should be terminated in its characteristic impedance (typically 120Ω) at the end farthest from the driver. In multireceiver applications, stubs connecting receivers to the main cable should be kept as short as possible. Multipoint (multidriver) systems require that the main cable be terminated in its characteristic impedance at both ends. Stubs connecting a transceiver to the main cable should be kept as short as possible.

The ISL3179E and ISL3180E may also be used at slower data rates over longer cables, but there are some limitations. The Rx is optimized for high speed operation, so its output may glitch if the Rx input differential transition times are too slow. Keeping the transition times below 500ns, which equates to the Tx driving a 1000' (305m) CAT 5 cable, yields excellent performance over the full operating temperature range.

## Built-in Driver Overload Protection

As stated previously, the RS-485 specification requires that drivers survive worst case bus contentions undamaged. These transmitters meet this requirement via driver output short circuit current limits, and on-chip thermal shutdown circuitry.

The driver output stages incorporate short-circuit current limiting circuitry, which ensures that the output current never exceeds the RS-485 specification, even at the common-mode voltage range extremes. In the event of a major short-circuit condition, the device also includes a thermal shutdown feature that disables the drivers whenever the die temperature becomes excessive. This eliminates the power dissipation, allowing the die to cool. The drivers automatically reenables after the die temperature drops about  $+15^{\circ}C$ . If the contention persists, the thermal shutdown/reenable cycle repeats until the fault is cleared. Receivers stay operational during thermal shutdown.

## Low Power Shutdown Mode

This BiCMOS transceiver uses a fraction of the power required by their bipolar counterparts, but it also includes a shutdown feature that reduces the already low quiescent  $I_{CC}$  to a 50nA trickle. It enters shutdown whenever the receiver and driver are *simultaneously* disabled ( $\overline{RE} = V_{CC}$  and  $DE = GND$ ) for a period of at least 600ns. Disabling both the driver and the receiver for less than 60ns guarantees that the transceiver will not enter shutdown.

Note that receiver and driver enable times increase when the transceiver enables from shutdown. Refer to [Notes 10, 11, 12, 13](#) and [14](#), at the end of the “Electrical Specifications” table on [page 7](#), for more information.

## Revision History

The revision history provided is for informational purposes only and is believed to be accurate, but not warranted. Please go to the web to make sure that you have the latest revision.

| DATE            | REVISION | CHANGE                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|-----------------|----------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| August 25, 2015 | FN6365.5 | Added Key Differences table to page 1.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| July 8, 2015    | FN6365.4 | Reformatted datasheet to newest template and standards.<br>Features, page 1 - Changed: "- Class 3 HBM Level on all Other Pins.....>9kV" to: "- Class 3 HBM level on all other pins (ISL3179E).....>9kV"<br>Pin Description on page 3 - Added row for EP pin and added to description of GND.<br>Elec Spec table, page 6 ESD Performance section:<br>Added "ISL3179E Only" to All Pins and changed Test Conditions for HBM and Machine Model to "per JEDEC".<br>- Added 2 rows for "All Pins, ISL3180E Only"<br>Updated note references on Figures 6B and 9B.<br>Die Characteristics section on page 11: removed Transistor Count<br>ESD Protection on page 12 - removed "and ISL3180E" from 1st sentence.<br>Added Revision History table and About Intersil section.<br>Updated POD L10.3x3C on page 16 from rev 2 to rev 4. Changes since rev 2:<br>- Removed package outline and included center to center distance between lands on recommended land pattern.<br>- Removed Note 4 "Dimension b applies to the metallized terminal and is measured between 0.18mm and 0.30mm from the terminal tip." since it is not applicable to this package. Renumbered notes accordingly.<br>- Tiebar Note 4 updated<br>From: Tiebar shown (if present) is a non-functional feature.<br>To: Tiebar shown (if present) is a non-functional feature and may be located on any of the 4 sides (or ends).<br>Updated POD M8.15 on page 17 from rev 3 to rev 4. Changes since rev 3:<br>- Changed Note 1 "1982" to "1994" |

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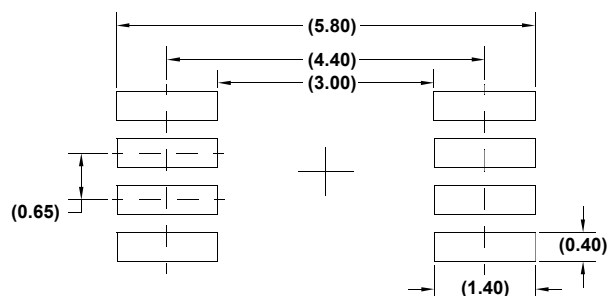
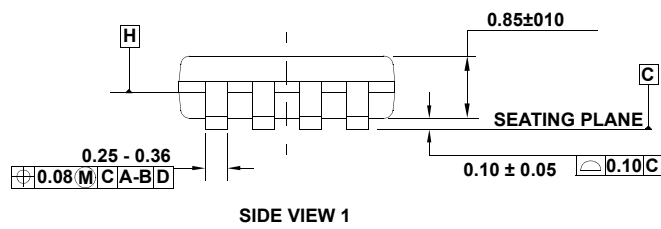
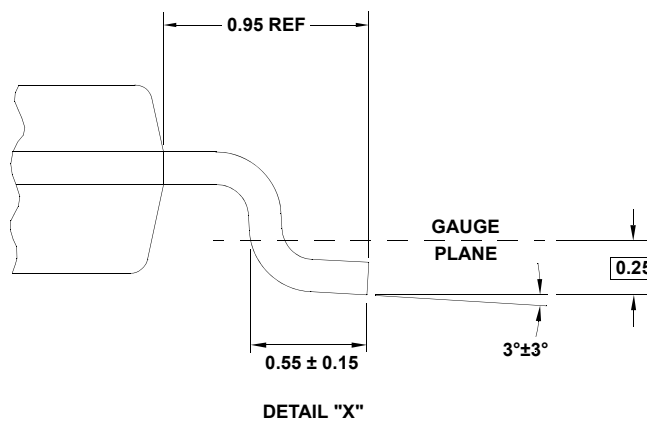
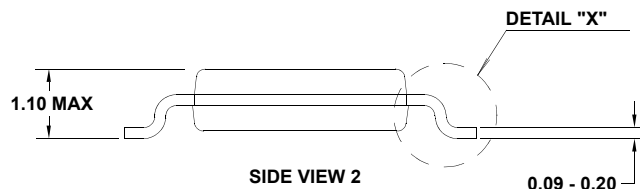
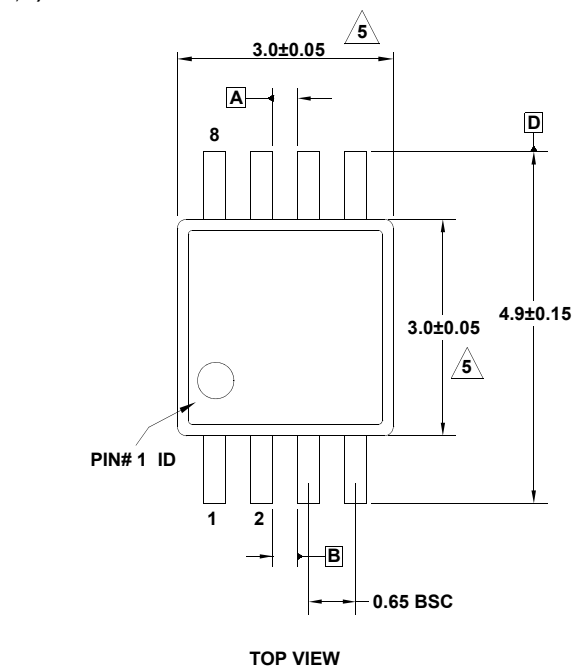
For information regarding Intersil Corporation and its products, see [www.intersil.com](http://www.intersil.com)

## Package Outline Drawing

M8.118

8 LEAD MINI SMALL OUTLINE PLASTIC PACKAGE

Rev 4, 7/11



### NOTES:

1. Dimensions are in millimeters.
2. Dimensioning and tolerancing conform to JEDEC MO-187-AA and AMSEY14.5m-1994.
3. Plastic or metal protrusions of  $0.15\text{mm}$  max per side are not included.
4. Plastic interlead protrusions of  $0.15\text{mm}$  max per side are not included.

5. Dimensions are measured at Datum Plane "H".

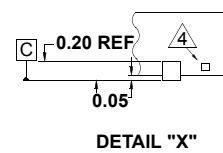
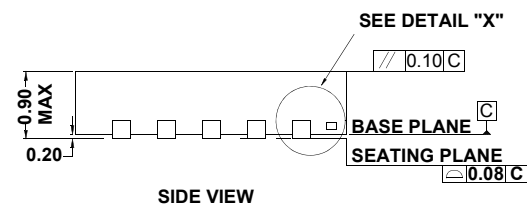
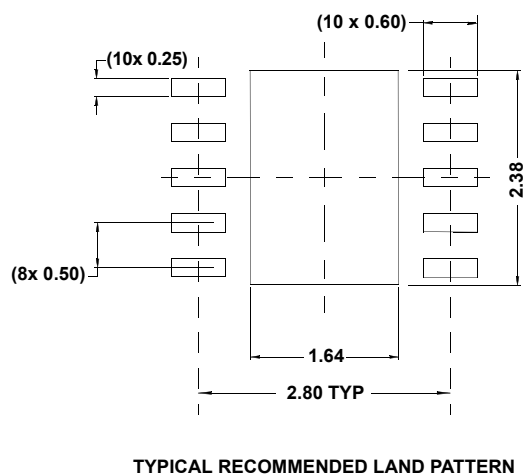
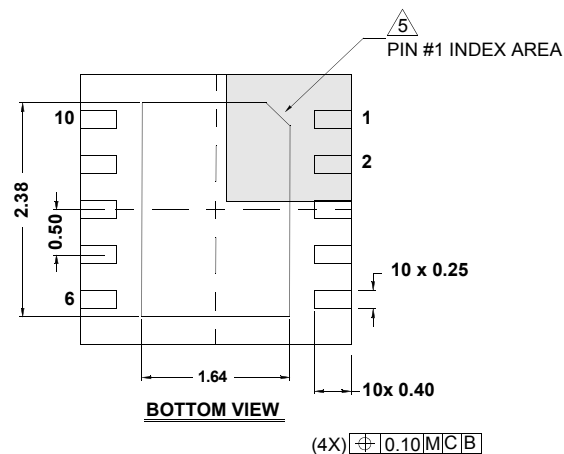
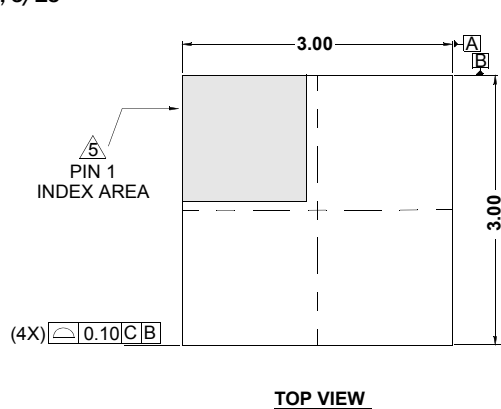
6. Dimensions in ( ) are for reference only.

## Package Outline Drawing

### L10.3x3C

10 LEAD DUAL FLAT PACKAGE (DFN)

Rev 4, 3/15



#### NOTES:

1. Dimensions are in millimeters.  
Dimensions in ( ) for Reference Only.
2. Dimensioning and tolerancing conform to AMSE Y14.5m-1994.
3. Unless otherwise specified, tolerance : Decimal  $\pm 0.05$
4. Tiebar shown (if present) is a non-functional feature and may be located on any of the 4 sides (or ends).
5. The configuration of the pin #1 identifier is optional, but must be located within the zone indicated. The pin #1 identifier may be either a mold or mark feature.
6. Compliant to JEDEC MO-229-WEED-3 except for E-PAD dimensions.



