

SIR-SD5

Photo Interrupter (Reflective)

SIR-SD5 reflective sensor combines a GaAs IrED with a high-sensitivity phototransistor in a super-mini ($\Phi 4$) ceramic package, reducing installation space.

Features

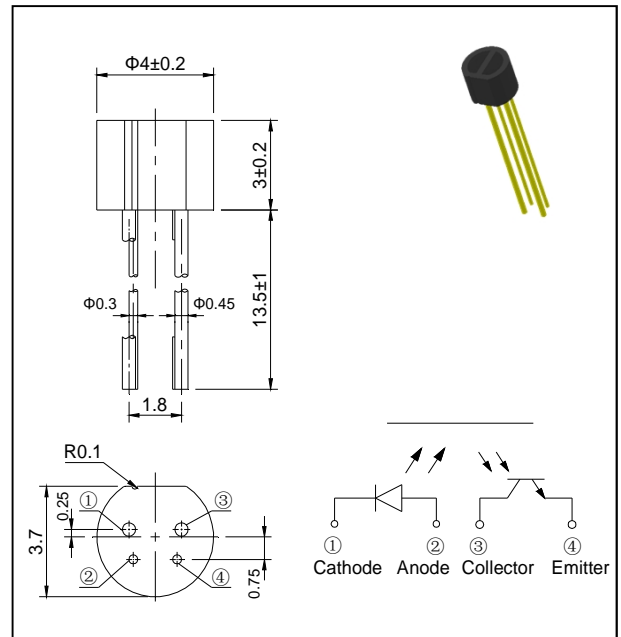
- Compact ($\Phi 4\text{mm}$)
- High performance
- High-speed response
- Easy to mount on PCB
- Widely applicable

Application

- Timing sensors
- Edge sensors
- Level sensors of liquid

Dimensions

(Unit:mm)



MAXIMUM RATINGS

($T_a = 25^\circ\text{C}$)

Item	Symbol	Rating	Unit	
Input	Power dissipation	P_D	75	mW
	Reverse voltage	V_R	5	V
	Forward current	I_F	50	mA
Output	Collector power dissipation	P_C	75	mW
	Collector current	I_C	20	mA
	C-E voltage	V_{CEO}	30	V
	E-C voltage	V_{ECO}	3	V
Operating temp.		$T_{opr.}$	-20~+90	$^\circ\text{C}$
Storage temp.		$T_{stg.}$	-30~+100	$^\circ\text{C}$
Soldering temp. ^{*2}		$T_{sol.}$	260	$^\circ\text{C}$

*1. Lead Soldering Temperature(3mm from package for 5sec)

ELECTRO- OPTICAL CHARACTERISTICS

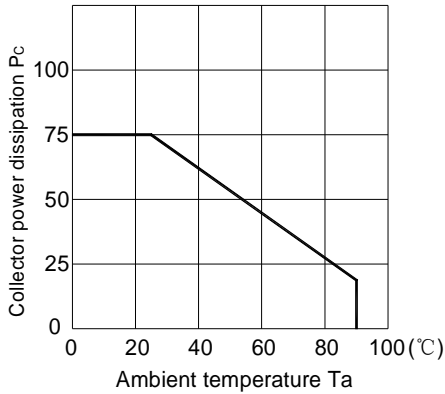
($T_a = 25^\circ\text{C}$)

Items	Symbol	Condition	Min.	Typ.	Max.	Unit
Input	Forward voltage	$I_F = 4\text{mA}$	-	-	1.2	V
	Reverse current	$V_R = 5\text{V}$	-	-	10	μA
	Peak wavelength	$I_F = 20\text{mA}$	-	940	-	nm
Output	Collector dark current	$V_{CE0} = 10\text{V}$	-	-	0.1	μA
	Light current	$V_{CE} = 2\text{V}, I_F = 4\text{mA}$	-	100	-	μA
	Leakage current	$V_{CE} = 2\text{V}, I_F = 4\text{mA}$	-	-	0.1	μA
Switching speeds Rise time	t_r	$V_{CC} = 2\text{V}, I_C = 100\mu\text{A}, R_L = 1\text{K}\Omega$	-	30	-	μsec
Switching speeds Fall time	t_f		-	30	-	μsec

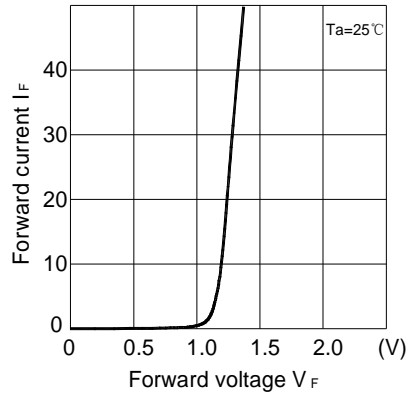
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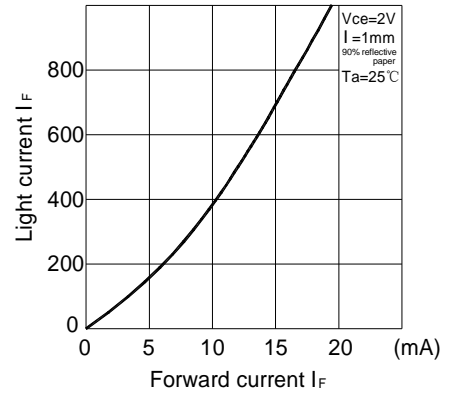
Collector power dissipation Vs. Ambient temperature



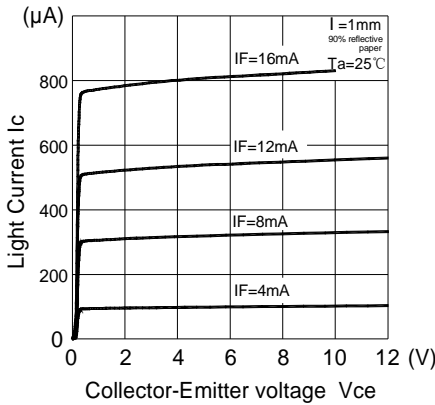
Forward current Vs. Forward voltage



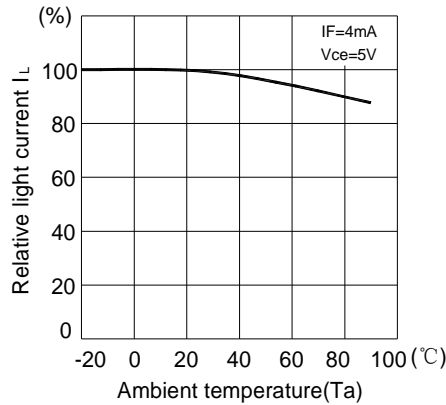
Light current Vs. Forward current



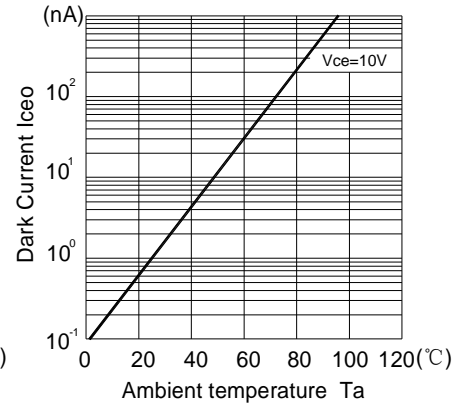
Light current Vs. Collector-Emitter Voltage



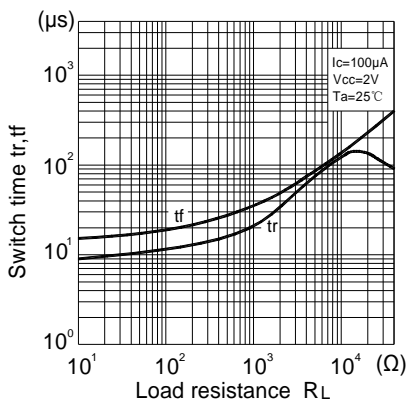
Relative light current Vs. Ambient temperature



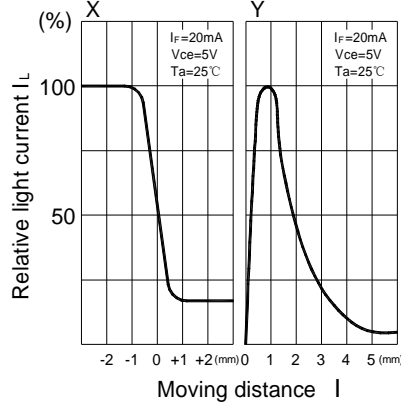
Dark Current Vs. Ambient temperature



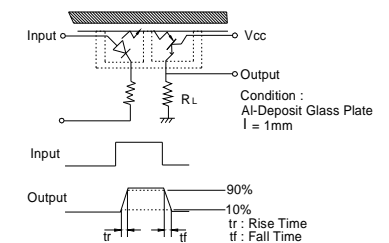
Switch time Vs. Load resistance



Relative light current Vs. Moving distance



Switching time measurement circuit



Method of measuring position detection characteristic

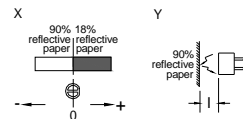


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Packing Specification

1. Fixed quantity (500pcs) of the products are packed into plastic bag
2. Seven bags of the products are put into #1 box
3. Ten #1 boxes are put into #2 box and two #2 boxes are put into #3 box(max 70,000pcs)
4. Packing slit is pasted on the out box

