

EPB5040G



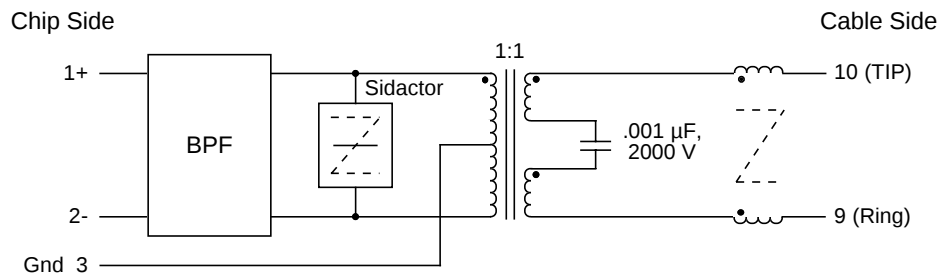
- Optimized for AMD79C978 , Conexant CN7221 •
& National DP83851 Chips
- UL/IEC 950 Approved File #132261 •
- Supports TUT Systems HPN Architecture •
- Robust construction allows for reflow processes •
- Temperature Range -40°C to +85°C •

Electrical Parameters @ 25° C

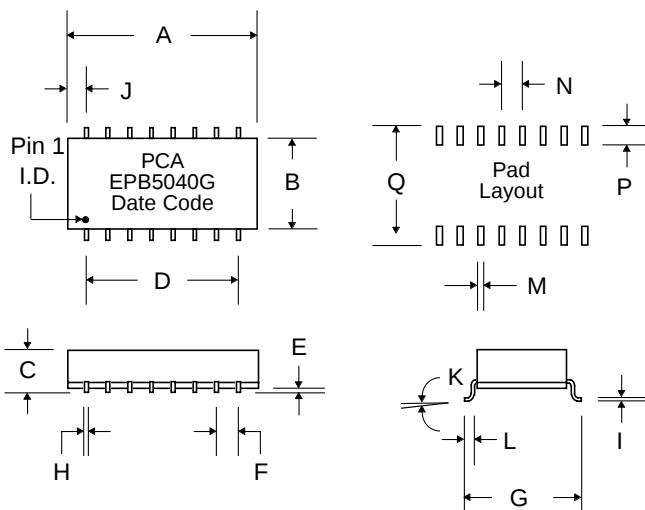
Cut-off Frequency		Insertion Loss (dB Max.)	Return Loss (dB Min.)	Attenuation (dB Min.)	
Lower Band MHz	Upper Band MHz	6.0-9.0 MHz	6.0-9.0 MHz	@ 1 MHz	@ 27 MHz
4.0-5.8	9-11	-3	-12	-60	-55

• Filter Characteristic Impedance : 100 Ω Isolation : 1500 Vrms •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.910	.930	.920	23.11	23.62	23.37
B	.370	.390	.380	9.40	9.91	9.65
C	.215	.235	.225	5.46	5.97	5.71
D	---	---	.700	---	---	17.78
E	.010	.015	.0125	.254	.381	.317
F	---	---	.100	---	---	2.54
G	.490	.510	.500	12.45	12.95	12.70
H	.016	.022	.018	.406	.559	.457
I	.008	.012	.010	.203	.305	.254
J	---	---	.110	---	---	2.79
K	0°	8°	4°	0°	8°	4°
L	.025	.045	.035	.635	1.14	.889
M	---	---	.030	---	---	.762
N	---	---	.100	---	---	2.54
P	---	---	.090	---	---	2.29
Q	---	---	.540	---	---	13.72