WL2817DA

Http://www.sh-willsemi.com

Ultra low dropout, 500mA/1A, CMOS LDO

Descriptions

The WL2817DA series are ultra low dropout, Low quiescent current, high PSRR CMOS LDO.

Using CMOS construction, the quiescent current consumed by the WL2817DA is typically 160uA over the entire input voltage range, making it attractive for consumer, networking applications that demand high output current. The WL2817DA series are available in wide output voltage range version from 1.0V to 3.3V.

The WL2817DA series offer thermal shutdown (OTP) and current limit functions, to assure the stability of chip and power system at wrong condition, and it uses trimming technique to guarantee output voltage accuracy within ±2%.

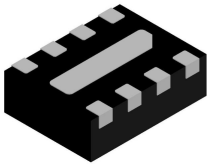
The WL2817DA series can choose the output current limit between 1.0A or 500mA by alternating the LCON pin between "H" or "L".The WL2817DA regulators are available in DFN1612-8L packages. Standard products are Pb-free and Halogen-free.

Features

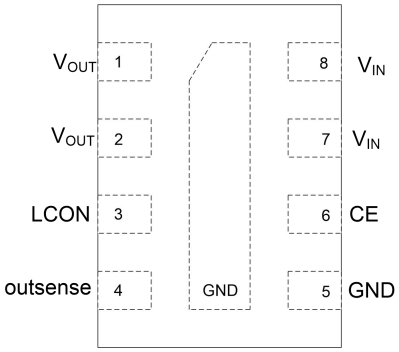
- Input voltage : 2.5V~5.5V
- Output voltage range :1.0V 1.1V 1.2V 1.5V 1.8V 2.7V 2.8V 2.9V 3V 3.3V
- Output current : 500mA/1A
- PSRR : 60dB(@ V_{OUT}=3V)
- Dropout voltage : 70mV @ I_{OUT}=0.5A
- Output noise : 50μV_{RMS}
- Quiescent current : 160μA Typ.

Applications

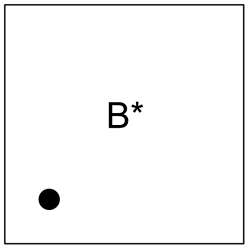
- LCD TV
- STB
- Computer, Graphic card
- Network communication equipments
- Others portable electronics devices



DFN1612-8L



Pin Configuration (Top View)



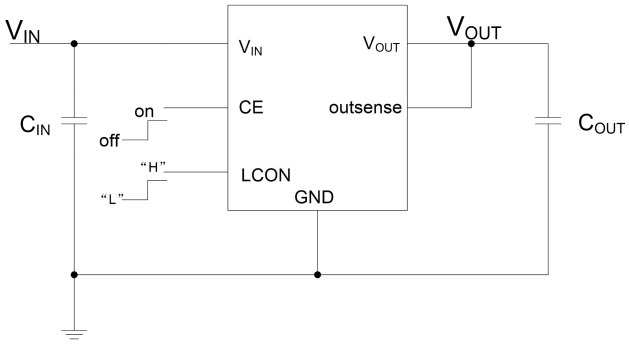
B = Device code (3.0V)
* = Month code (N: 2015.01, O:2015.02, and so on)

Marking

Order Information

For detail information, Please refer to page 15.

Typical Application

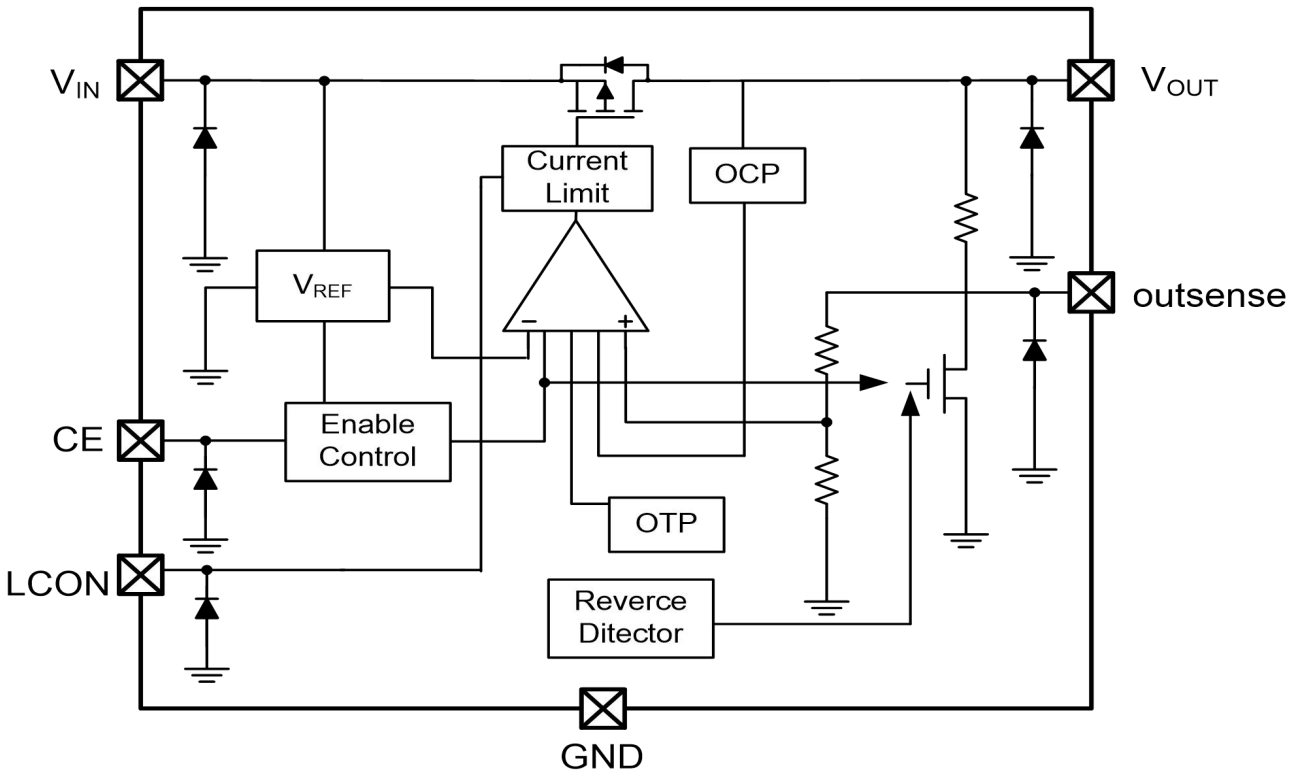


	Min.	Typ.	Max.
C_{IN}		4.7uF	
C_{OUT}		1uF	

Pin Description

PIN	Symbol	Description
1	V_{OUT}	Output
2	V_{OUT}	Output
3	LCON	Output Current Limit Alternate Pin（"H"=1A，"L"=500mA）
4	outsense	Feedback Pin
5	GND	Ground
6	CE	Enable, Active High
7	V_{IN}	Input
8	V_{IN}	Input

Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input voltage range	V_{IN}	-0.3～6.5	V
EN voltage range	V_{EN}	-0.3～V_{IN}	V
LCON voltage range	V_{LCON}	-0.3～V_{IN}	V
Output voltage range	V_{OUT}	-0.3～V_{IN}	V
Power dissipation *1	P_D	625	mW
Thermal resistance	$R_{\theta JA}$	165	°C/W
Junction temperature	T_J	150	°C
Lead temperature(10s)	T_L	260	°C
Storage temperature	Tstg	-55 ~ 150	°C
ESD Ratings	HBM	2000	V
	MM	200	V

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

***1:** Power dissipation is calculate by $P_D = (V_{IN}-V_{OUT}) \times I_{OUT}$

Recommend Operating Ratings

Parameter	Symbol	Value	Unit
Operating Supply voltage	V_{IN}	2.5～5.5	V
Operating Temperature Range	Topr	-40～85	°C

Electronics Characteristics (Ta=25°C, V_{IN}=V_{OUT}+1V, C_{IN}=4.7uF, C_{OUT}=1uF，I_{OUT}=1mA，LCON=EN=V_{IN}，unless otherwise noted)

Parameter	Symbol	Condition		Min.	Typ.	Max.	Unit
Output Voltage	V_{OUT}	T=25℃		Voset*0.98	Voset	Voset*1.02	V
		-40℃≤T≤85℃		Voset*0.97	Voset	Voset*1.03	
Current Limit	I_{LIM}	V_{IN}=Vset+0.5V	LCON= “H”	1			A
			LCON= “L”	0.5			A
Dropout Voltage	V_{DROP}	V_{OUT}=V_{OUT}*0.97	I_{OUT}=0.5A		70		mV
Load Regulation	△V_{Load}	V_{IN}=Vset+0.5V	LCON= “H”：1mA≤I_{OUT}≤1A		3.5		mV
			LCON= “L”：1mA≤I_{OUT}≤0.5A		1.5		mV
UVLO	V_{uvlo}				2		V
Line Regulation	△V_{LINE}	Vset+0.5V≤V_{IN}≤5.5V（V_{IN}≥UVLO）			5	10	mV
Quiescent Current	I_Q	I_{OUT}=0			160	220	uA
Shut-down Current	I_{SHDN}	V_{EN} = 0V			1	3	uA
V_{OUT} Temperature Coefficient	$\Delta V_{OUT}/\Delta T$	-40℃≤T≤85℃			100		ppm/℃
Short Current Limit	I_{sc}	V_{OUT}=0V	LCON= “H”		160		mA
			LCON= “L”		80		mA
Inrush Current Limit	I_{rush}*1	CC mode	LCON= “H”		500		mA
			LCON= “L”		250		mA
Reverse Current	I_{rev}*2	V_{OUT}=Voset+1V；EN=0；0≤V_{IN}≤Vrev_del			4.5	10	uA
Detector offset voltage in reverse current protection mode	V_{rev_det}*3	V_{OUT}=Voset+1V；EN=0			0.5		V
Release offset voltage in reverse current protection mode	V_{rev_rel}*4	V_{OUT}=Voset+1V；EN=0			0.35		V
Max reverse Current	I_{revmax}*5	V_{OUT}=Voset+1V；EN=0			70		uA
Discharge resistance	R_{dis}	EN=0			60		Ω
Power Supply Ripple Rejection	PSRR	V_{IN}=$(V_{OUT}+1V)_{DC}$+0.2V_{P-P} F=1KHz ,I_{OUT}=10mA			60		dB
Output noise voltage（V_{OUT}=3V）	e_{NO}	BW=10Hz to 100KHz I_{OUT}=0			40		μV_{RMS}
		BW=10Hz to 100KHz I_{OUT}=10mA			60		
Output noise voltage（V_{OUT}=1.0V）	e_{NO}	BW=10Hz to 100KHz I_{OUT}=0			20		μV_{RMS}
		BW=10Hz to 100KHz I_{OUT}=10mA			35		
EN logic high voltage	V_{ENH}	V_{IN}=5.5V, I_{OUT}=1mA		1.2			V

EN logic low voltage	V_{ENL}	V_{IN}=5.5V, I_{OUT}=0mA			0.4	V
EN pull-down current	I_{en}			0.2	1	uA
LCON pull-down current	I_{LCON}			0.2	1	uA
EN logic high voltage	V_{ENH}		1.2			V
EN logic low voltage	V_{ENL}				0.4	V
LCON logic high voltage	V_{ENH}		1.2			V
LCON logic low voltage	V_{ENL}				0.4	V
Thermal shutdown threshold	T_{SD}			165		°C
Thermal shutdown hysteresis	△ T_{SD}			30		°C

*1: For CC (Constant Current) mode, please refer to Start-up Characteristics.

*2 *3 *4 *5: Please refer to reverse current protection mode

Start-up Characteristics

Constant slope circuit is included in the WL2817DA to prevent the overshoot of the output voltage. If inrush current increases due to the large capacitance of C_{OUT}, the operation mode will be shift from Constant Slope (CS) mode to Constant Current (CC) mode. The CC mode maintains a constant inrush current. In the CC mode, t_{on} varies with the size of C_{OUT} and the load current.

Reverse Current Protection Circuit

The WL2817DA include a Reverse Current Protection Circuit, which stop the reverse current from V_{OUT} pin to V_{IN} pin or GND pin when V_{OUT} becomes higher than V_{IN}.

Following figure shows the load characteristics of each mode. When giving the V_{OUT} pin a constant voltage and decreasing the V_{IN} voltage,the V_{IN} voltage will become lower than V_{OUT}-V_{rev_det}, the reverse current protection starts to function to stop the load current. By increasing the V_{IN} voltage higher than V_{OUT}-V_{rev_rel}, the protection mode will be released to let the load current to flow. When V_{IN} voltage is between V_{OUT} and V_{rev_det}, the parasitic diode between V_{IN} pin and V_{OUT} pin becomes forward direction. As a result, the current flows from V_{OUT} pin to V_{IN} pin, and the maximum of the current is I_{revmax}.

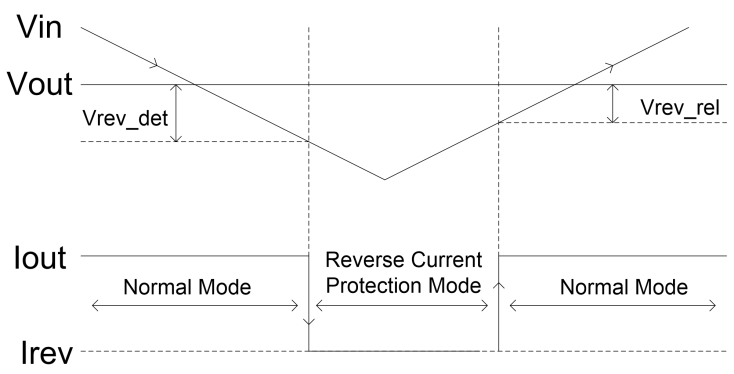
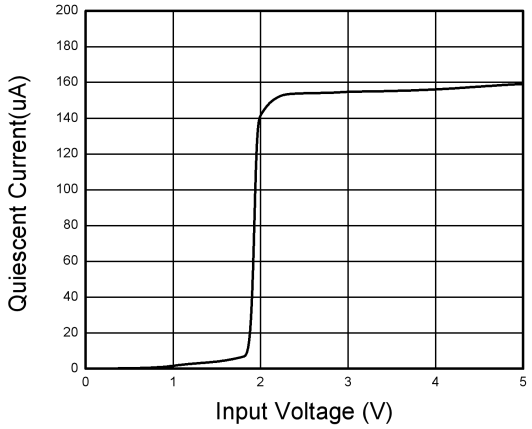
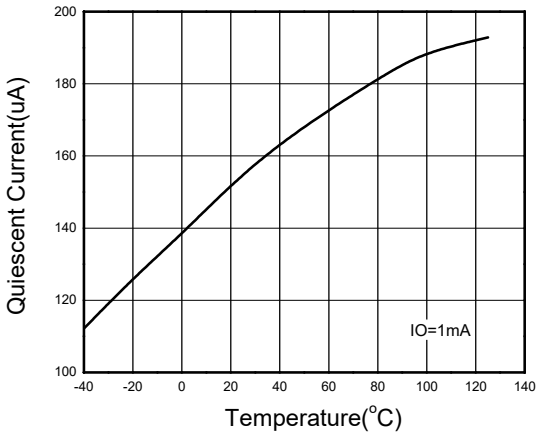


Figure1.Detection/Release Threshold value of Reverse Current Protection

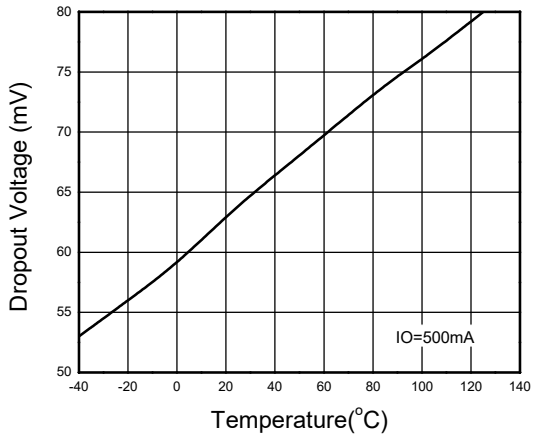
Typical characteristics (Ta=25°C, V_{IN}=4V, V_{OUT}=3V，C_{IN}=4.7uF，C_{OUT}=1uF, unless otherwise noted)



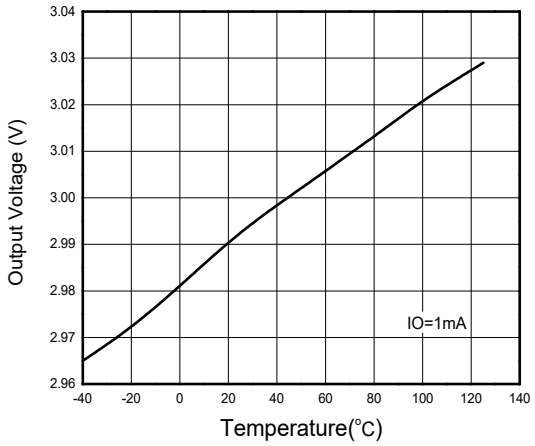
Quiescent current vs. Input voltage



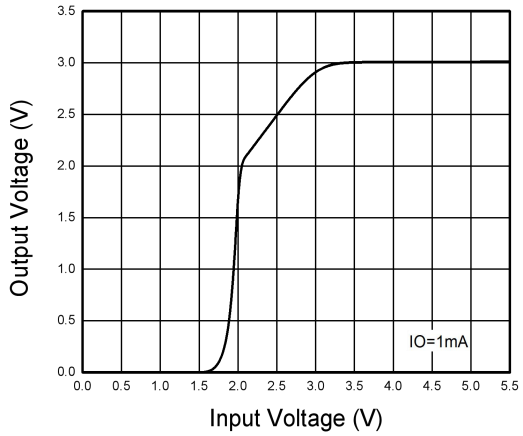
Quiescent current vs. Temperature



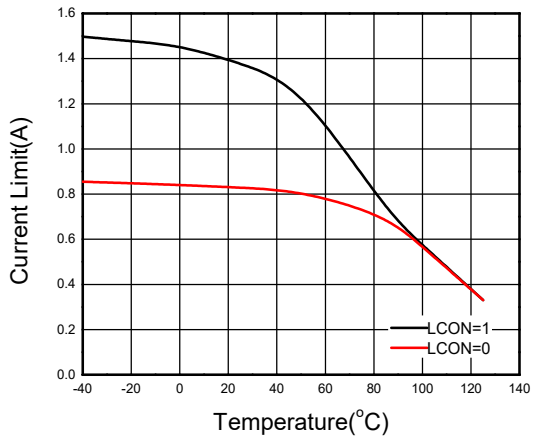
Dropout Voltage vs. Temperature



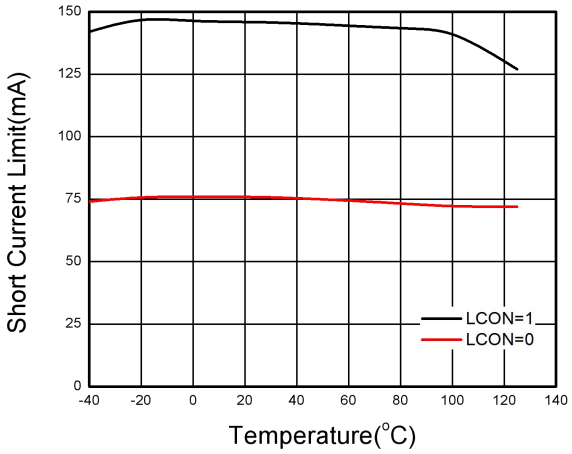
Output Voltage vs. Temperature



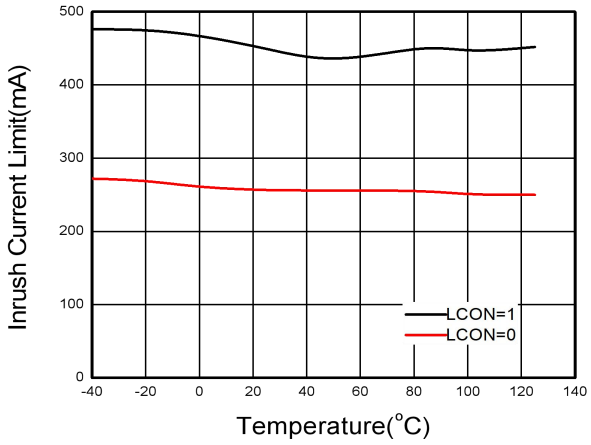
Output voltage vs. Input voltage



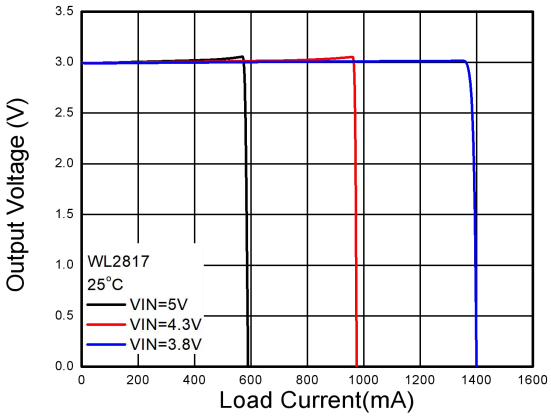
Current Limit vs. Temperature



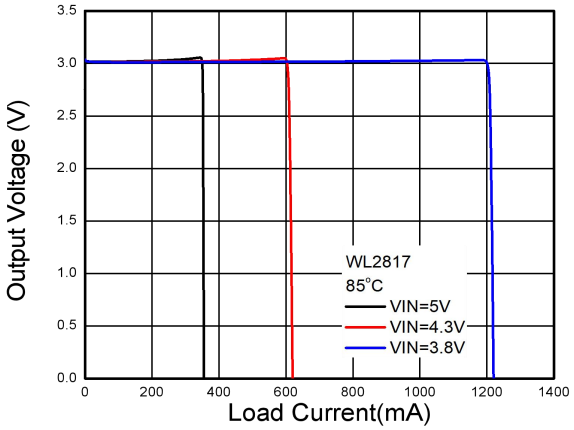
Short Current Limit vs. Temperature



Inrush Current Limit vs. Temperature



Output Voltage vs. Load Current



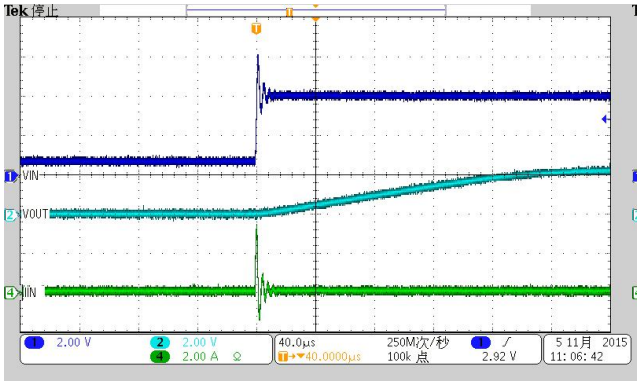
Output Voltage vs. Load Current

1.Start up

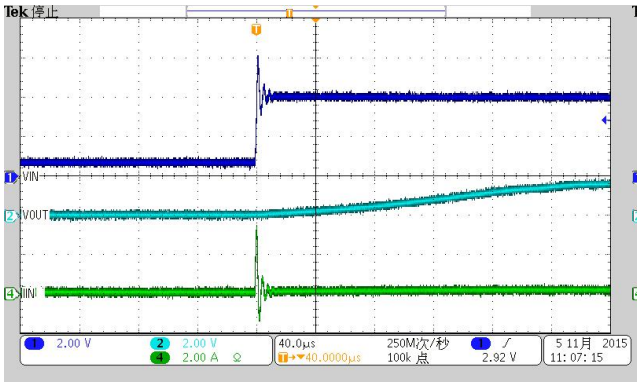
A：Different Load

LCON=0

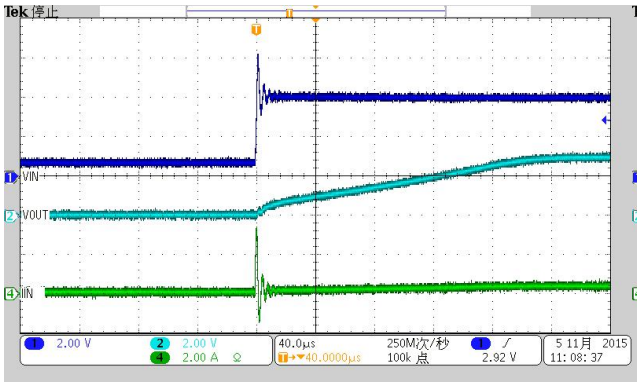
V_{IN}=4V,I_{OUT}=0mA



V_{IN}=4V,I_{OUT}=100mA

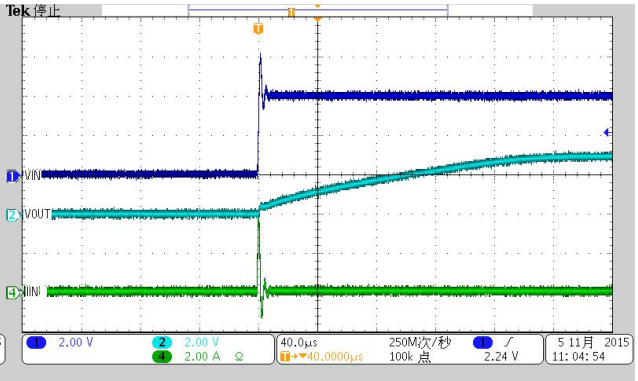


V_{IN}=4V,I_{OUT}=300mA

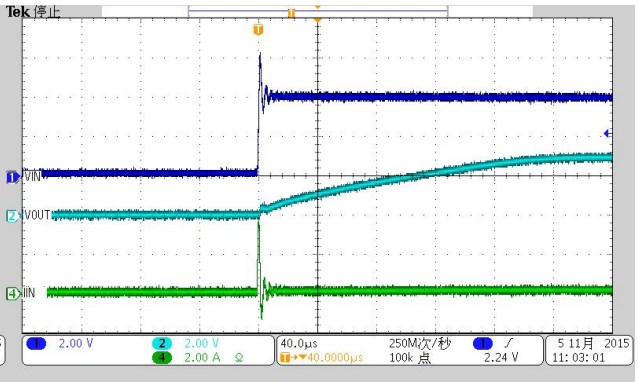


LCON=1

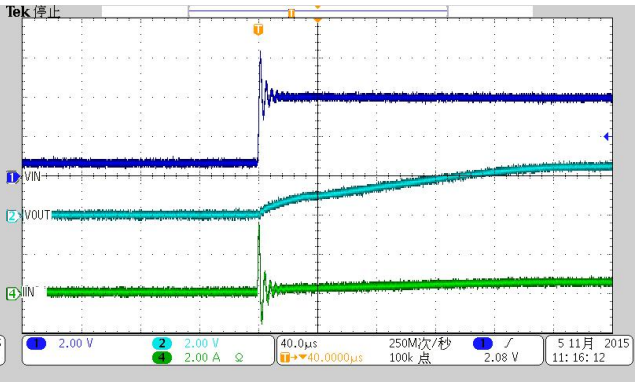
V_{IN}=4V,I_{OUT}=0mA



V_{IN}=4V,I_{OUT}=100mA



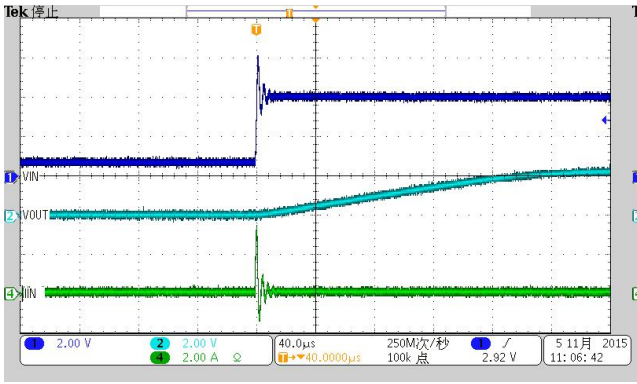
V_{IN}=4V,I_{OUT}=600mA



B：Different Cout

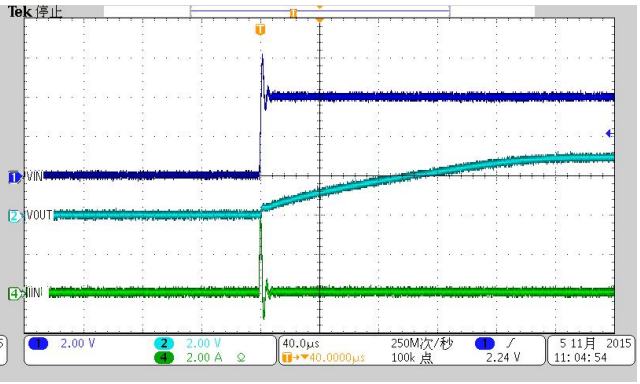
LCON=0

V_{IN}=4V，C_{OUT}=1uF

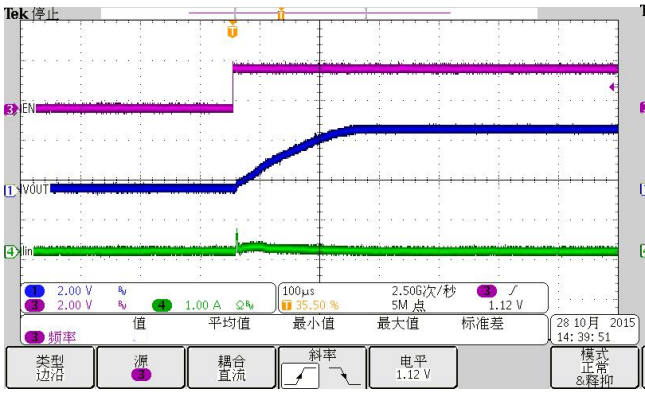


LCON=1

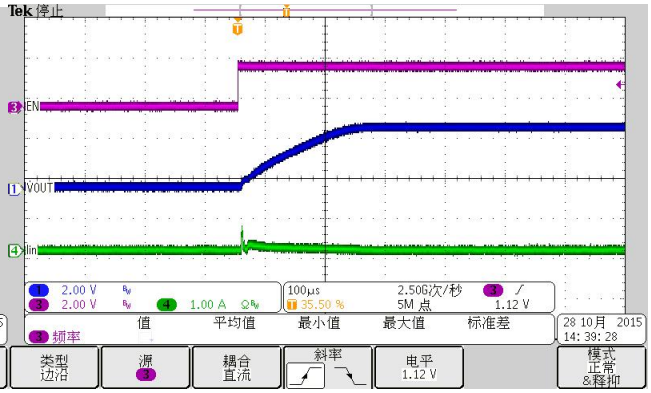
V_{IN}=4V，C_{OUT}=1uF



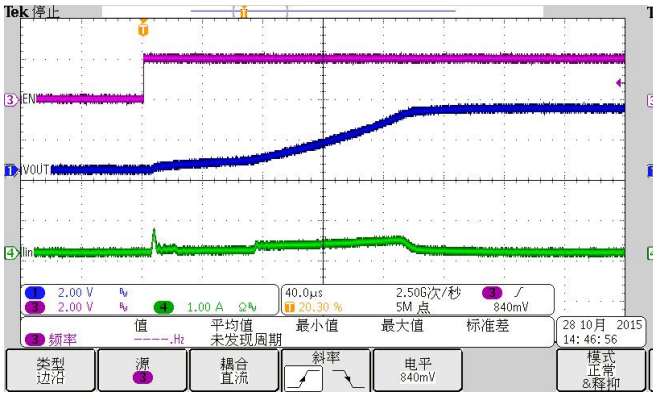
V_{IN}=4V，C_{OUT}=4.7uF



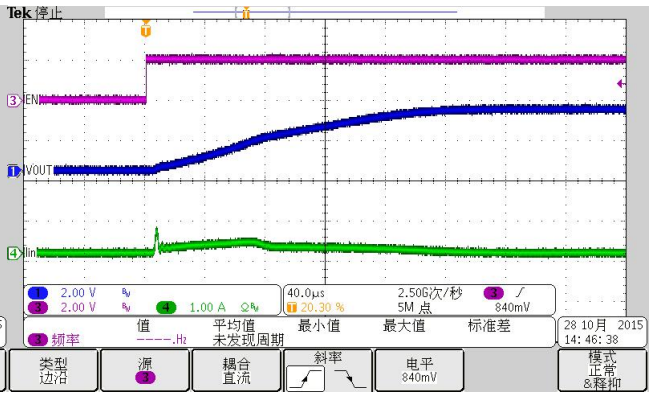
V_{IN}=4V，C_{OUT}=4.7uF



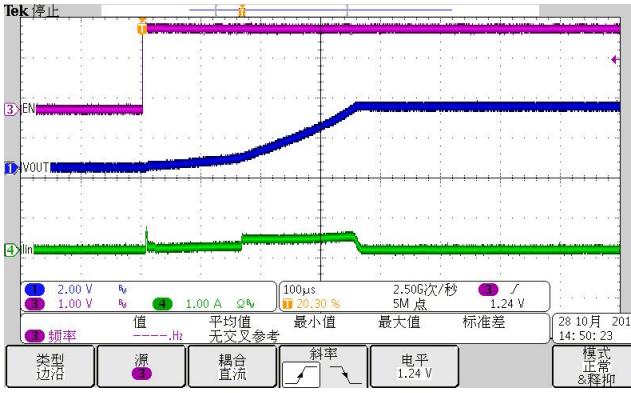
V_{IN}=4V，C_{OUT}=10uF



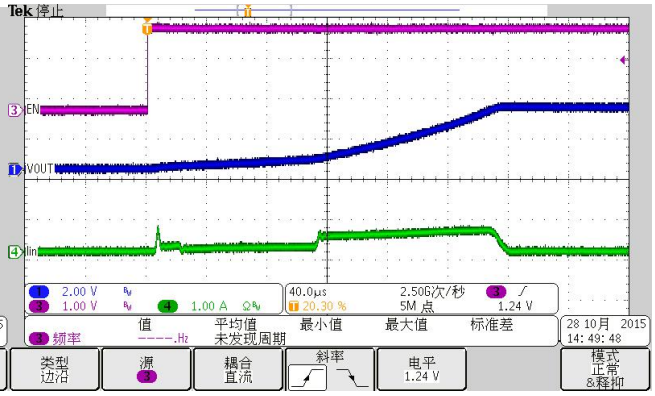
V_{IN}=4V，C_{OUT}=10uF



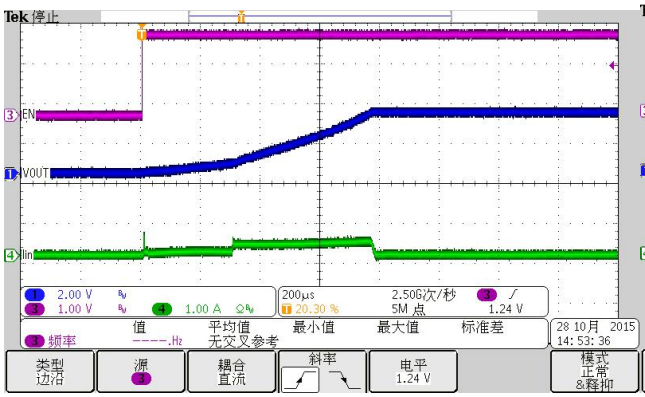
V_{IN}=4V，C_{OUT}=22uF



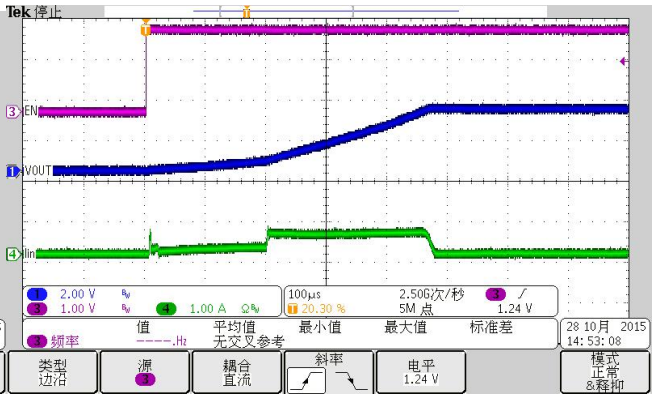
V_{IN}=4V，C_{OUT}=22uF



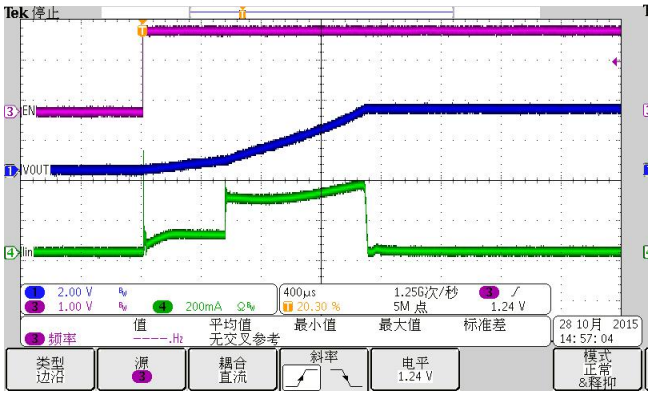
V_{IN}=4V，C_{OUT}=47uF



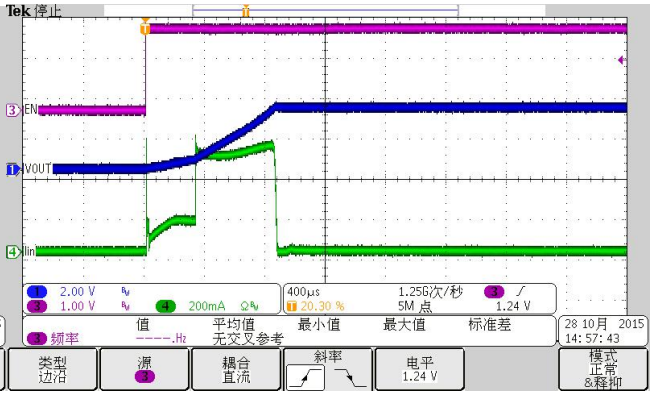
V_{IN}=4V，C_{OUT}=47uF



V_{IN}=4V，C_{OUT}=100uF

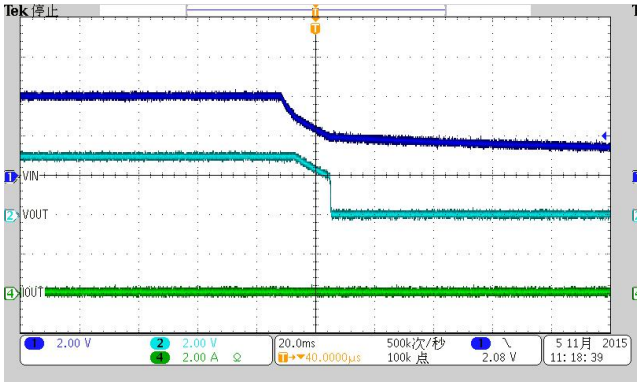


V_{IN}=4V，C_{OUT}=100uF

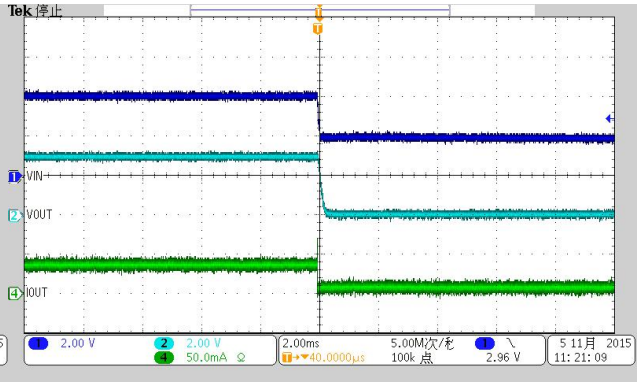


2.Shut down

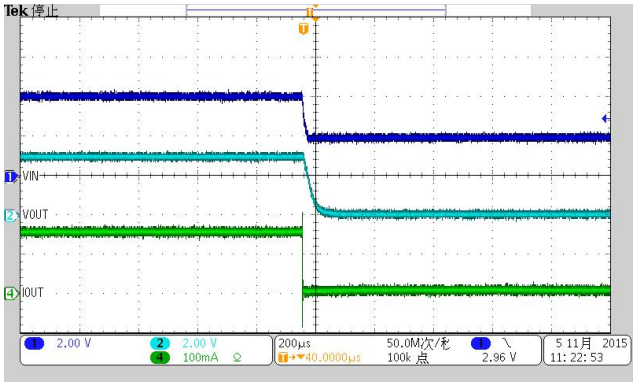
V_{IN}=4V，I_{OUT}=0mA



V_{IN}=4V，I_{OUT}=30mA

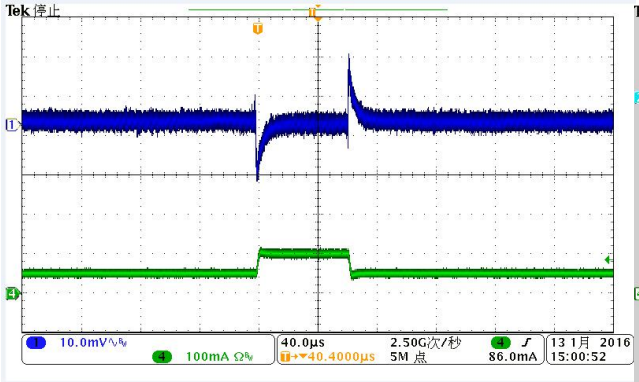


V_{IN}=4V，I_{OUT}=150mA

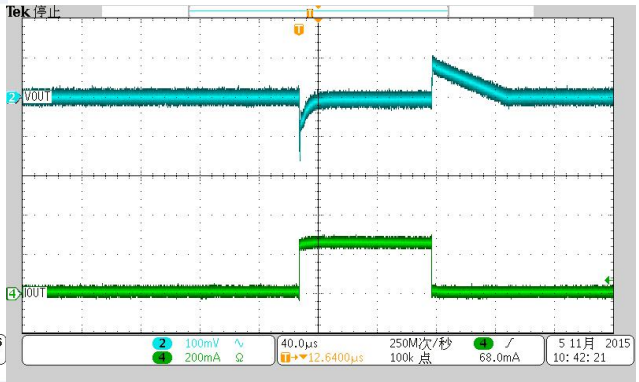


3.Load Step

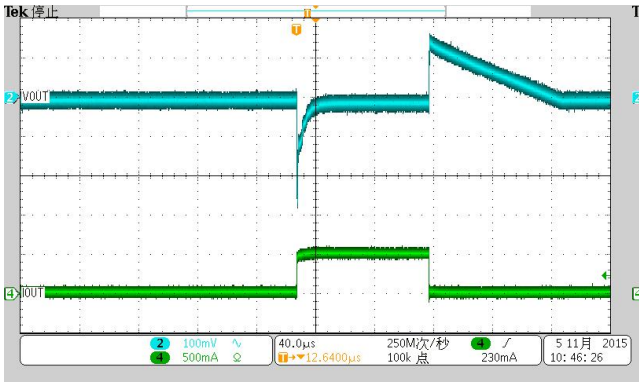
V_{IN}=4V,I_{OUT}=50mA-100mA



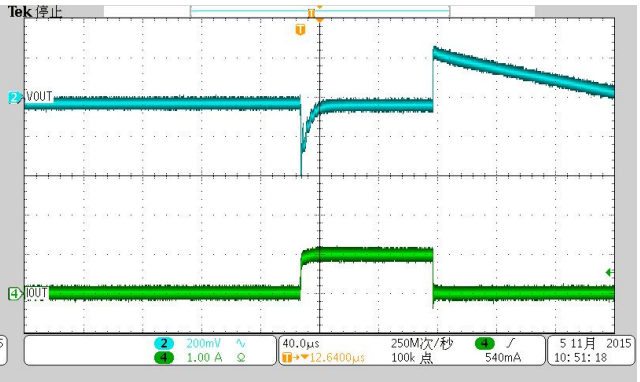
V_{IN}=4V,I_{OUT}=1mA-250mA



V_{IN}=4V,I_{OUT}=1mA-500mA

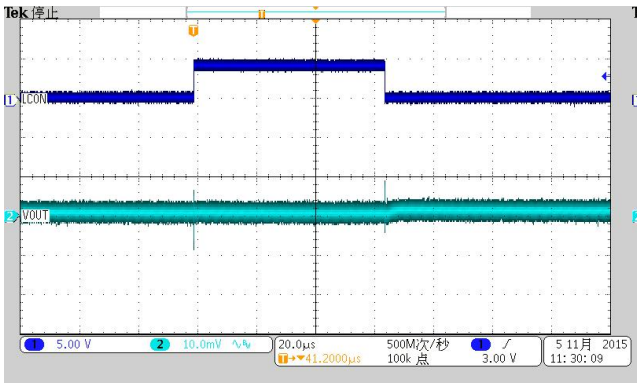


V_{IN}=4V,I_{OUT}=1mA-1A

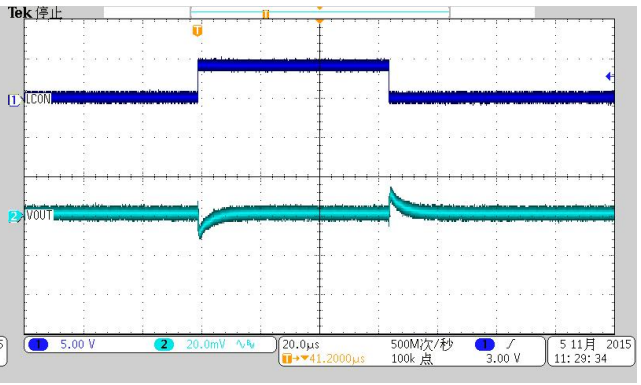


4.LCON Line Step

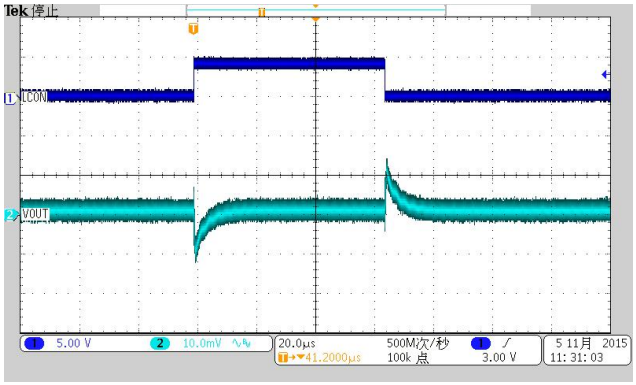
V_{IN}=4V，I_{OUT}=0mA



V_{IN}=4V，I_{OUT}=150mA

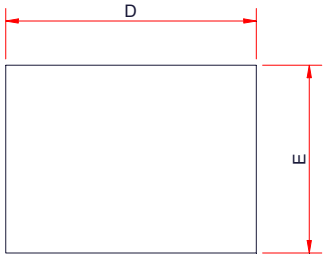


V_{IN}=4V，I_{OUT}=500mA

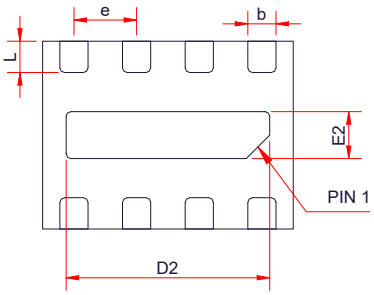


PACKAGE OUTLINE DIMENSIONS

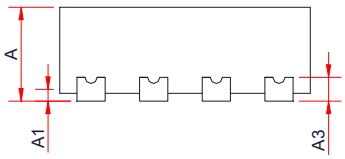
DFN1612-8L



TOP VIEW



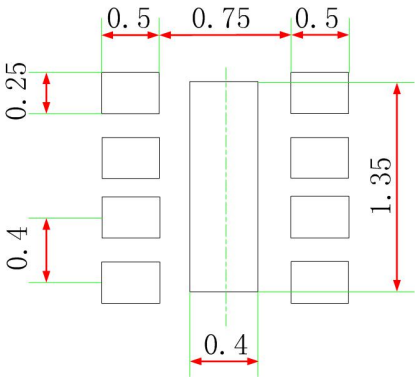
BOTTOM VIEW



SIDE VIEW

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.50	0.55	0.60
A1	-	-	0.05
A3	0.15 Ref.		
D	1.55	1.60	1.65
E	1.15	1.20	1.25
D2	1.25	1.30	1.35
E2	0.25	0.30	0.35
b	0.13	0.18	0.23
e	0.40 BSC		
L	0.15	0.20	0.25

Recommend PCB Layout (Unit: mm)

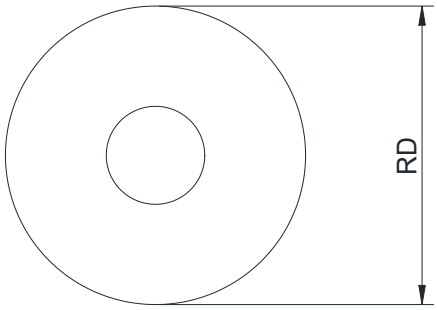


Notes:

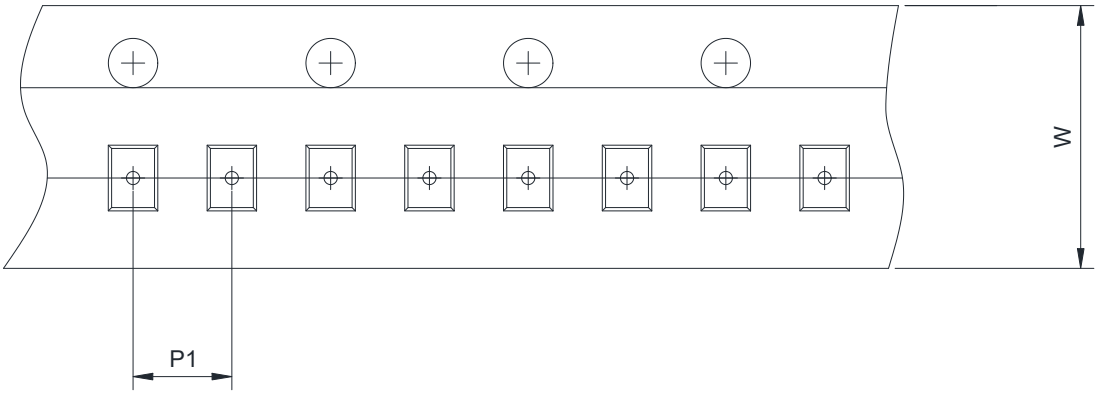
This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

TAPE AND REEL INFORMATION

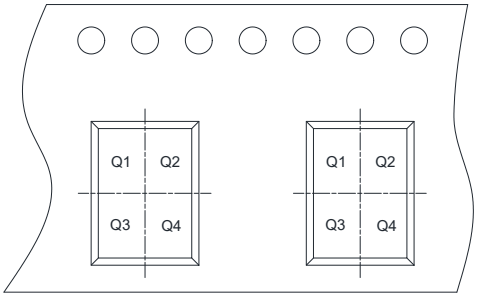
Reel Dimensions



Tape Dimensions



Quadrant Assignments For PIN1 Orientation In Tape



User Direction of Feed

RD	Reel Dimension	☑ 7inch	☐ 13inch		
W	Overall width of the carrier tape	☑ 8mm	☐ 12mm	☐ 16mm	
P1	Pitch between successive cavity centers	☐ 2mm	☑ 4mm	☐ 8mm	
Pin1	Pin1 Quadrant	☑ Q1	☐ Q2	☐ Q3	☐ Q4

ORDER INFORMATION

Ordering No.	V_{OUT} (V)	Package	Marking	Operating Temperature	Shipping
WL2817DA10-8/TR	1.0	DFN1612-8L	C*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA11-8/TR	1.1	DFN1612-8L	J*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA12-8/TR	1.2	DFN1612-8L	K*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA15-8/TR	1.5	DFN1612-8L	L*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA18-8/TR	1.8	DFN1612-8L	D*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA27-8/TR	2.7	DFN1612-8L	M*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA28-8/TR	2.8	DFN1612-8L	E*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA29-8/TR	2.9	DFN1612-8L	N*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA30-8/TR	3.0	DFN1612-8L	B*	-40 ~ +85°C	3000/Tape and Reel
WL2817DA33-8/TR	3.3	DFN1612-8L	I*	-40 ~ +85°C	3000/Tape and Reel