

SCHOTTKY RECTIFIER
HIGH EFFICIENCY SERIES

16JYQ100C
16 Amp, 100V

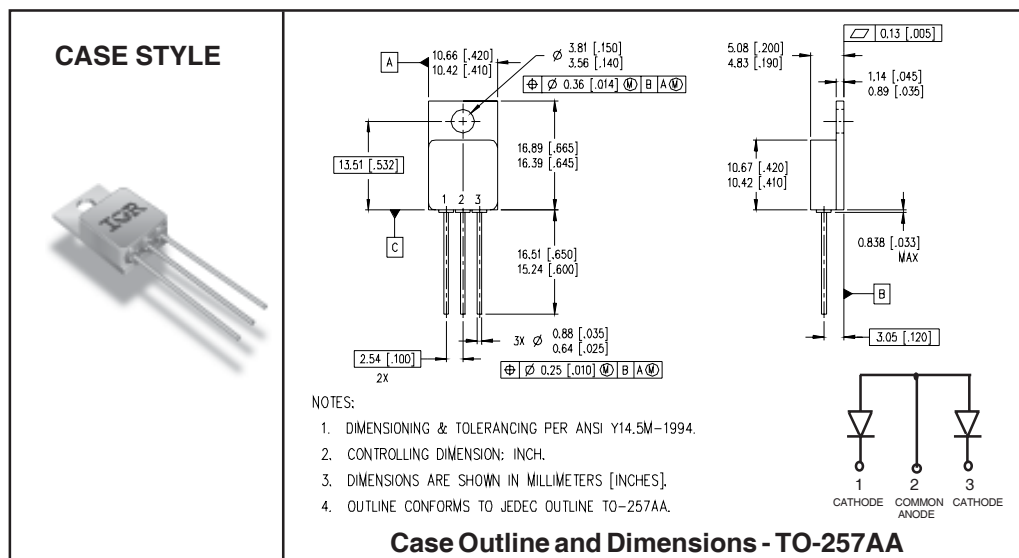
Major Ratings and Characteristics

Characteristics	Limits	Units
$I_{F(AV)}$	16	A
V_{RRM} (Per Leg)	100	V
I_{FSM} @ $t_p = 8.3\text{ms}$ half-sine (Per Leg)	250	A
V_F @ 16Apk, $T_J = 125^\circ\text{C}$ (Per Leg)	0.82	V
T_J, T_{stg} Operating and storage	-55 to 150	$^\circ\text{C}$

Description/Features

The 16JYQ100C center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of HiRel environments. It is packaged in the hermetic isolated TO-257AA package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

- Hermetically Sealed
- Ceramic Eyelets
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long term Reliability
- Lightweight



16JYQ100C

International
IOR Rectifier**Voltage Ratings**

Part number	16JYQ100C
V_R Max. DC Reverse Voltage (V) (Per Leg)	100
V_{RWM} Max. Working DC Reverse Voltage (V) (Per Leg)	

Absolute Maximum Ratings

Parameters	Limits	Units	Conditions
$I_{F(AV)}$ Max. Average Forward Current See Fig. 5	16	A	50% duty cycle @ $T_C = 131^\circ\text{C}$, square waveform
I_{FSM} Max. Peak One Cycle Non - Repetitive Surge Current (Per Leg)	250	A	@ $t_p = 8.3$ ms half-sine

Electrical Specifications

Parameters	Limits	Units	Conditions
V_{FM} Max. Forward Voltage Drop (Per Leg) See Fig. 1①	0.77	V	@ 8.0A $T_J = -55^\circ\text{C}$
	0.98	V	@ 16A $T_J = -55^\circ\text{C}$
	0.75	V	@ 8.0A $T_J = 25^\circ\text{C}$
	0.95	V	@ 16A $T_J = 25^\circ\text{C}$
	0.63	V	@ 8.0A $T_J = 125^\circ\text{C}$
	0.82	V	@ 16A $T_J = 125^\circ\text{C}$
I_{RM} Max. Reverse Leakage Current (Per Leg) See Fig. 2①	0.01	mA	$T_J = 25^\circ\text{C}$
	2.4	mA	$T_J = 100^\circ\text{C}$
	10	mA	$T_J = 125^\circ\text{C}$
C_T Max. Junction Capacitance (Per Leg)	430	pF	$V_R = 5V_{DC}$ (1MHz, 25°C)
L_S Typical Series Inductance (Per Leg)	6.9	nH	Measured from anode lead to cathode lead 6mm (0.025 in.) from package

Thermal-Mechanical Specifications

Parameters	Limits	Units	Conditions
T_J Max. Junction Temperature Range	-55 to 150	$^\circ\text{C}$	
T_{stg} Max. Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
R_{thJC} Max. Thermal Resistance, Junction to Case (Per Leg)	2.0	$^\circ\text{C/W}$	DC operation See Fig. 4
R_{thJC} Max. Thermal Resistance, Junction to Case (Per Package)	1.0	$^\circ\text{C/W}$	DC operation
wt Weight (Typical)	4.3	g	
Die Size (Typical)	105X125	mils	
Case Style	T0-257AA		

① Pulse Width < 300 μs , Duty Cycle < 2%

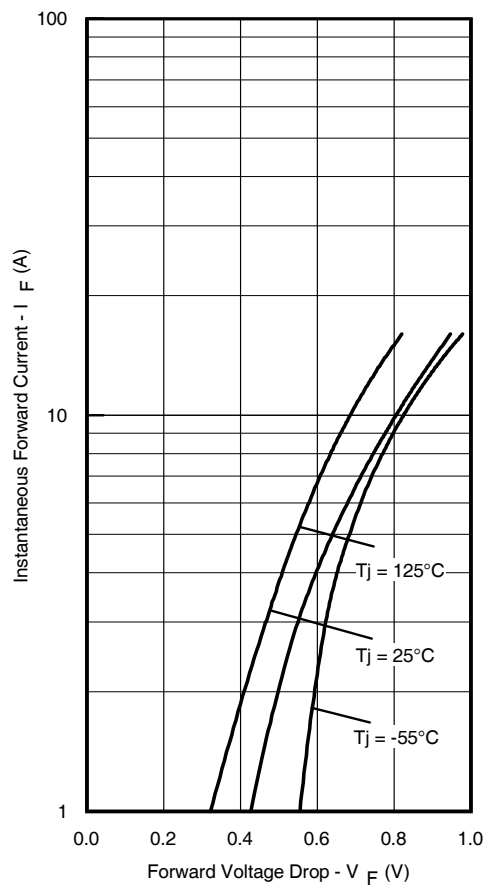


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

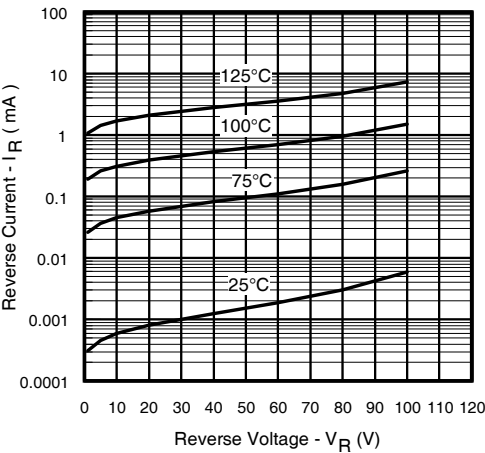


Fig. 2 - Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

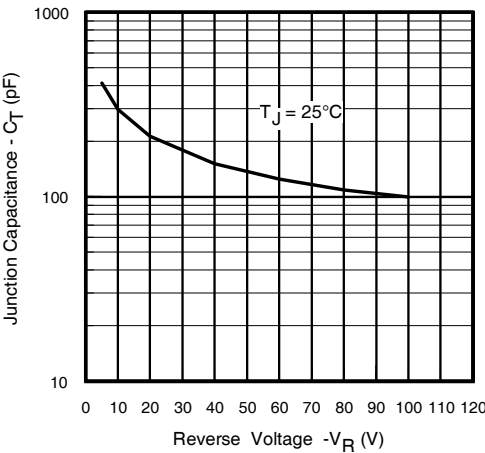
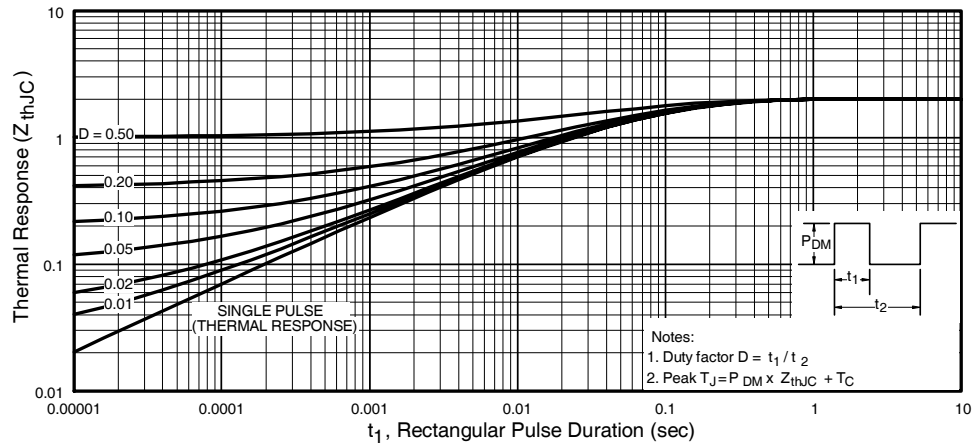
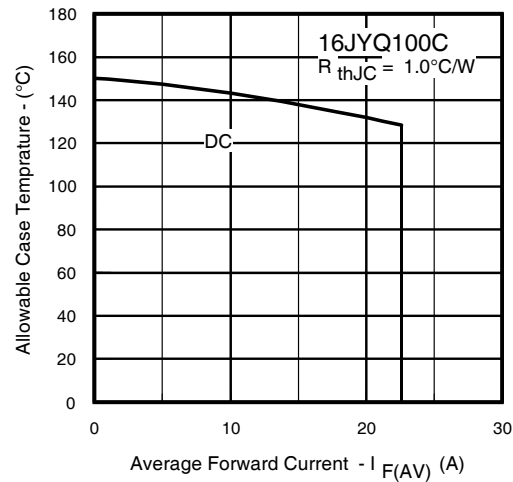


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)Fig. 5 - Max. Allowable Case Temperature Vs.
Average Forward CurrentInternational
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