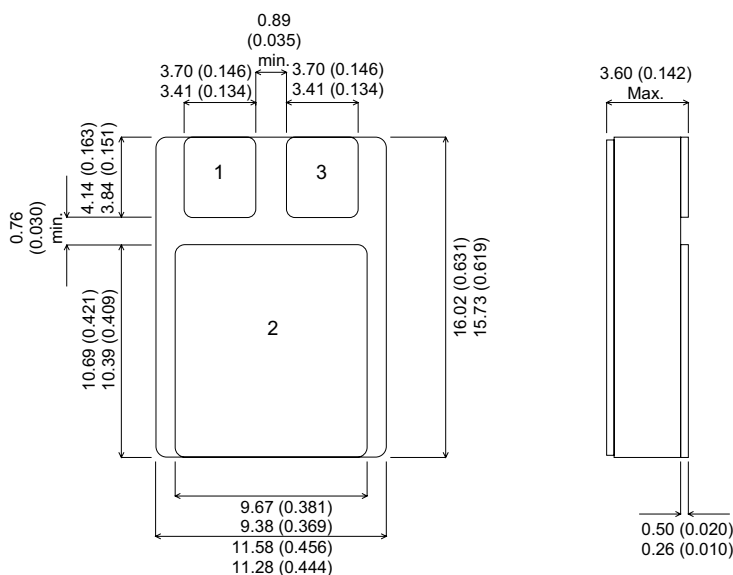


MECHANICAL DATA

Dimensions in mm (inches)



SMD 1

Pad 1 – Gate

Pad 2 – Drain

Pad 3 – Source

P-CHANNEL POWER MOSFET FOR HI-REL APPLICATIONS

V_{DS} -100V

$I_{D(cont)}$ -8A

$R_{DS(on)}$ 0.35Ω

FEATURES

- HERMETICALLY SEALED
- SIMPLE DRIVE REQUIREMENTS
- LIGHTWEIGHT
- SCREENING OPTIONS AVAILABLE
- ALL LEADS ISOLATED FROM CASE

(also available as IRFN9130SMD with Gate and Source reversed)

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V_{GS}	Gate – Source Voltage	±20V
I_D	Continuous Drain Current @ $T_{case} = 25^{\circ}C$	8A
I_D	Continuous Drain Current @ $T_{case} = 100^{\circ}C$	5A
I_{DM}	Pulsed Drain Current	40A
P_D	Power Dissipation @ $T_{case} = 25^{\circ}C$	45W
	Linear Derating Factor	0.36W/°C
T_J, T_{stg}	Operating and Storage Temperature Range	-55 to 150°C
$R_{\theta JC}$	Thermal Resistance Junction to Case	2.8°C/W max.

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise stated)

Parameter		Test Conditions		Min.	Typ.	Max.	Unit
STATIC ELECTRICAL RATINGS							
BV _{DSS}	Drain – Source Breakdown Voltage	V _{GS} = 0	I _D = 1mA	100			V
ΔBV _{DSS}	Temperature Coefficient of Breakdown Voltage	Reference to 25°C I _D = 1mA			0.1		V/°C
R _{DS(on)}	Static Drain – Source On–State Resistance	V _{GS} = 10V	I _D = 5A			0.35	Ω
		V _{GS} = 10V	I _D = 8A			0.4	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS}	I _D = 250μA	2		4	V
g _{fs}	Forward Transconductance	V _{DS} ≥ 15V	I _{DS} = 5A	3			S(ᵱ)
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} = 0	V _{DS} = 0.8BV _{DSS}			25	μA
			T _J = 125°C			250	
I _{GSS}	Forward Gate – Source Leakage	V _{GS} = 20V				100	nA
I _{GSS}	Reverse Gate – Source Leakage	V _{GS} = –20V				-100	
DYNAMIC CHARACTERISTICS							
C _{iss}	Input Capacitance	V _{GS} = 0			860		pF
C _{oss}	Output Capacitance	V _{DS} = 25V			350		
C _{rss}	Reverse Transfer Capacitance	f = 1MHz			125		
Q _g	Total Gate Charge	V _{GS} = 10V I _D = 8A V _{DS} = 0.5BV _{DSS}		12.5		29	nC
Q _{gs}	Gate – Source Charge	I _D = 8A		1.0		6.3	nC
Q _{gd}	Gate – Drain (“Miller”) Charge	V _{DS} = 0.5BV _{DSS}		2		27	
t _{d(on)}	Turn–On Delay Time	V _{DD} = 50V				60	ns
t _r	Rise Time	I _D = 8A				140	
t _{d(off)}	Turn–Off Delay Time	R _G = 7.5Ω				140	
t _f	Fall Time					140	
SOURCE – DRAIN DIODE CHARACTERISTICS							
I _S	Continuous Source Current					8	A
I _{SM}	Pulse Source Current					32	
V _{SD}	Diode Forward Voltage	I _S = 8A	T _J = 25°C			4.7	V
		V _{GS} = 0					
t _{rr}	Reverse Recovery Time	I _S = 8A	T _J = 25°C			300	ns
Q _{rr}	Reverse Recovery Charge	d _i / d _t ≤ 100A/μs V _{DD} ≤ 50V				3	μC
PACKAGE CHARACTERISTICS							
L _D	Internal Drain Inductance	(from 6mm down drain lead pad to centre of die)			8.7		nH
L _S	Internal Source Inductance	(from 6mm down source lead to centre of source bond pad)			8.7		