

Features

- ◆ 100 Watts Peak Pulse Power per Line (tp=8/20μs)
- ◆ Protects two or four I/O lines
- ◆ Low capacitance: 0.3pF typical (I/O to I/O)
- ◆ Low operating voltage: 5V
- ◆ RoHS Compliant
- ◆ IEC61000-4-2 (ESD) ±25kV (air), ±20kV (contact)
- ◆ IEC61000-4-4 (EFT) 40A (5/50ns)
- ◆ IEC61000-4-5 (Lightning) 4A (8/20μs)

Mechanical Characteristics

- ◆ Package: DFN2510-10 (2.5×1.0×0.5mm)
- ◆ Ultra low leakage: nA level
- ◆ Case Material: “Green” Molding Compound.
- ◆ UL Flammability Classification Rating 94V-0
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below

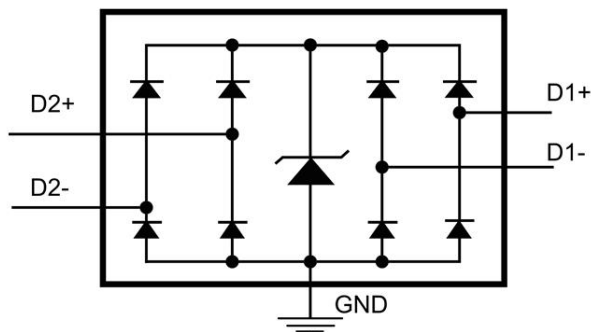
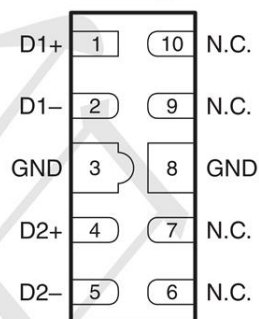
Applications

- ◆ High Definition Multimedia Interface (HDMI)
- ◆ Digital Visual Interface (DVI)
- ◆ Unified Display Interface (UDI)
- ◆ MDDI Ports
- ◆ PCI Express
- ◆ Serial ATA

Ordering Information

Part Number	Qty per Reel	Reel Size
TPD4EUSB30DQAR-TP	3000	7"

Dimensions and Pin Configuration



Marking: **xyBMR**

BMR = Device code
xy = date code

Absolute Maximum Ratings (Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	100	W
Peak Pulse Current (8/20μs)	I _{PP}	4	A
ESD per IEC 61000-4-2 (Air)	VESD	± 25	kV
ESD per IEC 61000-4-2 (Contact)		± 20	
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}			5	V	Any I/O pin to ground
Breakdown Voltage	V _{BR}	6	8	9	V	I _T = 1mA, any I/O pin to ground
Reverse Leakage Current	I _R			0.4	μA	V _{RWM} = 5V, any I/O pin to ground
Clamping Voltage	V _C			9	V	I _{PP} = 1A (8 x 20μs pulse), any I/O pin to ground
Clamping Voltage	V _C			25	V	I _{PP} = 4A (8 x 20us pulse), any I/O pin to ground
Junction Capacitance	C _J		0.3		pF	V _R = 0V, f = 1MHz, between I/O pins
Junction Capacitance	C _J		0.6	0.8	pF	V _R = 0V, f = 1MHz, any I/O pin to ground

Characteristic Curves

Fig1. 8/20 μ s Pulse Waveform

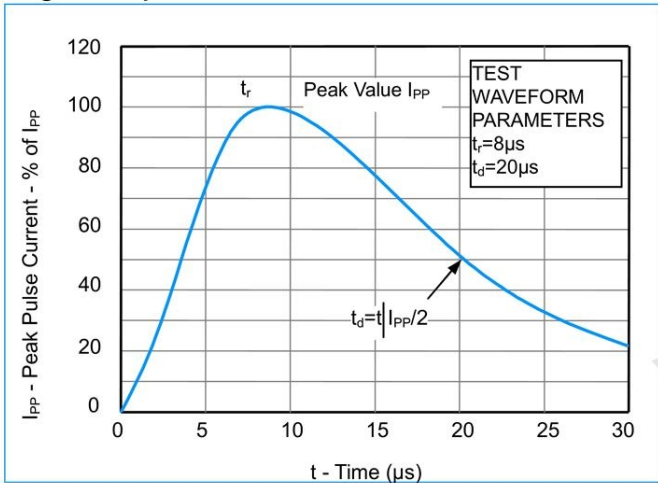


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

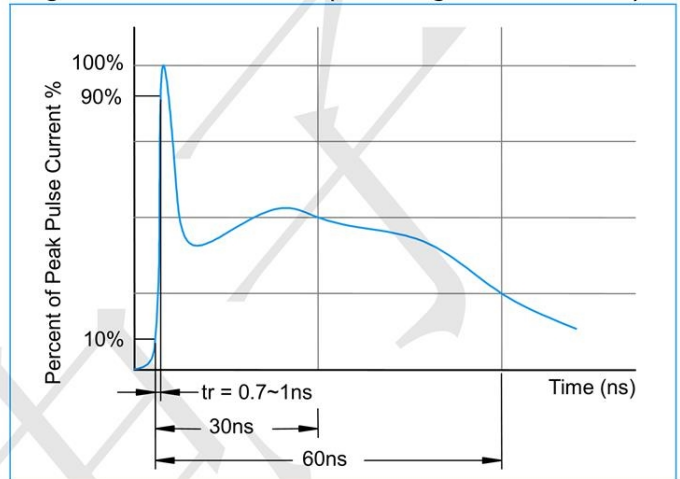


Fig3. Non - Repetitive Peak Pulse Power vs. Pulse Time

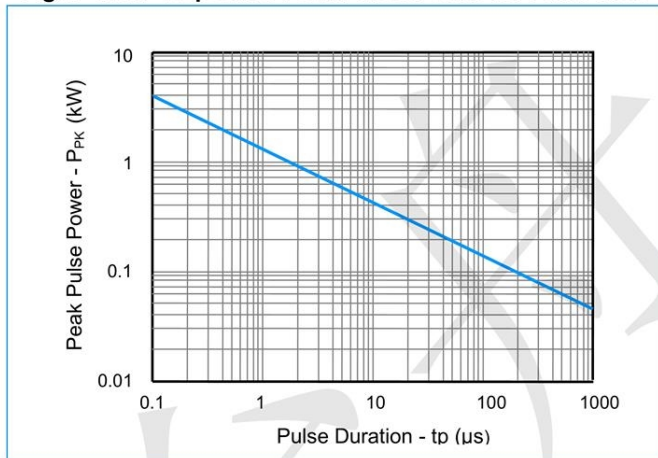
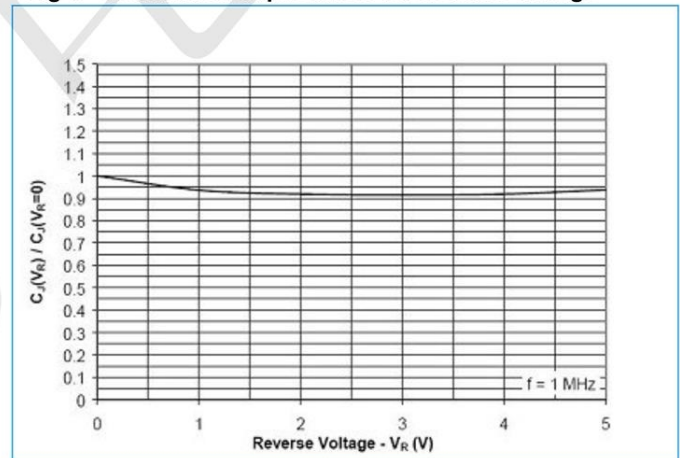
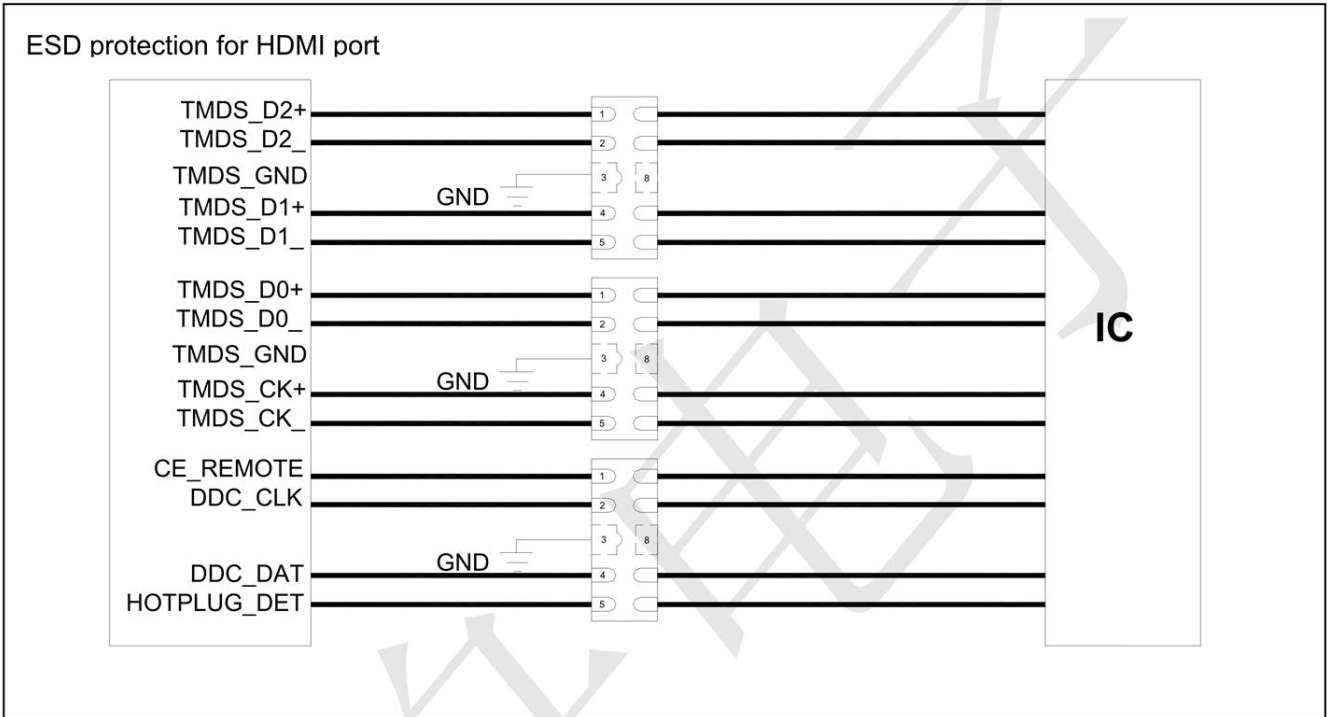


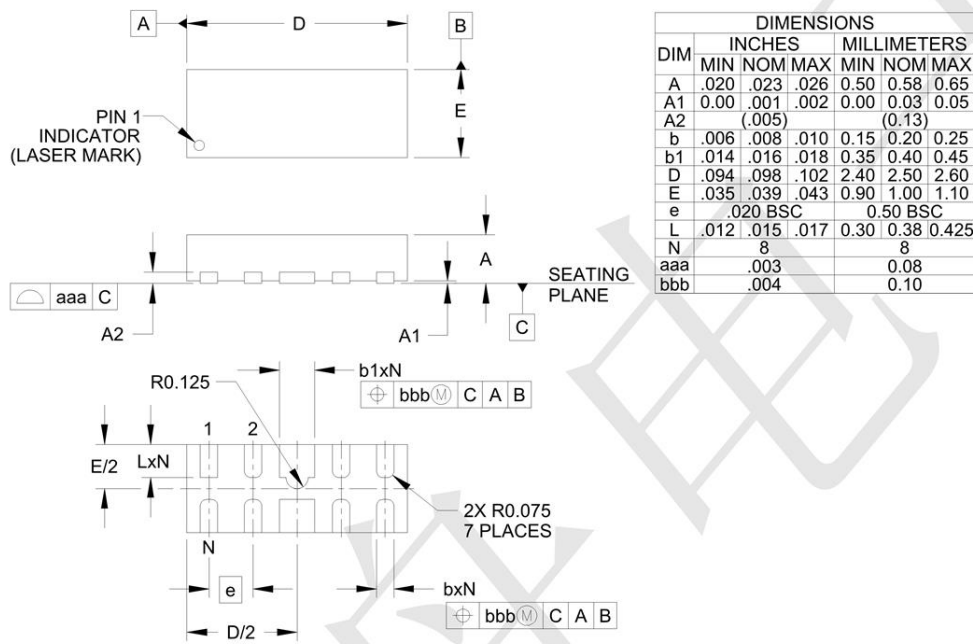
Fig4. Normalized Capacitance vs. Reverse Voltage



Application Information



Outline Drawing - DFN2510-10



Land Pattern - DFN2510-10

