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QM3003J

P-Channel 30V Fast Switching MOSFET

General Description

The QM3003J is a high performance trench P-channel MOSFET which utilizes extremely high cell density to provide low Rdson and gate charge characteristics. It is ideally suited to support synchronous buck converter applications.

The QM3003J meets RoHS and Green Product requirements while supporting full function reliability.

Features

- ✓ Advanced high cell density Trench technology
- ✓ Super Low Gate Charge
- ✓ Excellent CdV/dt effect decline
- ✓ Green Device Available

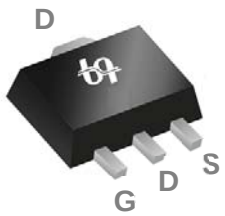
Product Summary

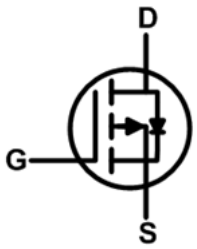
V_{DS}	$R_{DS(ON)}$ max (V_{GS}=-10V)	I_D (T_A=25 °C)
-30V	32mΩ	-5.8A

Applications

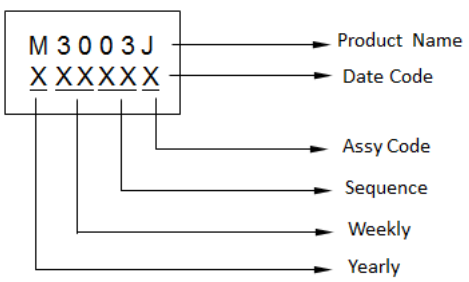
- ✓ High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- ✓ Networking DC-DC Power System
- ✓ Load Switch

Pin Configuration





Ordering Information

Order Number	Package Type	Top Marking
QM3003J	SOT89	

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Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_A=25°C	Continuous Drain Current, V_{GS} @ -10V[1]	-5.8	A
I_D@T_A=70°C	Continuous Drain Current, V_{GS} @ -10V[1]	-4.6	A
I_{DM}	Pulsed Drain Current[2]	-24	A
P_D@T_A=25°C	Total Power Dissipation[3]	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient[1]	--	85	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	--	21.6	°C/W

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P-Channel Electrical Characteristics

P-Channel Electrical Characteristics: (T_J=25 ℃, unless otherwise noted)						
Symbol	**Parameter**	**Conditions**	**Min**	**Typ**	**Max**	**Unit**
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-30	--	--	V
$\triangle BV_{DSS}/\triangle T_J$	BVDSS Temperature Coefficient	Reference to 25°C , I_D=-1mA	--	-0.022	--	V/°C
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V , I_D=-5A	--	26	32	mΩ
		V_{GS}=-4.5V , I_D=-4A	--	36	45	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-1.5	-2.5	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		--	4.6	--	mV/°C
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-24V , V_{GS}=0V , T_J=25°C	--	--	-1	uA
		V_{DS}=-24V , V_{GS}=0V , T_J=55°C	--	--	-5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	--	--	±100	nA
gfs	Forward Transconductance	V_{DS}=-5V , I_D=-6A	--	17	--	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	--	13	26	Ω
Q_g	Total Gate Charge	V_{DS}=-15V , V_{GS}=-4.5V , I_D=-5A	--	12.6	17.6	nC
Q_{gs}	Gate-Source Charge		--	4.8	6.7	
Q_{gd}	Gate-Drain Charge		--	4.8	6.7	
$t_{d(on)}$	Turn-On Delay Time	V_{DS}=-15V , V_{GS}=-10V , R_G=3.3Ω, I_D=-5A	--	4.6	9.2	ns
t_r	Rise Time		--	14.8	26.6	
$t_{d(off)}$	Turn-Off Delay Time		--	41	82	
t_f	Fall Time		--	19.6	39.2	
C_{iss}	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , f=1MHz	--	1345	1883	pF
C_{oss}	Output Capacitance		--	194	272	
C_{rss}	Reverse Transfer Capacitance		--	158	221	

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Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current [1,4]	$V_G=V_D=0V$, Force Current	--	--	-5.8	A
I_{SM}	Pulsed Source Current[2,4]		--	--	-24	A
V_{SD}	Diode Forward Voltage[2]	$V_{GS}=0V$, $I_S=-1A$, $T_J=25°C$	--	--	-1.2	V
t_{rr}	Reverse Recovery Time	$I_F=-5A$, di/dt=100A/μs , $T_J=25°C$	--	16.3	--	nS
Q_{rr}	Reverse Recovery Charge		--	5.9	--	nC

Note:

1. Test data conducted with surface mount attachment to 1 inch^2, FR-4 board utilizing 2oz copper
2. Pulse Test. Pulse width ≦ 300uS, duty cycle ≦ 2%
3. The power dissipation is limited by a 150°C maximum junction temperature
4. The data is theoretically the same as I_D and I_{DM}. In real applications, it will be limited by total power

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Typical Characteristics

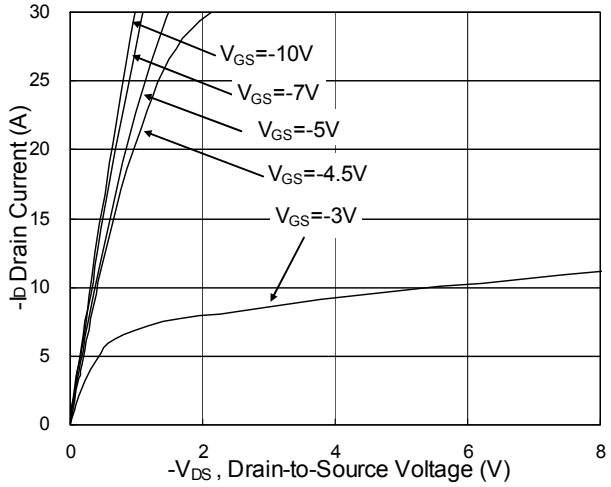


Fig.1: Typical Output Characteristics

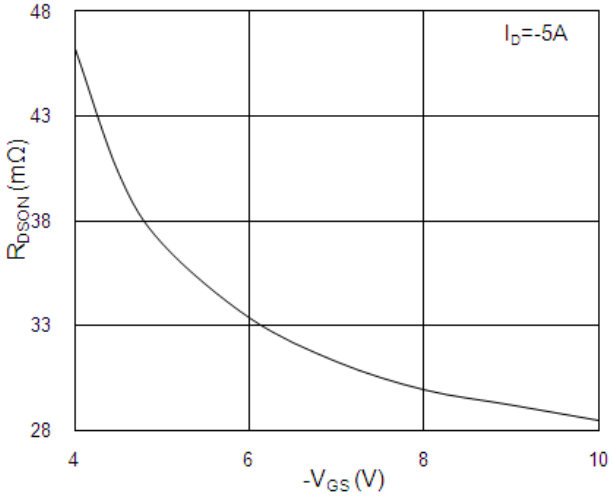


Fig.2: On-Resistance v.s Gate-Source

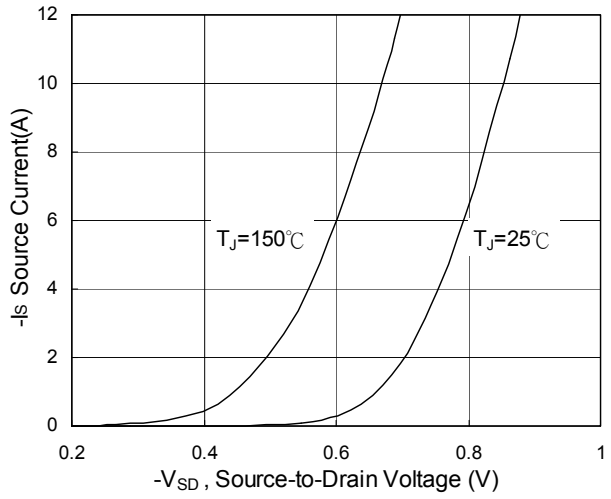


Fig.3: Forward Characteristics of Reverse

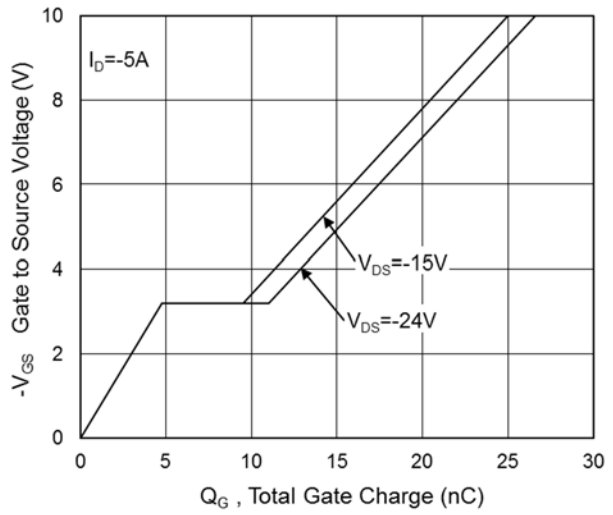


Fig.4: Gate-Charge Characteristics

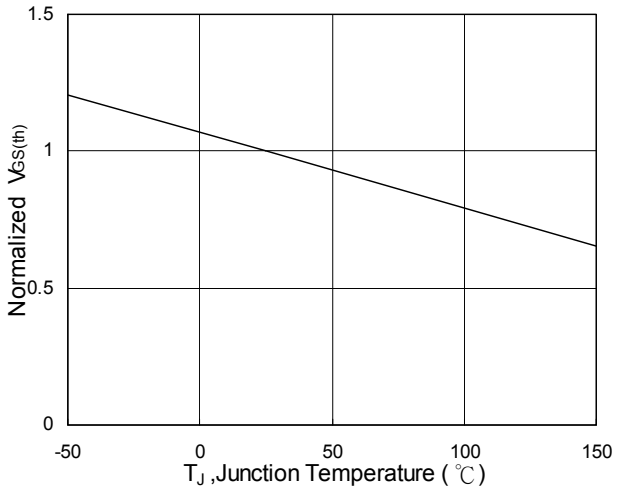


Fig.5: Normalized $V_{GS(th)}$ vs. T_J

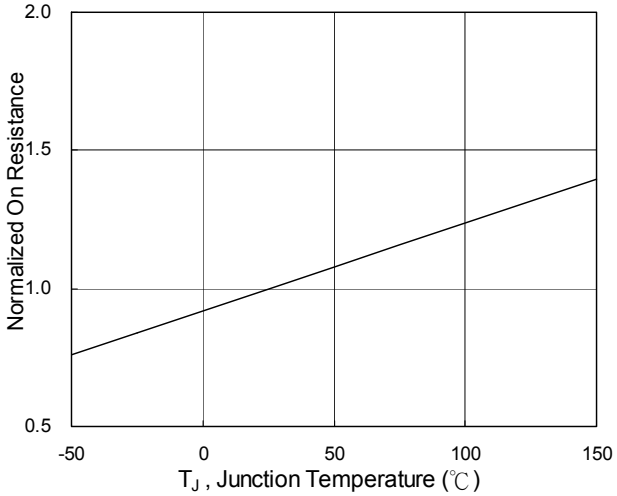


Fig.6: Normalized R_{DSON} vs. T_J

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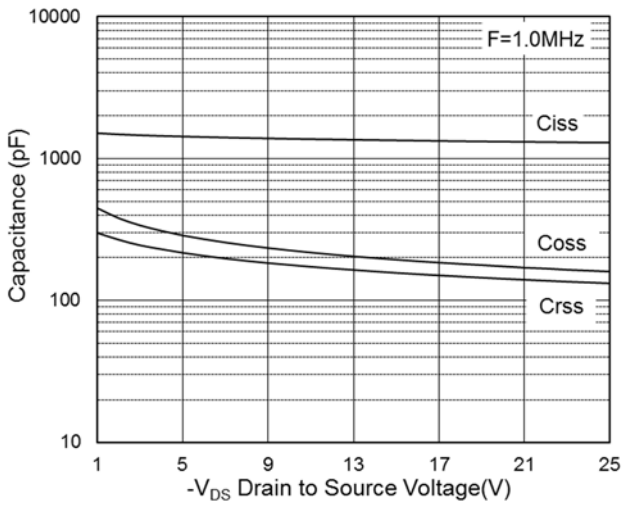


Fig.7: Capacitance

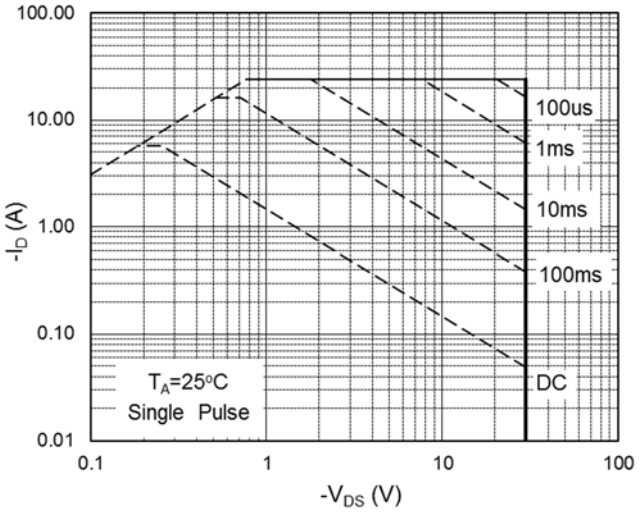


Fig.8: Safe Operating Area

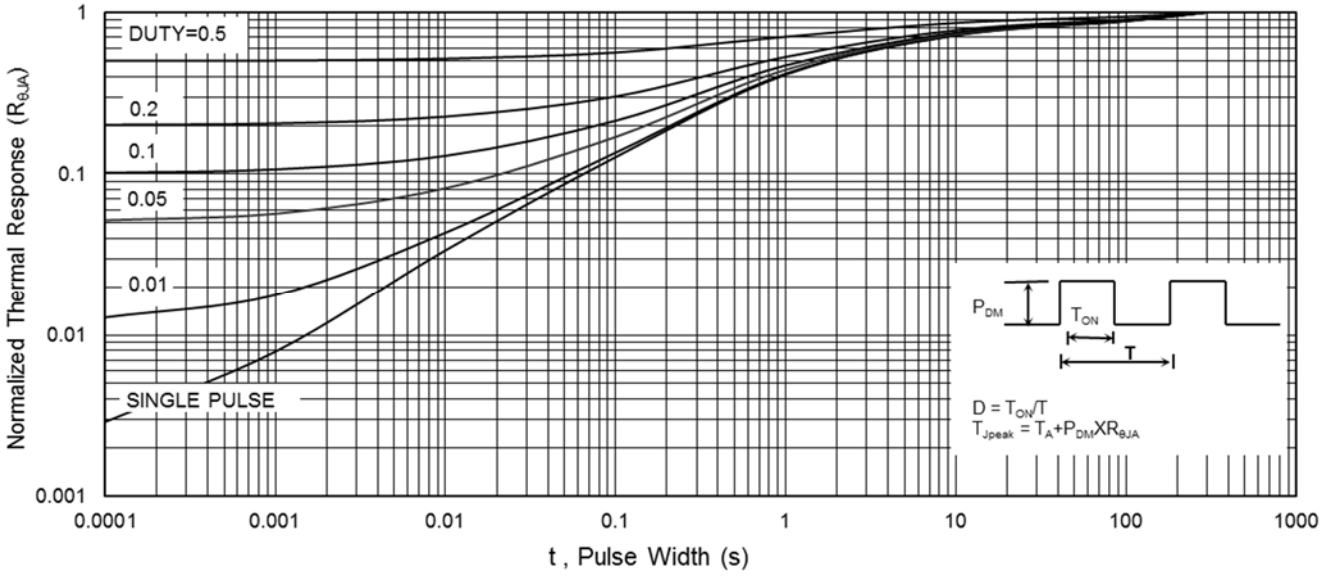


Fig.9: Normalized Maximum Transient Thermal Impedance

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