

# CCPD-914 Model 9X14 mm SMD, 3.3V, LVPECL

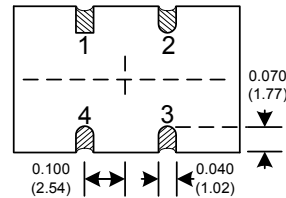
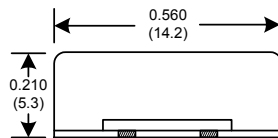
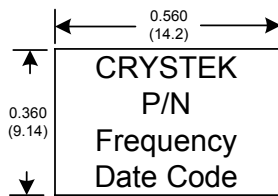


# Differential LVPECL Clock Oscillator

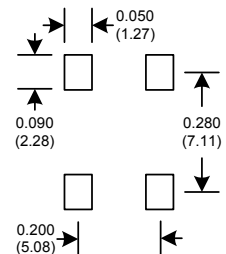


Designed to meet today's requirements for 3.3V Differential LVPECL applications. The CCPD-914 is a very low noise, low jitter clock oscillator for demanding telecom and other applications.

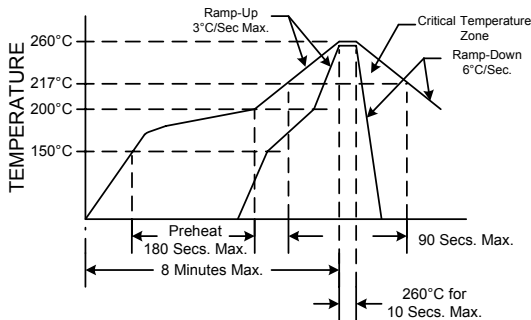
<b>Frequency Range:</b>	77.760MHz to 161.1328MHz
<b>Frequency Stability:</b>	±20ppm to ±100ppm
<b>Temperature Range:</b>	0°C to 70°C
(Option M)	0°C to 70°C
(Option X)	-40°C to 85°C
<b>Storage:</b>	-55°C to 120°C
<b>Input Voltage:</b>	3.3V ± 0.3V
<b>Input Current:</b>	55mA Typ, 88mA Max
<b>Output:</b>	Differential LVPECL
Symmetry:	45/55% Max @ 50% Vdd
Rise/Fall Time:	1ns Max @ 20% to 80% Vdd
Logic:	Terminated to Vdd-2V into 50 ohms
Temp. 0°C to 85°C	"0" = 1.490 Min, 1.680 Max
	"1" = 2.275 Min, 2.420 Max
Temp. -40°C to 0°C	"0" = 1.470 Min, 1.745 Max
	"1" = 2.215 Min, 2.420 Max
	200ns Max
Disable Time	1mSec Typ., 2mSec Max
Start-up Time	
<b>Jitter:</b>	12KHz to 20MHz
	0.45 psec Typ. @ 80 MHz, 1ps RMS Max
	0.25 psec Typ. @ 160 MHz, 1ps RMS Max
<b>Phase Noise:</b>	10Hz
	100Hz
	1KHz
	10KHz
	100KHz-100MHz
	-65dBc Typical
	-98dBc Typical
	-125dBc Typical
	-140dBc Typical
	-145dBc Typical
<b>Aging:</b>	<3ppm 1st/yr, <1ppm every year thereafter



### SUGGESTED PAD LAYOUT



### RECOMMENDED REFLOW SOLDERING PROFILE



NOTE: Reflow Profile with 240°C peak also acceptable.

### Crystek Part Number Guide

**CCPD-914 X - 25 - 155.520**

#1	#2	#3	#4	#5
#1 Crystek SMD PECL Osc.	#2 Model 914 = 9x14mm smd 3.3V	#3 Temp. Range: Blank = 0/70°C, M= -20/70°C, X= -40/85°C	#4 Stability: (see Table 1)	#5 Frequency in MHz: 3 or 6 decimal places

Stability Indicator:	
Blank (std)	± 100ppm
50	± 50ppm
25	± 25ppm
20 **	± 20ppm
** Available 0/70°C & -20/70°C	

Table 1

Example:  
CCPD-914X-25-155.520 = 3.3V, 45/55, -40/85°C, 25ppm, 155.520 MHz

Pad	Connection
1	COUT
2	GND
3	OUT
4	Vdd

Table 2

Specifications subject to change without notice.

TD-030301 Rev.G



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