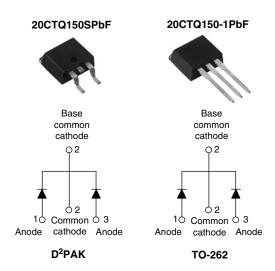




Vishay High Power Products

COMPLIANT

Schottky Rectifier, 2 x 10 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 10 A				
V _R 150 V				

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed for industrial level

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	20	A		
V _{RRM}		150	V		
I _{FSM}	$t_p = 5 \mu s \text{ sine}$	1030	А		
V _F	10 Apk, T _J = 125 °C (per leg)	0.66	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	20CTQ150SPbF 20CTQ150-1PbF	UNITS	
Maximum DC reverse voltage	V _R	150 V		
Maximum working peak reverse voltage	V_{RWM}	150	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		50 % duty cycle at T _C = 154 °C, rectangular waveform 20		10	
See fig. 5 per device	I _{F(AV)}			20	- A
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		. The chief of a particular particular	Following any rated load condition and with	1030	A
		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	180	
Non-repetitive avalanche energy per leg EA		$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 2 \text{mH}$		1.0	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1	А

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	10 A	- T _J = 25 °C	0.80	0.88	
		20 A		0.90	1.0	V
		10 A	- T _J = 125 °C	0.63	0.66	
		20 A		0.73	0.77	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3.0	25	μΑ
See fig. 2	'RM \"/	T _J = 125 °C	VR = nateu VR	2.7	5.0	mA
Typical junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) at 25 °C		-	280	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body			8.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R -		-	10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance,	per leg	D. DC an author	2.0			
junction to case	per package	□thJC	R _{thJC} DC operation		°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	R _{thCS} Mounting surface, smooth and greased (Only for TO-262)		5,	
A				2	g	
Approximate weight				0.07	oz.	
	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device			Case style D ² PAK	20CTQ150S		
			Case style TO-262	20CTC	150-1	



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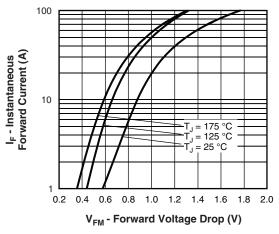


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

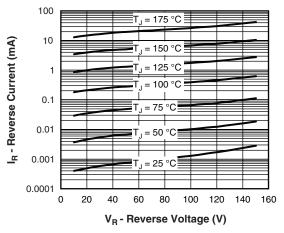


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

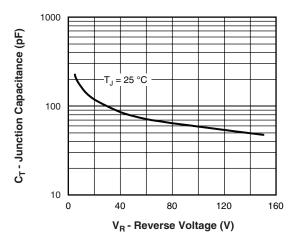


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

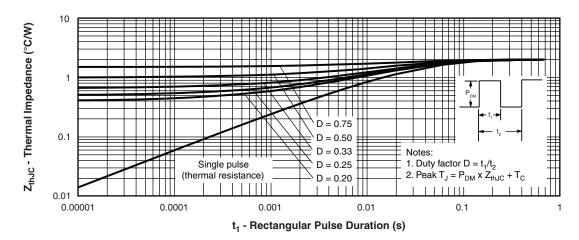


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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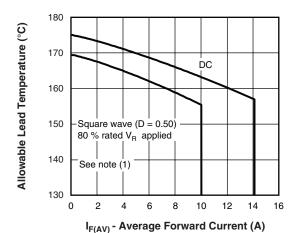


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

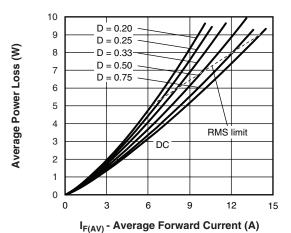


Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

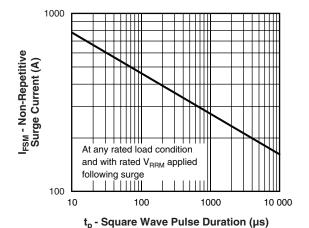


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

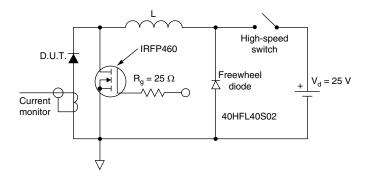


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

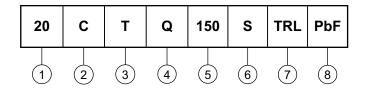


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ORDERING INFORMATION TABLE

Device code



1 - Current rating (20 = 20 A)

C = Common cathode

3 - T = TO-220

4 - Q = Schottky "Q" series

5 - Voltage rating (150 = 150 V)

6 - • -1 = TO-262

• $S = D^2PAK$

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95014			
Part marking information	http://www.vishay.com/doc?95008		
Packaging information http://www.vishay.com/doc?95032			

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