

# UNISONIC TECHNOLOGIES CO., LTD

BTB04A Preliminary TRIAC

# SENSITIVE GATE TRIACS

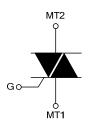
#### ■ DESCRIPTION

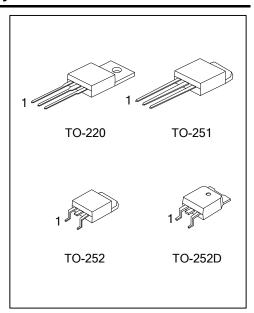
The UTC **BTB04A** is a 4A triacs which can be operated in 3 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances.

The UTC **BTB04A** is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

#### ■ FEATURES

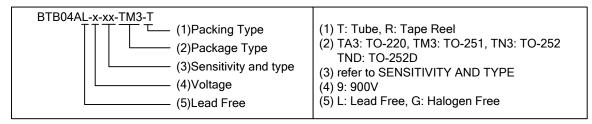
- \* Low gate trigger current
- \* Low holding current
- SYMBOL





## ■ ORDERING INFORMATION

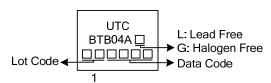
Ordering Number			Dookogo	Pin Assignment			Dooking	
Lead Free		Halogen Free	Package	1	2	3	Packing	
BTB04AL-x-xx-TA	3-T	BTB04AG-x-xx-TA3-T	TO-220	MT1	MT2	G	Tube	
BTB04AL-x-xx-TM	3-T	BTB04AG-x-xx-TM3-T	TO-251	MT1	MT2	G	Tube	
BTB04AL-x-xx-TN	3-R	BTB04AG-x-xx-TN3-R	TO-252	MT1	MT2	G	Tape Reel	
BTB04AL-x-xx-TN	D-R	BTB04AG-x-xx-TND-R	TO-252D	MT1	MT2	G	Tape Reel	



### ■ SENSITIVITY AND TYPE

PART NUMBER	VOLTAGE	SENSITIVITY	TYPE
SW	900V	10mA	LOGIC LEVEL

#### MARKING



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#### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT	
RMS On-State Current (360° Conduction Angle)	T <sub>C</sub> =90°C	I <sub>T(RMS)</sub>	4	Α
Non Repetitive Surge Peak On-State	t <sub>p</sub> =8.3ms	I <sub>TSM</sub>	42	Α
Current (T <sub>J</sub> initial=25°C)	t <sub>p</sub> =10ms	- 1 OW	40	Α
I <sup>2</sup> t Value	t <sub>p</sub> =10ms	l <sup>2</sup> t	8	$A^2s$
Critical Rate of Rise of On-State Current:	Repetitive F=50Hz	dI/dt	10	A/µs
I <sub>G</sub> =50mA, dI <sub>G</sub> /dt=0.1A/μs	Non Repetitive	dl/dt	50	A/µs
Repetitive Peak Off-State Voltage	400 T/D	\/ \/\	400	V
(T <sub>J</sub> =110°C)	600 T/S	$V_{DRM}/V_{RRM}$	600	V
Peak Gate Current	t <sub>p</sub> =20µs	$I_{GM}$	4	Α
Peak Positive Gate Voltage	t <sub>p</sub> =20µs	$V_{GM}$	16	V
Peak Positive Gate Power Dissipation	t <sub>p</sub> =20µs	$P_{GM)}$	40	W
Average Gate Power Dissipation	$P_{G(AV)}$	0.5	W	
Operating Junction Temperature	$T_J$	-40~+110	°C	
Storage Junction Temperature	T <sub>STG</sub>	-40~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	$\theta_{JA}$	60	°C/W	
Junction to Case for 360° Conduction Angle (F=50Hz) (AC)	TO-220		3	
	TO-251/TO-252 TO-252D	$\theta_{JC}$	3.6	°C/W

# ■ ELECTRICAL CHARACTERISTICS

#### FOR LOGIC LEVEL (3 QUADRANTS)

TON LOGIC LLVLL (3 QUADINAINTS)							
PARAMETER	SYMBOL	TEST CONDITIONS		SW			UNIT
FARAWETER	STIVIDOL			MIN	TYP	MAX	UNIT
Gate Trigger Current	I <sub>GT</sub>	V <sub>D</sub> =12V (DC)				10	mA
Gate Trigger Voltage	$V_{GT}$	$R_L$ =33 $\Omega$ $T_J$ =25 $^{\circ}$ C	1-11-111			1.5	V
Gate Non-Trigger Voltage	$V_{GD}$	$V_D=V_{DRM}, R_L=3.3k\Omega,$ $T_J=110$ °C	1-11-111	0.2			<b>V</b>
Time Gate Trigger	t <sub>GT</sub>	$V_D=V_{DRM}$ , $I_G=40$ mA, $dI_G/dt=0.5$ A/ $\mu$ s, $T_J=25$ °C	1-11-111		2		μs
Holding Current (Note)	I <sub>H</sub>	I <sub>T</sub> =100mA, Gate Open, T <sub>J</sub> =25°C				25	mA
Latabina Current	$I_L$ $I_G=1.2I_{GT}$ , $T_J=25^{\circ}C$ $I-III$ $II$	1-111		20		mA	
Latching Current			40		mA		
Peak On-State Voltage (Note)	$V_{TM}$	I <sub>TM</sub> =5.5A, t <sub>p</sub> =380μs, T <sub>J</sub> =25°C				1.65	V
Popotitivo Book Off State Current	I <sub>DRM</sub>	V <sub>DRM</sub> Rated, T <sub>J</sub> =25°C				0.01	mA
Repetitive Peak Off-State Current	I <sub>RRM</sub> V <sub>RRM</sub> Rated, T <sub>J</sub> =110°C				0.75	mA	
Critical Rate of Rise of Off-State Voltage (Note)	dV/dt	Linear Slope up to $V_D$ =67% $V_{DRM}$ , Gate Open, $T_J$ =110°C		10			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note)	(dV/dt)c	(dl/dt)c=1.8A/ms, T <sub>J</sub> =110°C			5		V/µs

Note: For either polarity of electrode MT2 voltage with reference to electrode MT1.

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