Zibo Seno Electronic Engineering Co., Ltd.

淄博圣诺

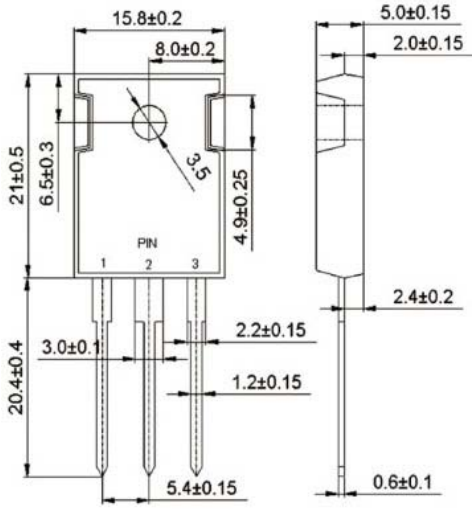
SF2020PT – SF2060PT

20.0A GLASS PASSIVATED SUPERFAST RECTIFIER

Features

- Glass Passivated Die Construction
- Super-Fast Switching
- Low Forward Voltage Drop
- Low Reverse Leakage Current
- High Surge Current Capability
- Plastic Material has UL Flammability Classification 94V-O

TO-3P/TO-247AD



Mechanical Data

- Case: TO-3P/TO-247AD, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 5.6 grams (approx.)
- Mounting Position: Any
- Mounting Torque: 11.5 cm-kg (10 in-lbs) Max.
- **Lead Free: For RoHS / Lead Free Version**

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	SF2020PT	SF2040PT	SF2060PT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	200	400	600	V
RMS Reverse Voltage	$V_{R(RMS)}$	140	280	420	V
Average Rectified Output Current @T_C = 125°C	I_O	20.0			A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	250			A
Forward Voltage @I_F=10 .0A	V_{FM}	0.98	1.3	1.7	V
Peak Reverse Current @T_A = 25°C At Rated DC Blocking Voltage @T_A = 100°C	I_{RM}	10 400			μA
Reverse Recovery Time (Note 1)	t_{rr}	50			nS
Typical Junction Capacitance (Note 2)	C_j	170		130	pF
Operating and Storage Temperature Range	T_j, T_{STG}	-55 to +150			°C

Note: 1. Measured with IF = 0.5A, IR = 1.0A, IRR = 0.25A.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

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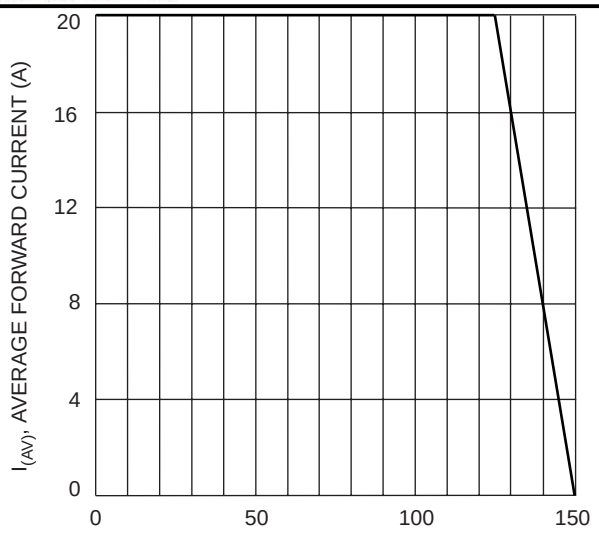


Fig. 1 Forward Current Derating Curve

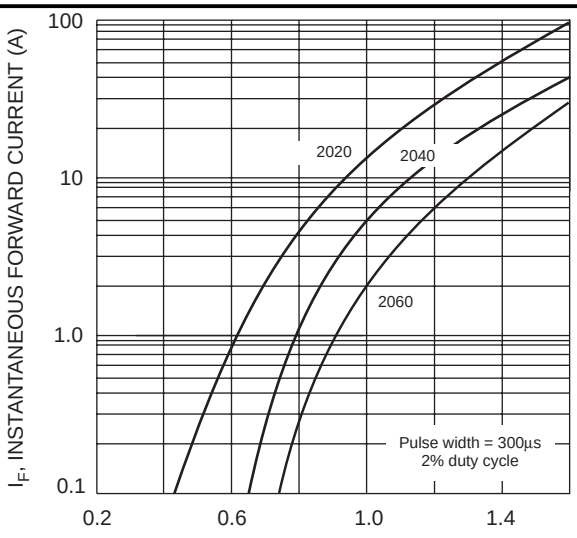


Fig. 2 Typical Forward Characteristics

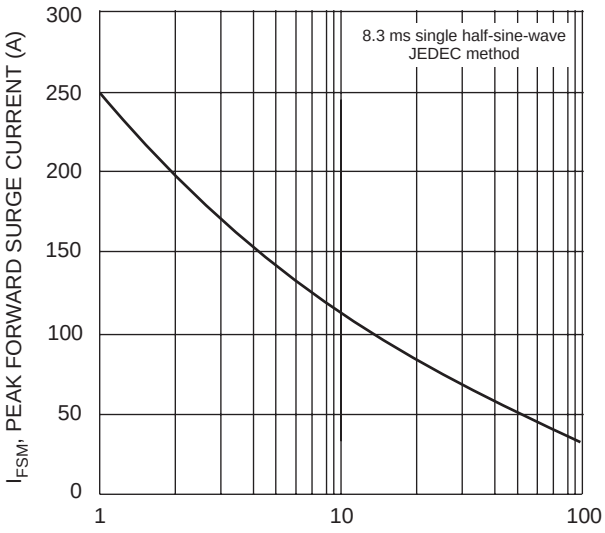


Fig. 3 Max Non-Repetitive Surge Current

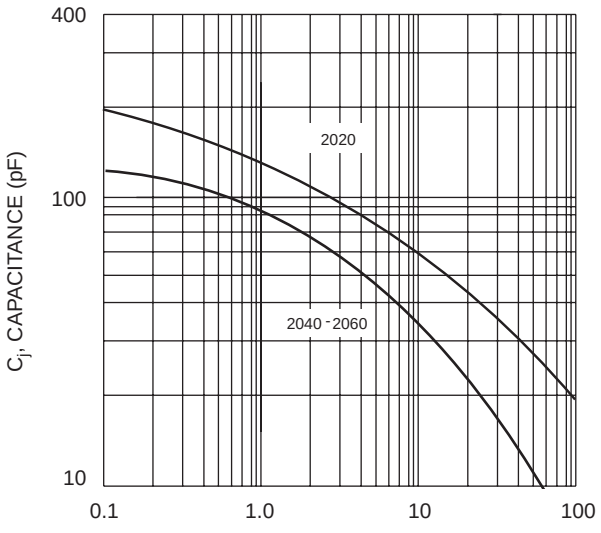
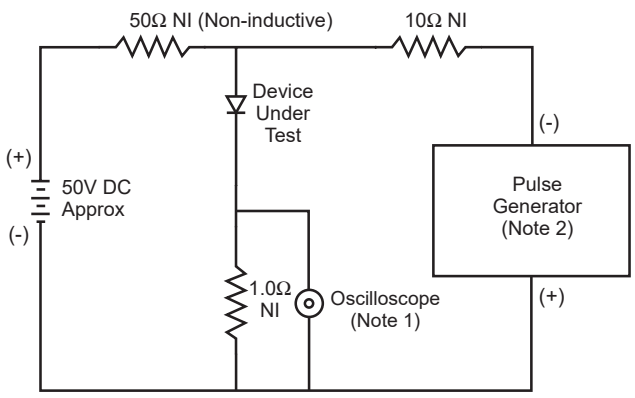
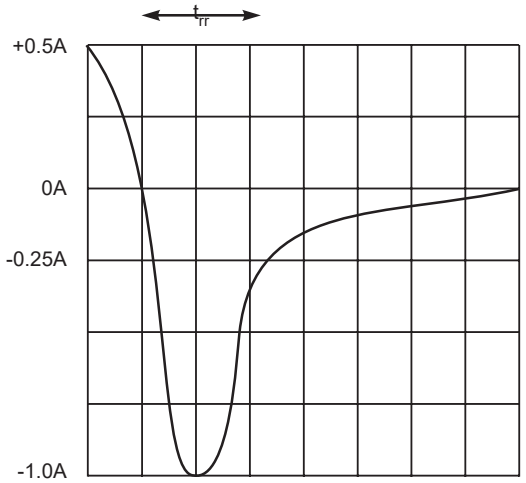


Fig. 4 Typical Junction Capacitance





Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
2. Rise Time = 10ns max. Input Impedance = 50Ω.

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

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