

Non-isolated Buck APFC Offline LED Driver

Features

- Active PFC for High Power Factor and Low THD
- Critical Conduction Mode Operation
- System Efficiency up to 95%
- Without auxiliary winding
- Excellent Line and Load Regulation
- $\pm 3\%$ LED Output Current Accuracy
- Thermal Regulation Function
- LED Short/Open Protection
- Current Sensing Resistor Open Protection
- Auto Fault Recovery
- Available in SOT23-6 Package

Applications

- E27 LED Bulb, Spot Light
- PAR30, PAR38 Lamp
- T8/T10 LED String
- Other LED Lighting

Description

The WS3410 is a high precision non-isolated buck driver with active PFC, specially designed for universal input offline constant current LED lighting. The driver with on-chip PFC circuit achieves high power factor and low THD. Operating in critical conduction mode, the power MOSFET switching loss is reduced and the inductor is fully utilized.

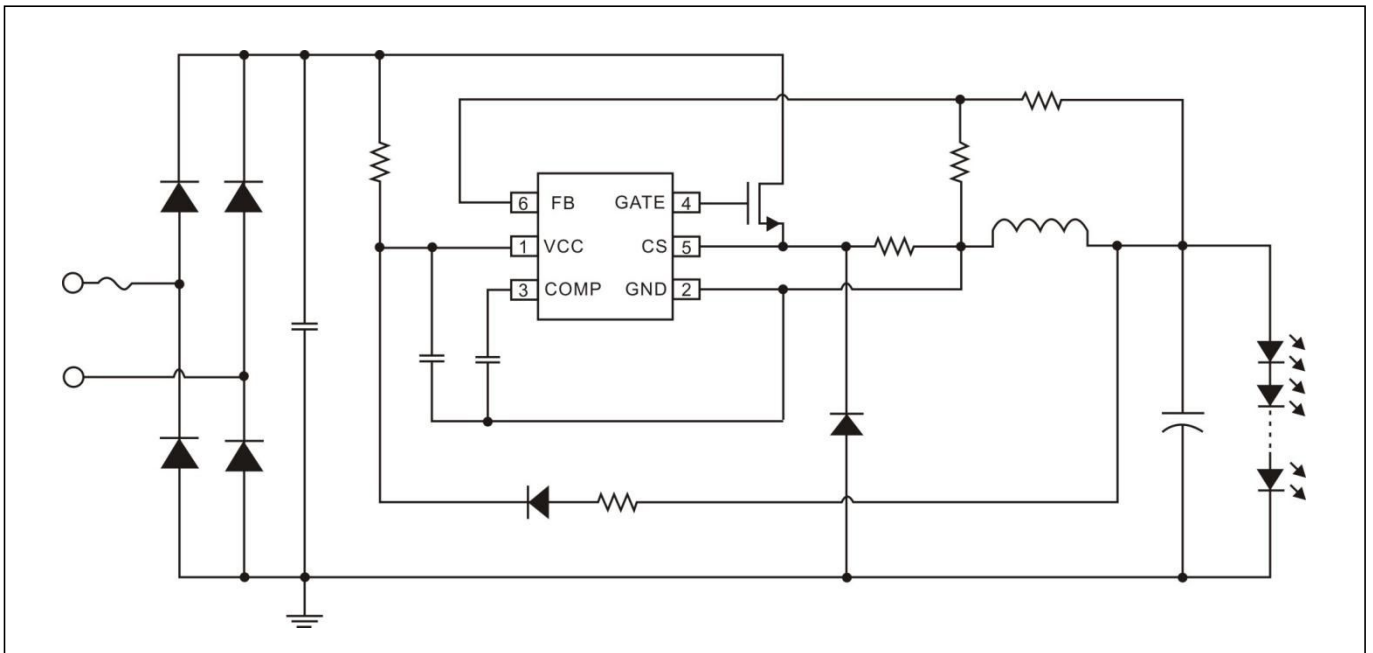
With few external components, the LED output current can be precisely controlled.

The WS3410 offers rich protection functions to improve the system reliability, including LED open circuit protection, LED short circuit protection, CS resistor open circuit protection and cycle by cycle current limit. All the protection functions are auto-recovery. The system reliability is further improved by the thermal regulation function. The output current is reduced when the driver is over temperature.

The WS3410 has ultra low start up current and ultra low operating current.

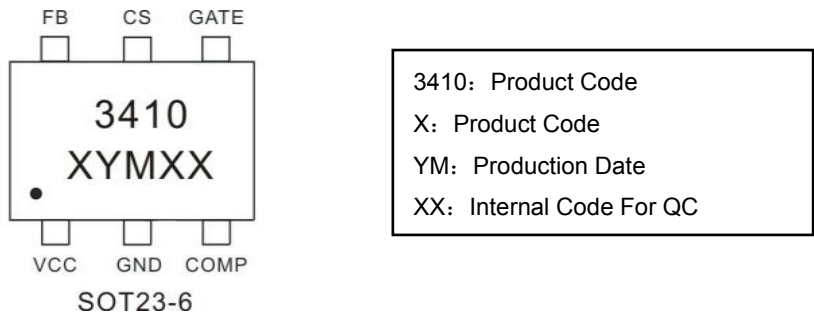
The WS3410 is available in SOT23-6 Package

Typical Application



Pin Configuration and Marking Information

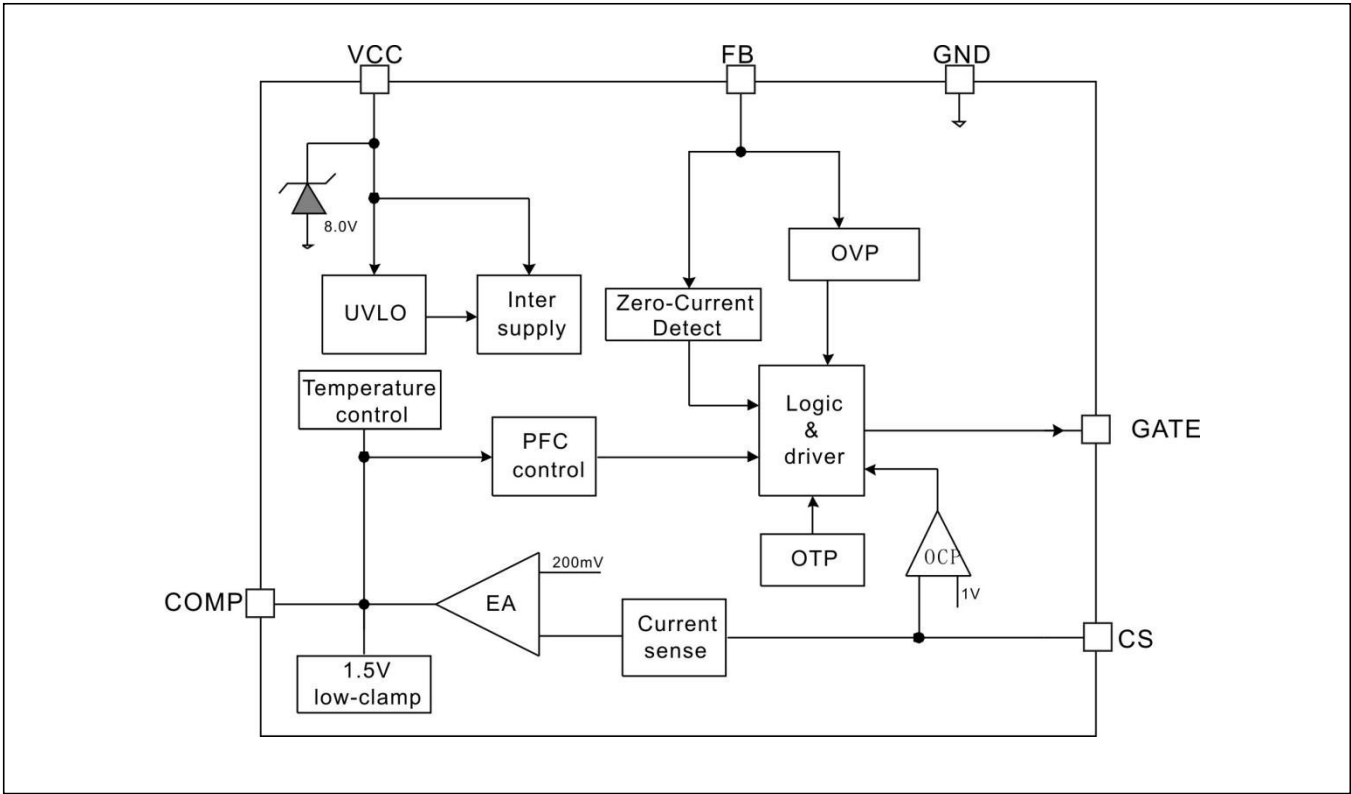
The WS3410 is available in SOT23-6 Package, the top marking is shown as below:



Pin Definition

Name	Pin No.	Description
VCC	1	Power Supply Pin. Connect a bypass capacitor from this pin to GND.
GND	2	Ground.
COMP	3	Loop Compensation Node. This pin connects a capacitor to GND for stabilization of the control loop, achieving accurate LED current, high Power Factor and low THD.
GATE	4	Gate Driver Pin. Connect this pin to the gate of external power MOSFET.
CS	5	Current Sense Pin. Connect a resistor to GND to sense the inductor current.
FB	6	Feedback Voltage Input Pin. This pin detects the inductor demagnetization signal and the output voltage.

Internal Block Diagram



Ordering Information

Package	Marking	Part Number
6-Pin SOT23-6, Pb-free	WS3410YP	WS3410YP

Recommended Operation Conditions

Part Number	Package	Input voltage	Maximum output power
WS3410YP	SOT23-6	175VAC-264VAC	120V/450mA
		90VAC-264VAC	80V/450mA

Absolute Maximum Ratings

symbol	paramete	Range	unit
V_{CC}	Vcc pin voltage	-0.3~8	V
V_{gate}	Gate driver pin voltage	-0.3~8	V
V_{CS}	Current sense pin input voltage	-0.3~7	V
V_{FB}	Feedback pin input voltage	-0.3~7	V
V_{COMP}	Compensation pin voltage	-0.3~7	V
P_{DMAX}	Power dissipation	0.2	W
T_J	Operating junction temperature	165	°C
T_{STG}	Storage temperature range	-55~165	°C

Note : Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device.

Electrical Characteristics (Unless otherwise specified, VCC=8V and TA =25 °C)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Supply Voltage Section						
V _{cc_clamp}	VCC Clamp Voltage		7.5	8.0	8.5	V
I _{cc_clamp}	VCC Clamp Current			5		mA
V _{CC_ST}	VCC Turn On Threshold	VCC Rising	7.07	7.57	8.07	V
V _{uvlo_HYS}	VCC Turn Off hysteresis	VCC Falling		1.32		V
I _{st}	VCC Startup Current	VCC<VCC_ST-0.5V		70	100	uA
I _{op}	VCC Operating Current			400		uA
Current Sense Section						
V _{ocp}	Over current Protectionthreshold			1		V
T _{LEB}	Leading Edge Blanking Time for Current Sense			350		ns
T _d	Switch off Delay Time			200		ns
Compensation Section						
V _{REF}	Internal Reference Voltage		194	200	206	mV
V _{CL}	COMP Low Clamp Voltage			1.5		V
V _{CH}	COMP High Clamp Voltage			4		V
Internal driver						
T _{OFF_MIN}	Minimum Off Time			3		us
T _{ON_MAX}	Maximum On Time			20		us
Feedback Section						
V _{FB}	FB Over Voltage Protection Threshold			1.6		V
V _{ZCD}	FB Falling Edge Threshold Voltage			0.2		V
Thermal Regulation Section						
T _{REG}	Thermal Regulation Temperature			150		°C

Application Information

The WS3410 is a high precision Active PFC driver, specially designed for non-isolated buck offline constant current LED lighting. Operating in critical conduction mode, the driver achieves high power factor, low THD and high efficiency.

Start Up

After system powered up, the VCC pin capacitor is charged up by the start up resistor. When the VCC pin voltage reaches the turn on threshold, the internal circuits start working. The COMP pin voltage is pulled up to 1.5V quickly. Then the WS3410 starts switching. The system works at 10k Hz frequency at the beginning, the COMP voltage rises up gradually, and the inductor peak current also rises up. The LED current hence achieves a soft start without overshoot. After the output voltage is built up, the VCC power is supplied by the output voltage through a diode.

The maximum value of the startup current is 100uA. For the application of 85VAC—264VAC, the startup resistor can be calculated by the equation:

$$R_{st} < \frac{V_{in_min}}{I_{st_max}} \approx \frac{85V}{100\mu A} = 850K\Omega$$

Constant Current Control

The WS3410 utilizes floating ground structure. The inductor current is sensed during the whole switching cycle, thus it achieves high precision output current control and also excellent line and load regulation.

The current in LED can be calculated by the equation:

$$I_o = \frac{V_{CS}}{R_{CS}} = \frac{200mV}{R_{CS}}$$

Rcs is the current sensing resistor.

Feedback Network

The WS3410 senses the output current zero crossing information through the feedback network, the FB falling threshold voltage is set to 0.2V with 0.15V hysteresis. The FB pin is also used to detect output OVP, the threshold voltage is 1.6V. The ratio of FB upper resistor to lower resistor can be set as:

$$\frac{R_{FBL}}{R_{FBL} + R_{FBH}} = \frac{1.6V}{V_{OVP}}$$

Where,

RFBH: The lower resistor of the feedback network

RFBH: The upper resistor of the feedback network

VOVP: Output over voltage setting point

The minimum VOVP should be set about 1.3Vo. It is recommended that the FB lower resistor is set to 3.3KΩ—5.1KΩ paralleled with a 22pF ceramic capacitor.

Protection Functions

The WS3410 offers rich protection functions to improve the system reliability.

When the LED is open circuit, the output voltage will gradually rise up. The output voltage is sensed by the FB pin when power MOSFET is turned off. When FB voltage reaches the OVP threshold, it will trigger fault logic and the system stops switching.

When the LED is shorted circuit, the system will work under 10k Hz switching frequency. Meanwhile, the output voltage is low and the VCC pin cannot be charged up by the output voltage, so the VCC pin voltage will gradually decrease and finally reaches the UVLO threshold.

After the system enters into fault condition, the VCC voltage will decrease until it reaches UVLO threshold. Then the system will re-start again. If the fault condition is removed, the system will resume normal operation.

When the output is short circuit or the inductor is saturated, the CS peak voltage will be relatively high. When CS voltage reaches the internal limitation (1V), the power MOSFET will be turned off instantaneously. This cycle by cycle current limitation can help protecting the power MOSFET, the inductor and the output diode.

Thermal Regulation

The WS3410 integrates thermal regulation function. When the system is over temperature, the output current is gradually reduced; the output power and thermal dissipation are also reduced. The system temperature is regulated and the system reliability is improved. The thermal regulation temperature is set to 150°C internally.

PCB Layouts

The following guidelines should be followed in WS3410 PCB layout:

Bypass Capacitor

The bypass capacitor on VCC pin should be as close as possible to the VCC and GND pins.

Ground Path

The power ground path for current sense resistor should be short and wide, and it should be as close as possible to the IC ground (pin 2), otherwise the LED output current accuracy maybe affected. The IC signal ground for COMP and FB components should be connected to the IC GND pin with short traces and should be away from the power ground path.

The Area of Power Loop

The area of main current loop should be as small as possible to reduce EMI radiation.

FB Pin

The feedback resistor divider should be as close as possible to the FB pin, and the trace must keeps away from dynamic node of the inductor (DRAIN pin trace、the positive pin and the negative pin of Vo trace), otherwise the FB pin OVP function might have risk to be mis-triggered by the system noise.

DRAIN Pin

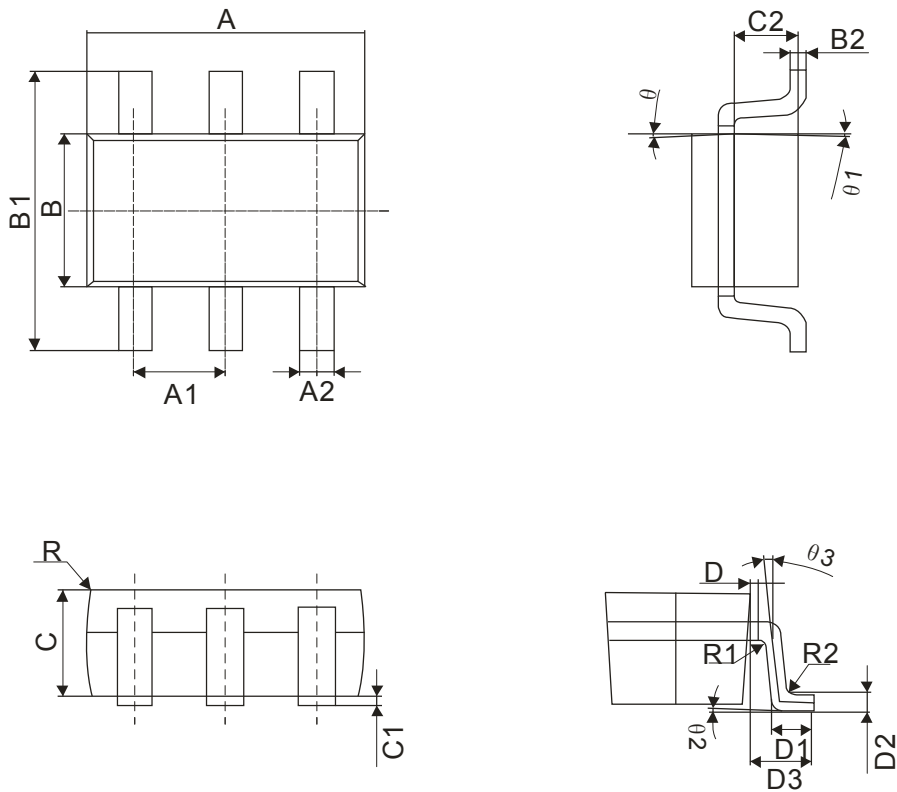
To increase the copper area of DRAIN pin for better thermal dissipation. However, too large copper area may compromise EMI performance.

The material of PCB

Avoid choosing the material that is easy to absorb the moisture, just like paper copper-clad laminates.

Package Information
SOT23-6Package Outline Dimensions

Unit:mm



Symbol	Winsemi			
	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	2.72	3.12	0.107	0.123
B	1.40	1.80	0.055	0.071
C	1.00	1.20	0.039	0.047
A1	0.90	1.00	0.035	0.039
A2	0.30	0.50	0.012	0.020
B1	2.60	3.00	0.102	0.118
B2	0.119	0.135	0.005	0.005
C1	0.03	0.15	0.001	0.006
C2	0.55	0.75	0.022	0.030
D	0.03	0.13	0.001	0.005
D1	0.30	0.60	0.012	0.024
D2	0.25TYP		0.01TYP	
D3	0.60	0.70	0.024	0.028

NOTE:

- 1.We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.
- 2.Please do not exceed the absolute maximum ratings of the device when circuit designing.
- 3.Winsemi Microelectronics Co., Ltd reserved the right to make changes in this specification sheet and is subject to change without prior notice.

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