

Transient Voltage Suppressors Array for ESD Protection

Low Capacitance

ESD05V26T-2L

Description

The ESD05V26T-2L is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

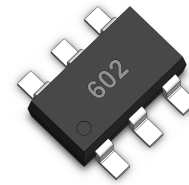
Feature

- u 300 Watts Peak Pulse Power per Line (tp=8/20µs)
- u Protects up to two I/O lines & power line
- u Low capacitance (1.2pF typ.) for high-speed interfaces
- u Working voltages : 5V
- u Ultra-small package requires less than 2.9mm² of PCB area
- u IEC61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
- u IEC61000-4-4 (EFT) 40A (5/50ns)

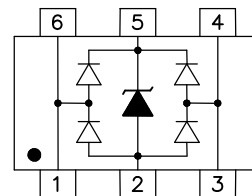
Applications

- u USB 2.0 High Speed
- u 10/100/1000 Ethernet Ports
- u High-Definition Multimedia Interface (HDMI)
- u Digital Visual Interface (DVI)
- u Monitors and Flat Panel Displays
- u Video Graphics Cards
- u IEEE 1394 Fire wire Ports High Speed

SOT-26



Functional Diagram



Mechanical Characteristics

- u JEDEC SOT-26 Package
- u Molding Compound Flammability Rating : UL 94V-0
- u Weight 13.0 Milligrams (Approximate)
- u Quantity Per Reel : 3,000pcs
- u Reel Size : 7 inch
- u Lead Finish : Lead Free

Mechanical Characteristics

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (tp=8/20µs waveform)	300	W
T _L	Lead Soldering Temperature	260 (10sec)	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C
T _J	Operating Temperature Range	-55 to +150	°C
	IEC61000-4-2 (ESD)	Air Discharge ±15	KV
		Contact Discharge ±8	
	IEC61000-4-4 (EFT)	40	A

Transient Voltage Suppressors Array for ESD Protection

Low Capacitance

ESD05V26T-2L

Electrical Characteristics (@ 25°C Unless Otherwise Specified)

Part Number	Device Marking	V _{RWM} (V) (Max.)	V _B (V) (Min.)	I _T (mA)	V _C @1A (Max.)	V _C		I _R (μA) (Max.)	C (pF) (Typ.)
						(Max.)	(@A)		
ESD05V26T-2L	602	5	6	1	13	20	5	1	1.2

Characteristic Curves

Fig1. 8/20μs Pulse Waveform

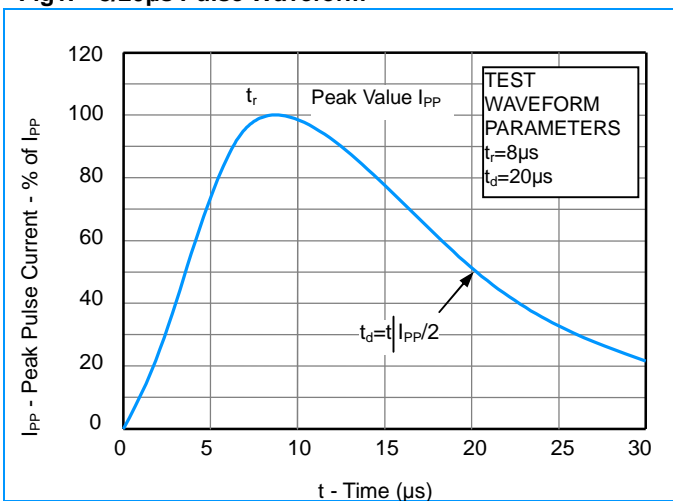


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

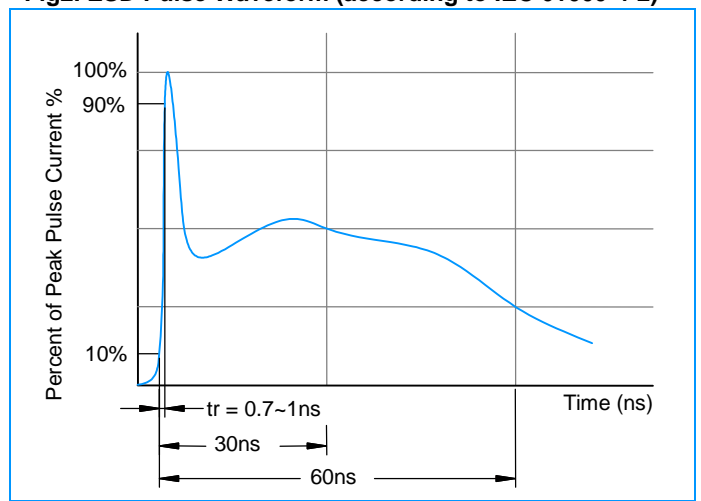
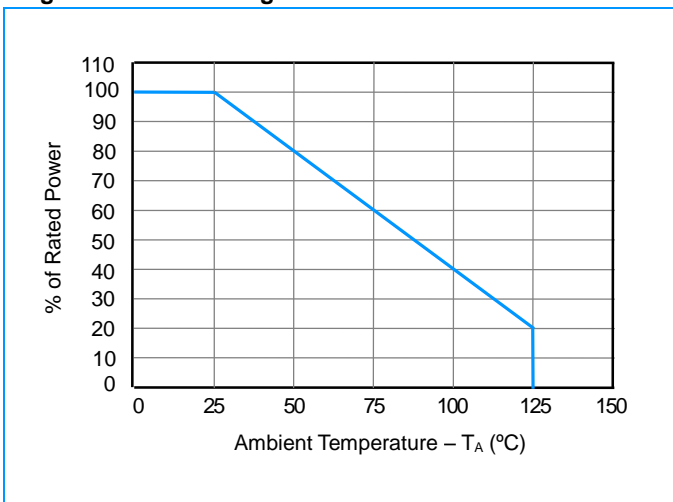


Fig3. Power Derating Curve



Transient Voltage Suppressors Array for ESD Protection

Low Capacitance

ESD05V26T-2L

Characteristic Curves

Fig4. ESD Clamping (+8KV Contac per IEC61000-4-2)

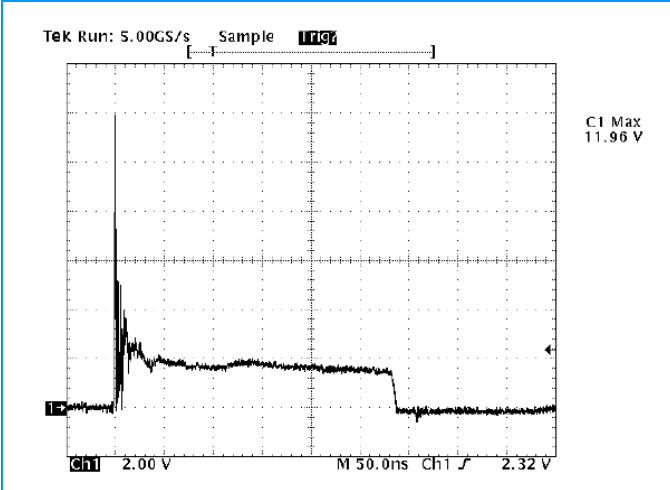
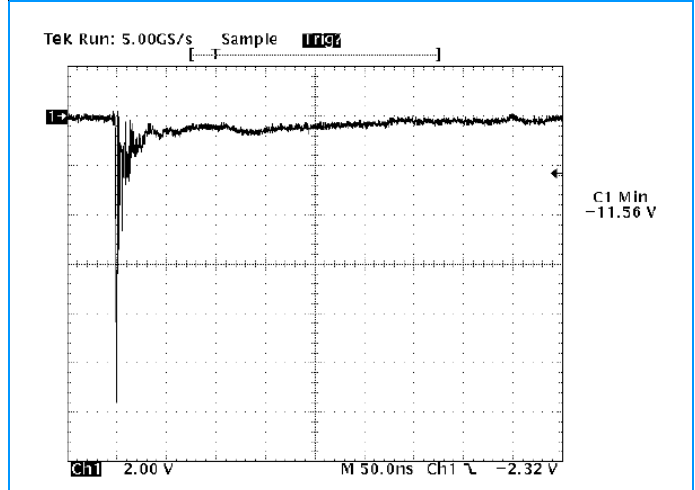
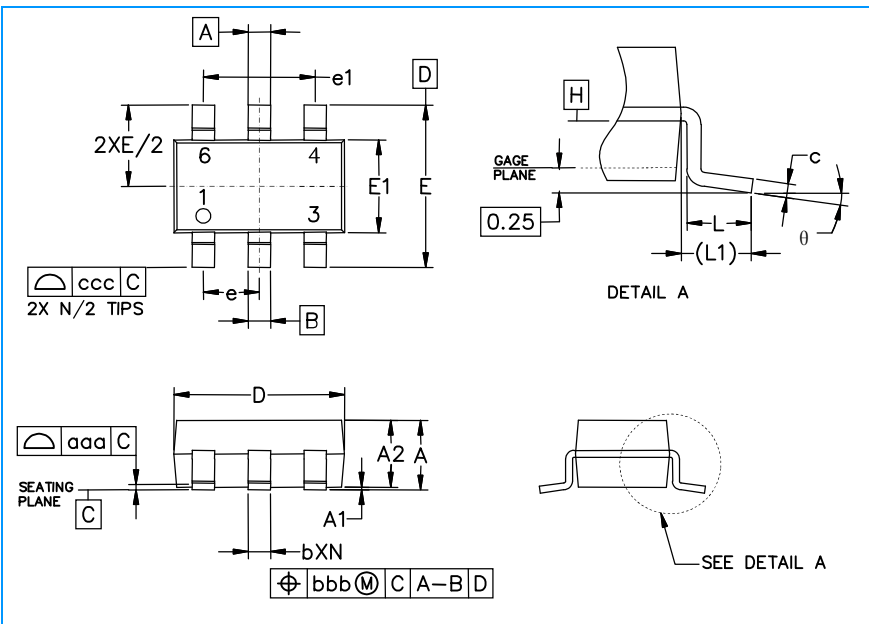


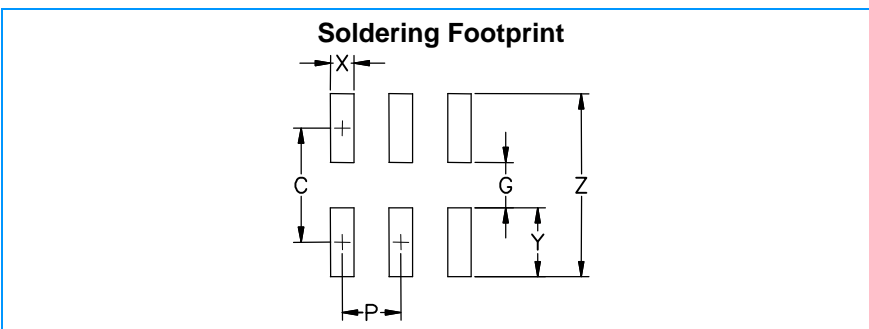
Fig5. ESD Clamping (-8KV Contac per IEC61000-4-2)



SOT-26 Package Outline & Dimensions



Symbol	Inches			Millimeters		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	0.035	-	0.057	0.90	-	1.45
A1	0.000	-	0.006	0.00	-	0.15
A2	0.035	0.045	0.051	0.90	1.15	1.30
b	0.010	-	0.020	0.25	-	0.50
c	0.003	-	0.009	0.08	-	0.22
D	0.110	0.114	0.122	2.80	2.90	3.10
E1	0.060	0.063	0.069	1.50	1.60	1.75
E	0.110 BSC			2.80 BSC		
e	0.037 BSC			0.95 BSC		
e1	0.075 BSC			1.90 BSC		
L	0.012	0.018	0.024	0.30	0.45	0.60
L1	(0.024)			(0.60)		
θ	0°	-	10°	0°	-	10°
aaa	0.004			0.10		
bbb	0.008			0.20		
ccc	0.008			0.20		



Symbol	Inches	Millimeters
C	(0.098)	(2.50)
G	0.055	1.40
P	0.037	0.95
X	0.024	0.60
Y	0.043	1.10
Z	0.141	3.60