CMD271P3

6-11 GHz Low Noise Amplifier

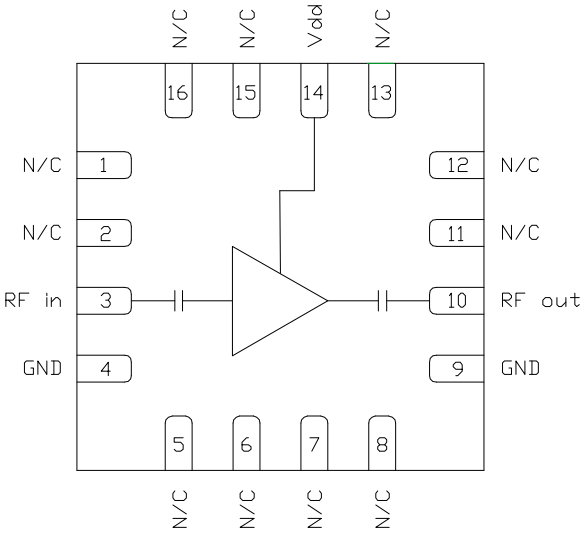
Features

- Low noise figure
- High IP3
- High gain broadband performance
- Single positive supply voltage
- Pb-free RoHs compliant 3x3 QFN package

Description

The CMD271P3 is a broadband MMIC low noise amplifier housed in a leadless 3x3 mm plastic surface mount QFN package. The CMD271P3 is ideally suited for EW and communications systems where small size and low power consumption are needed. The broadband device delivers 20 dB of gain with a corresponding output IP3 of +27 dBm and a noise figure of 1.8 dB at 8 GHz. The CMD271P3 is a 50 ohm matched design eliminating the need for external DC blocks and RF port matching.

Functional Block Diagram



Electrical Performance - V_{dd} = 5.0 V, T_A = 25 °C, F = 8 GHz

Parameter	Min	Typ	Max	Units
Frequency Range		6 - 11		GHz
Gain		20		dB
Noise Figure		1.8		dB
Input Return Loss		15		dB
Output Return Loss		15		dB
Output P1dB		16.5		dBm
Output IP3		27		dBm
Supply Current		66		mA

ver 1.0 0919

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Specifications

Absolute Maximum Ratings

Parameter	Rating
Drain Voltage, Vdd	5.5 V
RF Input Power	+20 dBm
Channel Temperature, Tch	150 °C
Power Dissipation, Pdiss	537 mW
Thermal Resistance, Θ_{JC}	121 °C/W
Operating Temperature	-40 to 85 °C
Storage Temperature	-55 to 150 °C

Exceeding any one or combination of the maximum ratings may cause permanent damage to the device.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Vdd	3.0	5.0	5.25	V
Idd		66		mA

Electrical performance is measured at specific test conditions. Electrical specifications are not guaranteed over all recommended operating conditions.

Drain Current vs. Drain Voltage

Vdd (V)	Idd (mA)
3.0	30
4.0	46
5.0	66

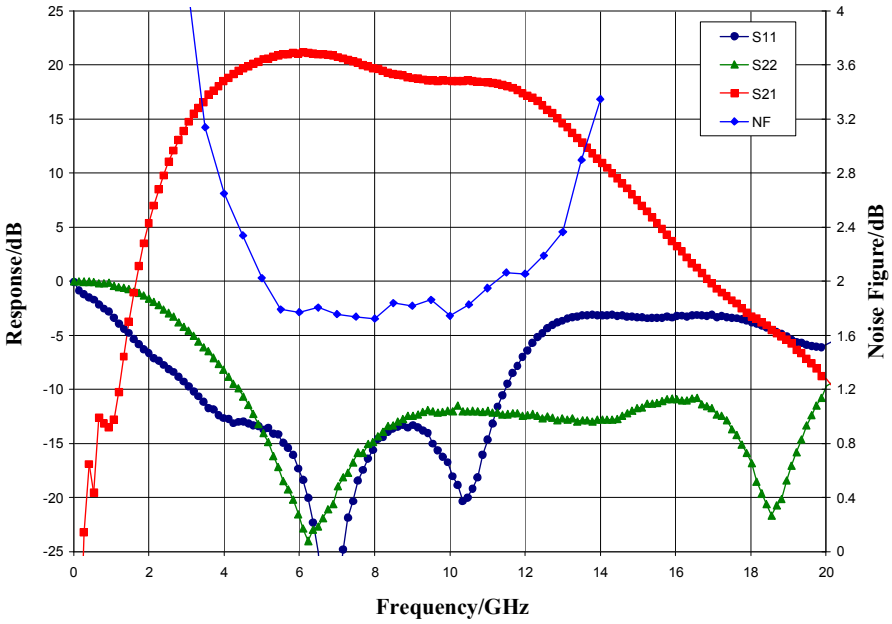
Electrical Specifications - V_{dd} = 5.0 V, T_A = 25 °C

Parameter	Min	Typ	Max	Min	Typ	Max	Units
Frequency Range	6 - 8			8 - 11			GHz
Gain	17	20		15.5	18.5		dB
Noise Figure		1.8	2.3		1.9	2.4	dB
Input Return Loss		18			15		dB
Output Return Loss		18			12		dB
Output P1dB		16.5			16.5		dBm
Output IP3		26.5			26.5		dBm
Supply Current	46	66	86	46	66	86	mA
Gain Temperature Coefficient		0.015			0.015		dB/°C
Noise Figure Temperature Coefficient		0.008			0.008		dB/°C

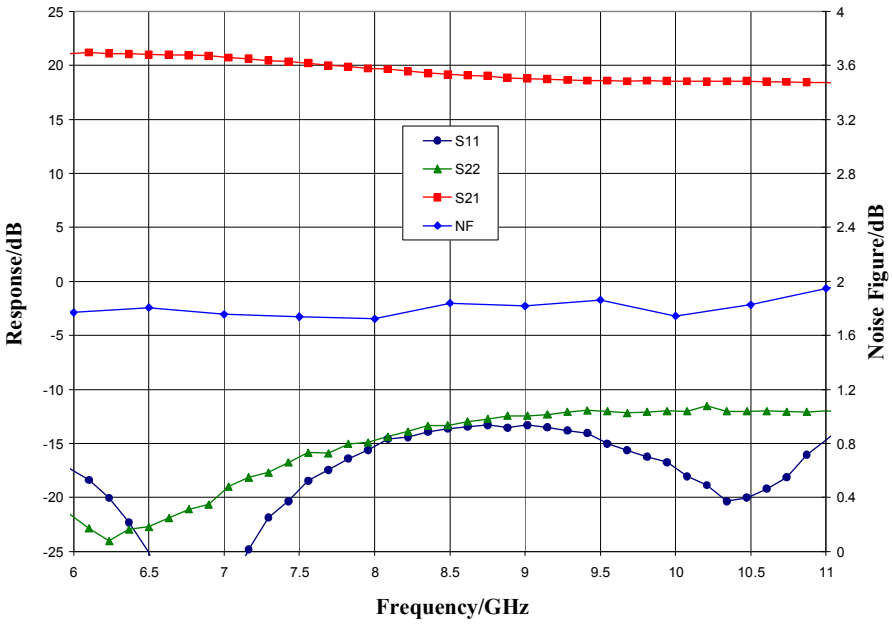
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Typical Performance

Broadband Performance, V_{dd} = 5.0 V, T_A = 25 °C



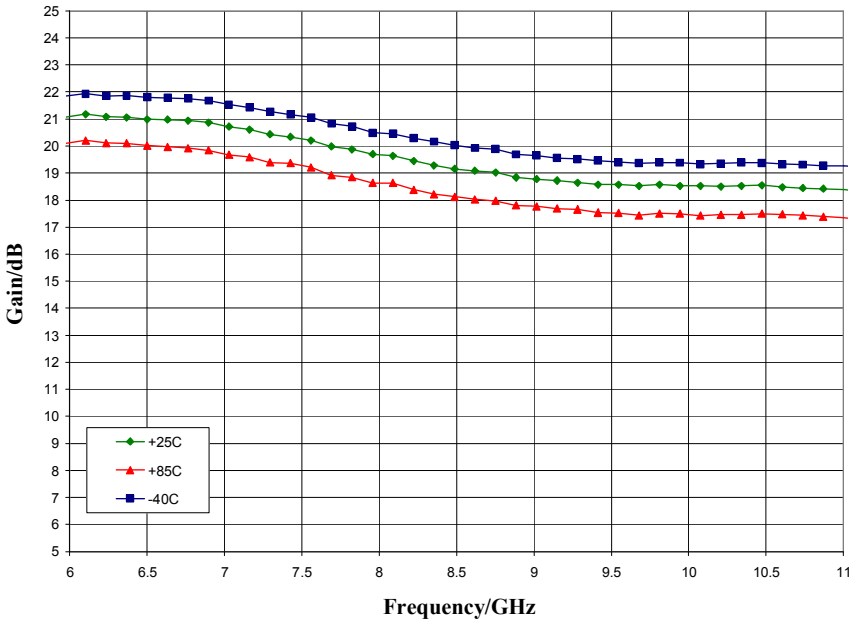
Narrow-band Performance, V_{dd} = 5.0 V, T_A = 25 °C



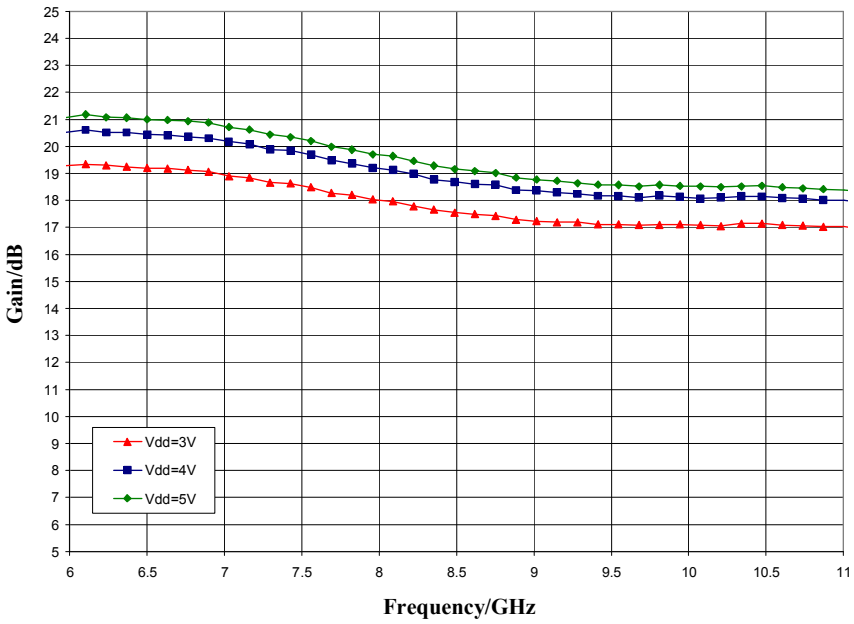
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Typical Performance

Gain vs. Temperature, V_{dd} = 5.0 V



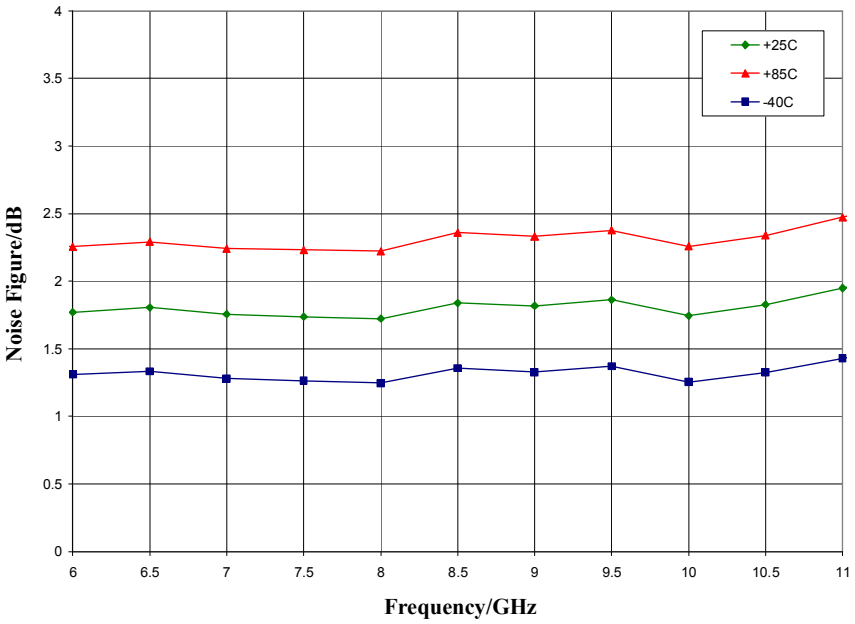
Gain vs. Vdd, T_A = 25 °C



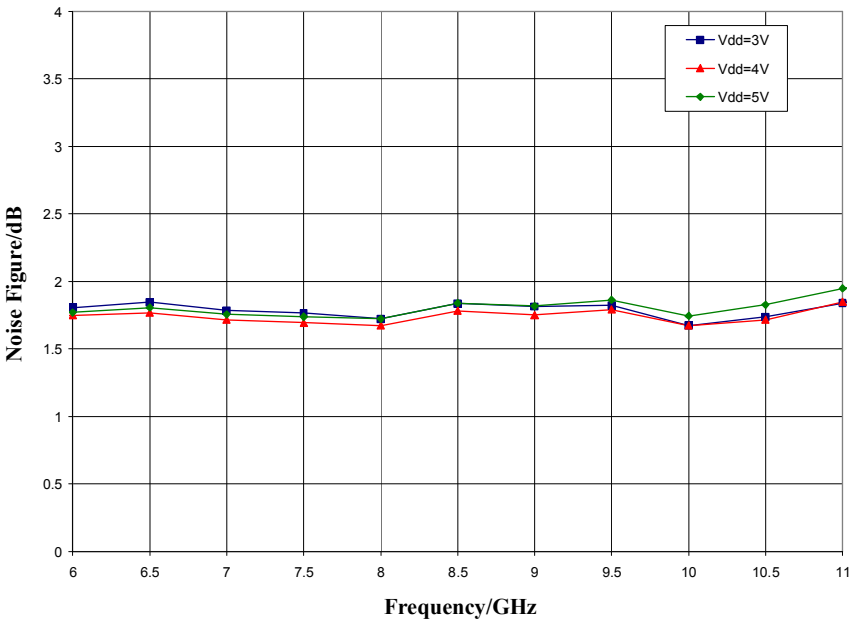
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Typical Performance

Noise Figure vs. Temperature, V_{dd} = 5.0 V



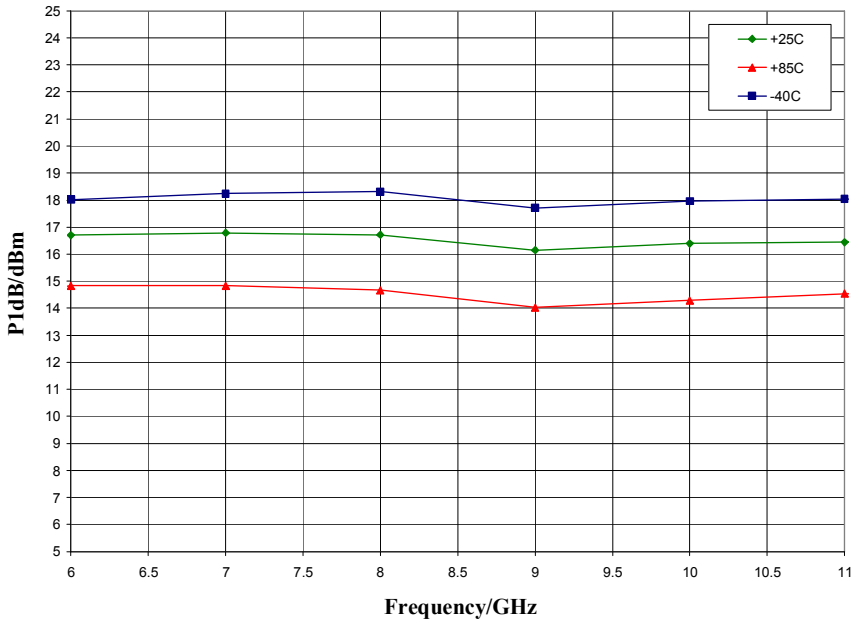
Noise Figure vs. Vdd, T_A = 25 °C



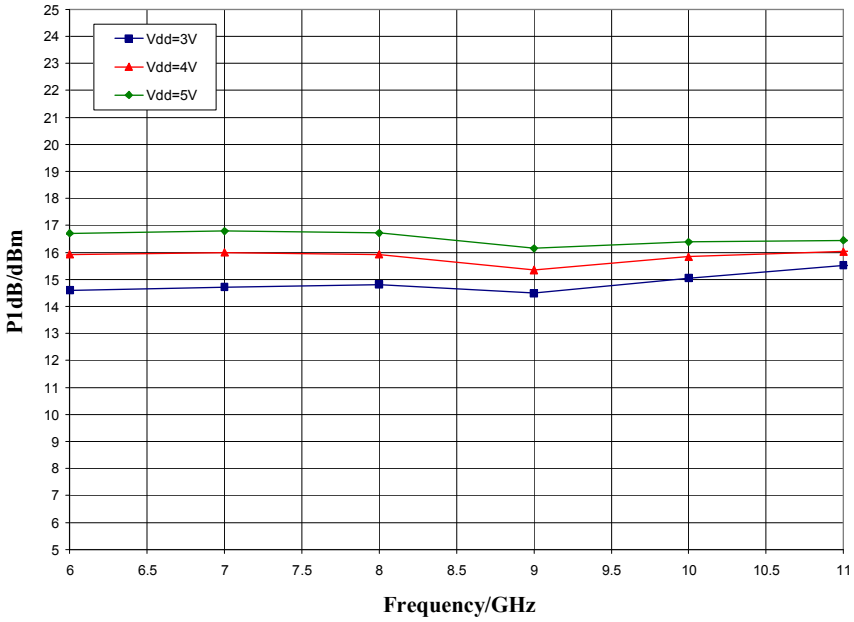
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Typical Performance

P1dB vs. Temperature, V_{dd} = 5.0 V



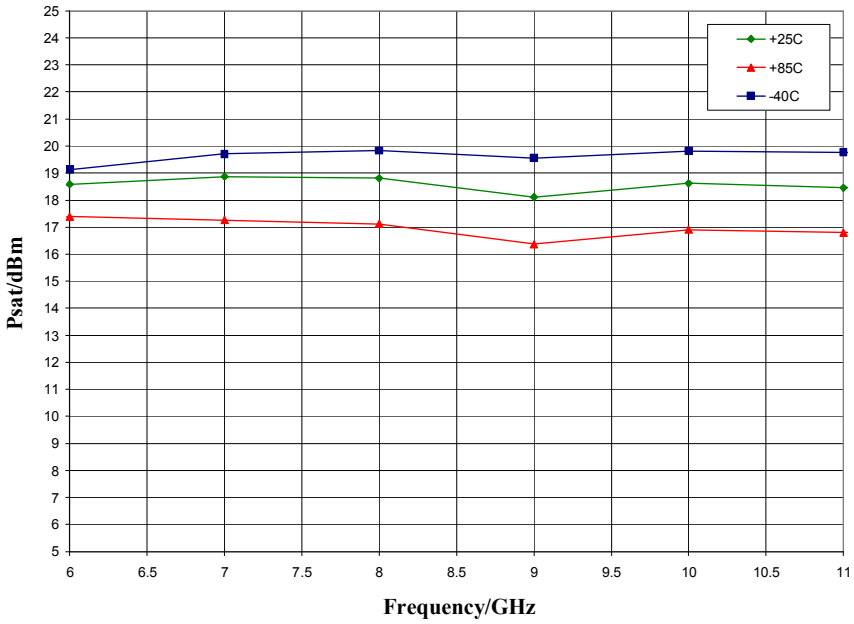
P1dB vs. Vdd, T_A = 25 °C



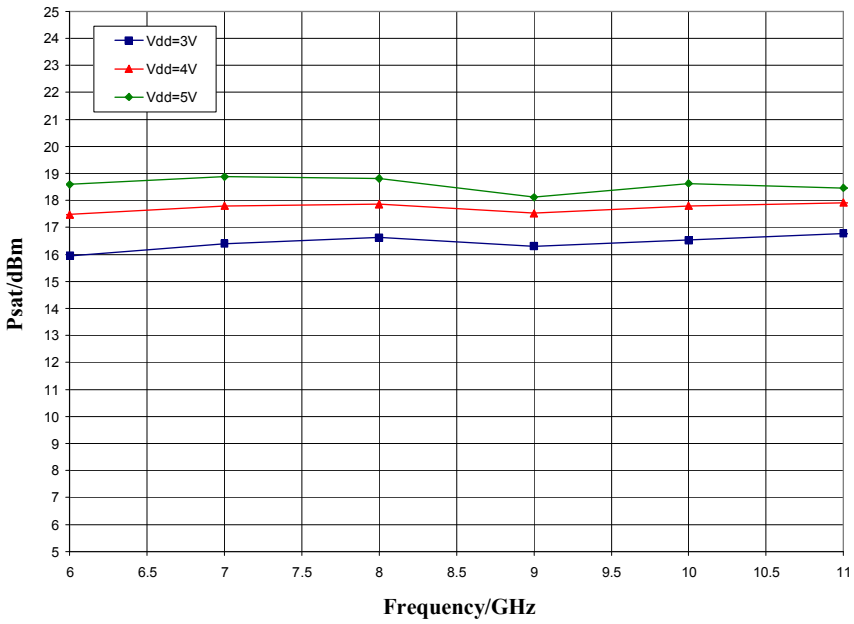
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Typical Performance

Psat vs. Temperature, V_{dd} = 5.0 V



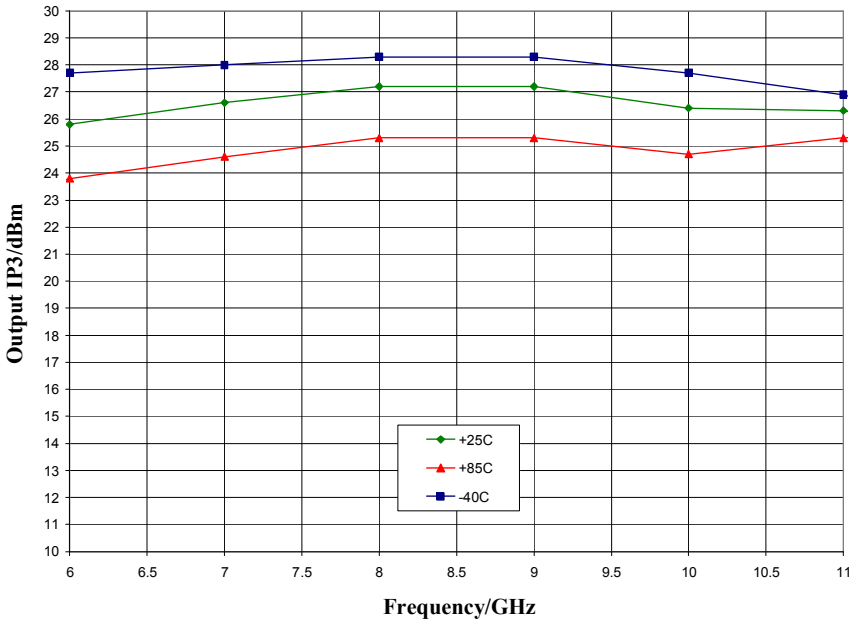
Psat vs. Vdd, T_A = 25 °C



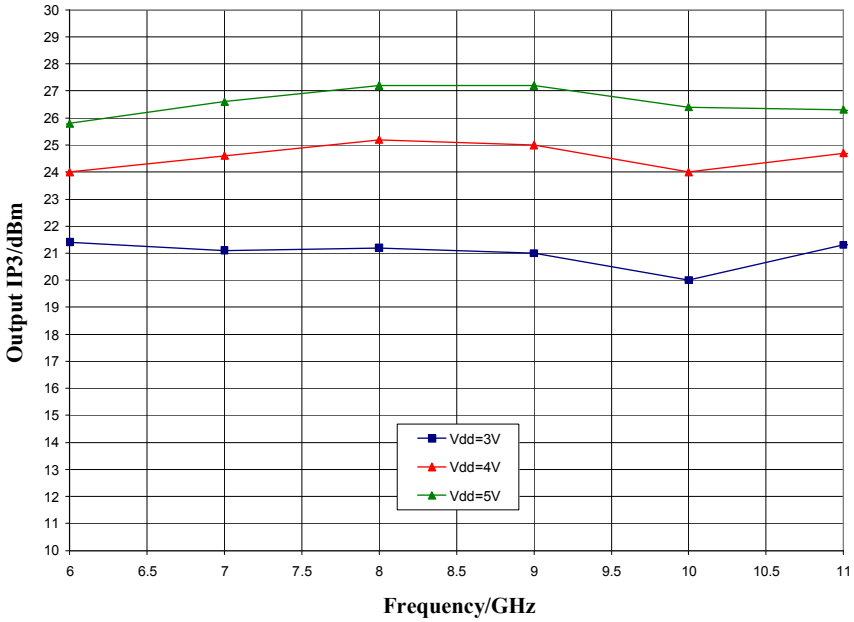
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Typical Performance

Output IP3 vs. Temperature, V_{dd} = 5.0 V



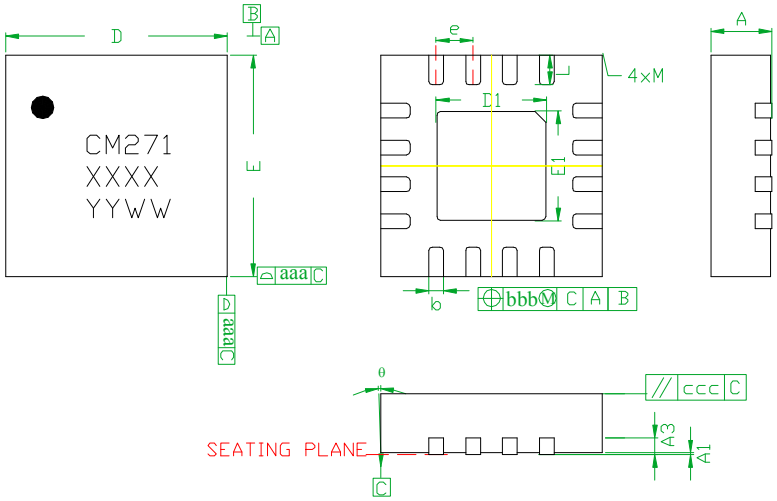
Output IP3 vs. Vdd, T_A = 25 °C



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Mechanical Information

Package Information and Dimensions



SYMBOLS	DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MAX
A	0.80	0.90	1.00
A1	0	0.02	0.05
A3	----	0.25REF.	----
b	0.18	0.23	0.30
D	2.85	3.00	3.15
D1	----	1.5BSC	----
E	2.85	3.00	3.15
E1	----	1.5BSC	----
e	----	0.50BSC	----
L	0.30	0.40	0.50
θ	0	----	12
aaa	----	0.25	----
bbb	----	0.10	----
ccc	----	0.10	----
M	----	----	0.05

NOTES:
1. DIMENSIONS ARE IN MILLIMETERS
2. RoHs COMPLIANT MOLD COMPOUND
3. LEADFRAME MATERIAL: COPPER ALLOY
4. LEAD FINISH: 100% MATTE Sn
5. INDICATED DIMENSION/TOLERANCE APPLIES TO LEADS AND EXPOSED PAD

Recommended PCB Land Pattern

Custom MMIC recommends that the user develop the land pattern that will provide the best design for proper solder reflow and device attach for their specific application. Please review Custom MMIC Application Note AN 105 for a recommended land pattern approach.

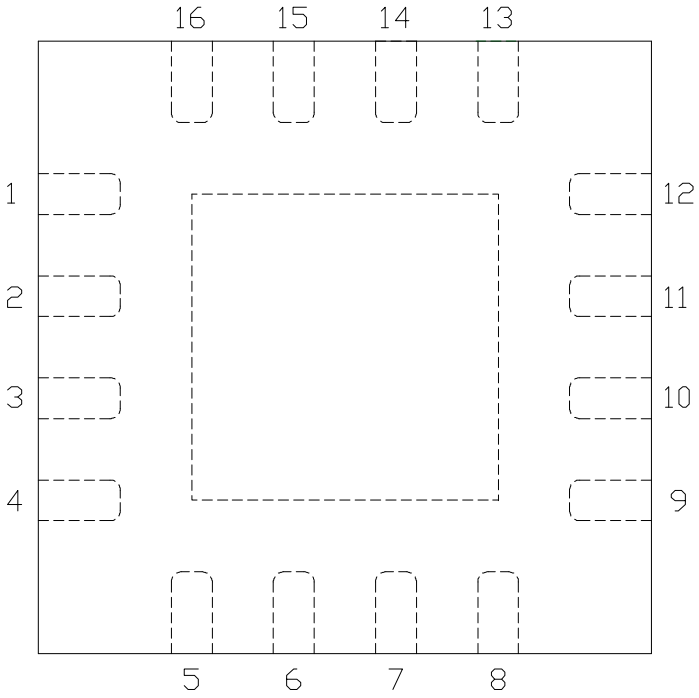
Recommended Solder Reflow Profile

Custom MMIC recommends screen printing with belt furnace reflow to ensure proper solder reflow and device attach. Please review Custom MMIC Application Note AN 102 for a recommended solder reflow profile.

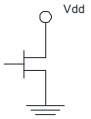
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Pin Description

Pin Diagram



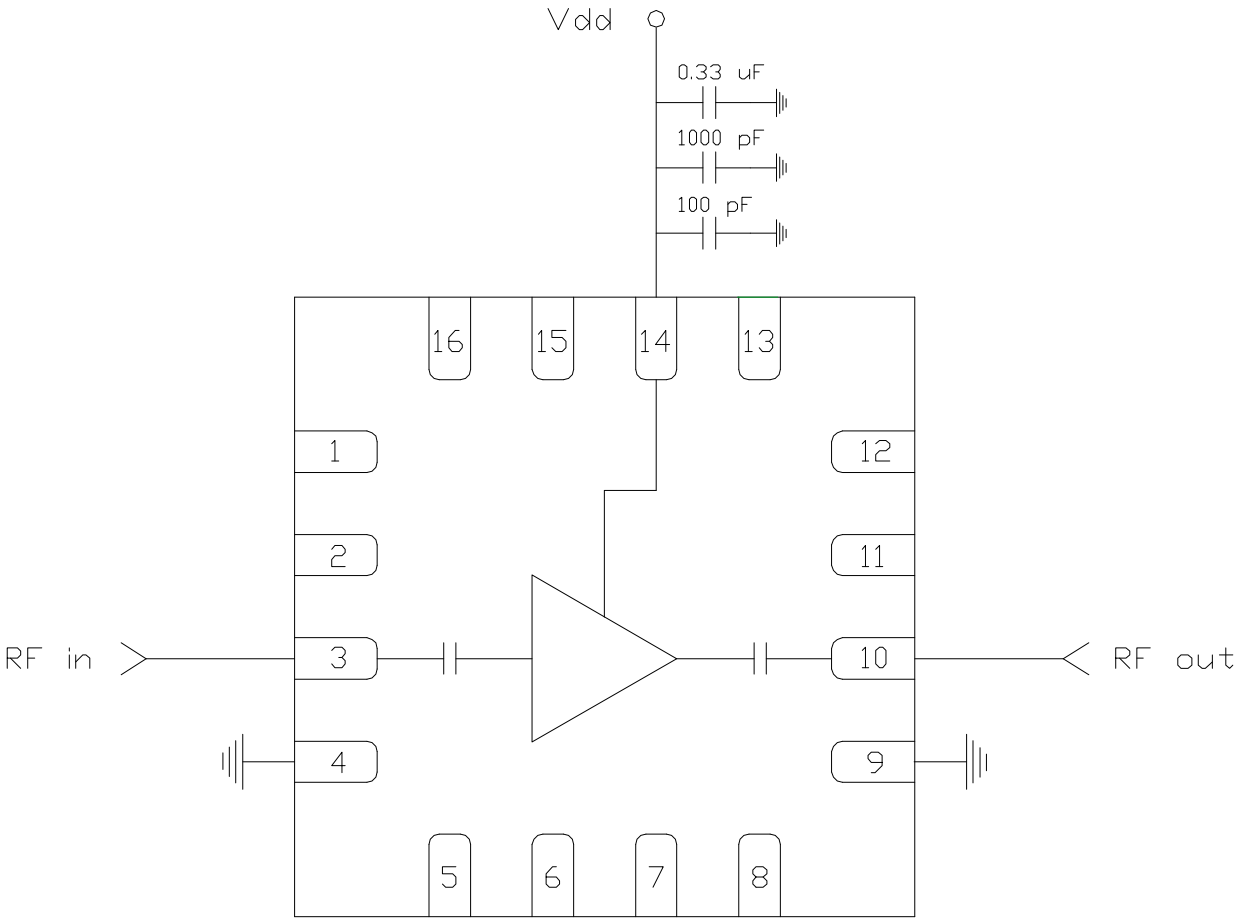
Functional Description

Pin	Function	Description	Schematic
1, 2, 5-8, 11-13, 15, 16	N/C	No connection required. These pins may be connected to RF/DC ground	
3	RF in	DC blocked and 50 ohm matched	
10	RF out	DC blocked and 50 ohm matched	
14	Vdd	Power supply voltage Decoupling and bypass caps required	
4, 9 and die paddle	Ground	Connect to RF / DC ground	

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Applications Information

Application Circuit



Biasing and Operation

The CMD271P3 is biased with a single 5.0 V positive drain supply.

RF power can be applied at any time.

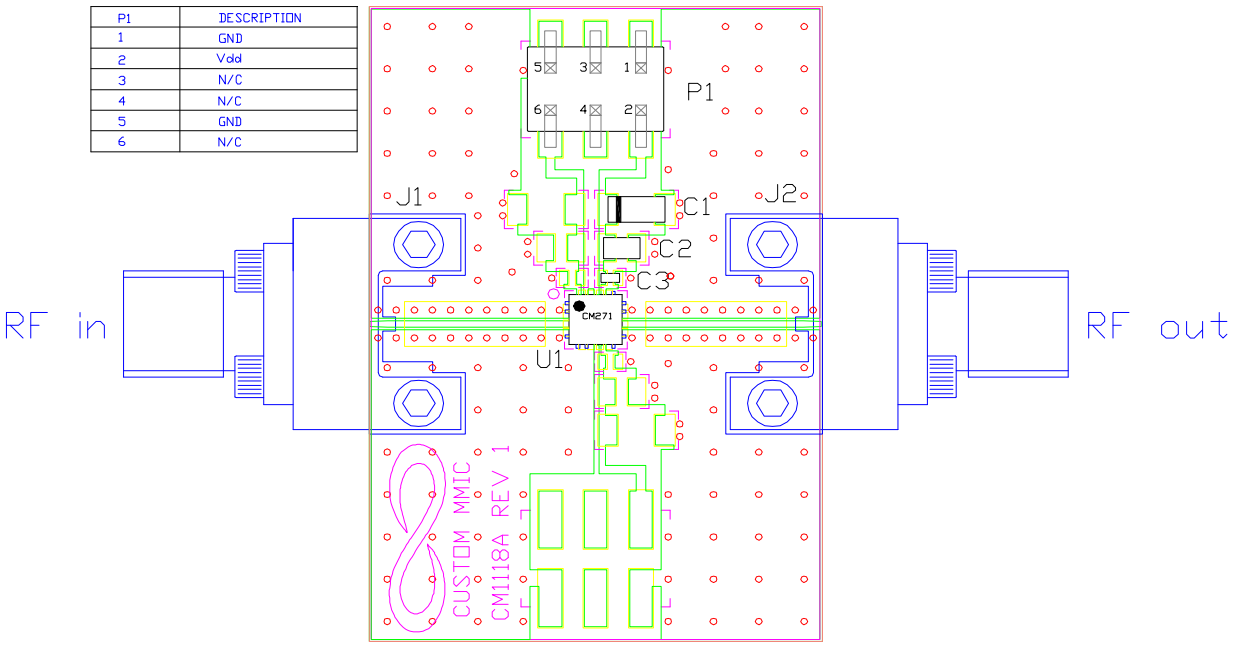
GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

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Applications Information

Evaluation Board

The circuit board shown has been developed for optimized assembly at Custom MMIC. A sufficient number of via holes should be used to connect the top and bottom ground planes. As surface mount processes vary, careful process development is recommended.



Bill of Material

Designator	Value	Description
J1, J2		SMA End Launch Connector
P1		6 Pin Header
C1	0.33 µF	Capacitor, Tantalum
C2	1000 pF	Capacitor, 0603
C3	100 pF	Capacitor, 0402
U1		CMD271P3 Low Noise Amplifier
PCB		CM1118A Evaluation PCB

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