

# HCD70R600S / HCU70R600S

## 700V N-Channel Super Junction MOSFET

### FEATURES

- ☐ Originative New Design
- ☐ Superior Avalanche Rugged Technology
- ☐ Robust Gate Oxide Technology
- ☐ Very Low Intrinsic Capacitances
- ☐ Excellent Switching Characteristics
- ☐ Unrivalled Gate Charge : 16 nC (Typ.)
- ☐ Extended Safe Operating Area
- ☐ Lower  $R_{DS(ON)}$  : 0.54  $\Omega$  (Typ.) @  $V_{GS}=10V$
- ☐ 100% Avalanche Tested

$$BV_{DSS} = 700 \text{ V}$$

$$R_{DS(on) \text{ typ}} = 0.54 \Omega$$

$$I_D = 7.3 \text{ A}$$

**D-PAK**

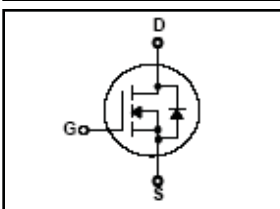


**I-PAK**



HCD70R600S HCU70R600S

1.Gate 2. Drain 3. Source



### Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise specified

| Symbol         | Parameter                                                                     |  | Value       | Units               |
|----------------|-------------------------------------------------------------------------------|--|-------------|---------------------|
| $V_{DSS}$      | Drain-Source Voltage                                                          |  | 700         | V                   |
| $I_D$          | Drain Current – Continuous ( $T_C = 25^\circ\text{C}$ )                       |  | 7.3         | A                   |
|                | Drain Current – Continuous ( $T_C = 100^\circ\text{C}$ )                      |  | 4.6         | A                   |
| $I_{DM}$       | Drain Current – Pulsed (Note 1)                                               |  | 22          | A                   |
| $V_{GS}$       | Gate-Source Voltage                                                           |  | $\pm 20$    | V                   |
| $E_{AS}$       | Single Pulsed Avalanche Energy (Note 2)                                       |  | 120         | mJ                  |
| $I_{AR}$       | Avalanche Current (Note 1)                                                    |  | 2.0         | A                   |
| $E_{AR}$       | Repetitive Avalanche Energy (Note 1)                                          |  | 0.5         | mJ                  |
| $dv/dt$        | Peak Diode Recovery $dv/dt$                                                   |  | 15          | V/ns                |
| $P_D$          | Power Dissipation ( $T_A = 25^\circ\text{C}$ )*                               |  | 2.5         | W                   |
|                | Power Dissipation ( $T_C = 25^\circ\text{C}$ )                                |  | 69          | W                   |
|                | - Derate above $25^\circ\text{C}$                                             |  | 0.55        | W/ $^\circ\text{C}$ |
| $T_J, T_{STG}$ | Operating and Storage Temperature Range                                       |  | -55 to +150 | $^\circ\text{C}$    |
| $T_L$          | Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds |  | 300         | $^\circ\text{C}$    |

### Thermal Resistance Characteristics

| Symbol          | Parameter            | Typ. | Max. | Units              |
|-----------------|----------------------|------|------|--------------------|
| $R_{\theta JC}$ | Junction-to-Case     | --   | 1.8  | $^\circ\text{C/W}$ |
| $R_{\theta JA}$ | Junction-to-Ambient* | --   | 50   |                    |
| $R_{\theta JA}$ | Junction-to-Ambient  | --   | 110  |                    |

\* When mounted on the minimum pad size recommended (PCB Mount)

**Electrical Characteristics**  $T_J=25^\circ\text{C}$  unless otherwise specified

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Units |
|--------|-----------|-----------------|-----|-----|-----|-------|
|--------|-----------|-----------------|-----|-----|-----|-------|

**On Characteristics**

|              |                                   |                                              |     |      |     |          |
|--------------|-----------------------------------|----------------------------------------------|-----|------|-----|----------|
| $V_{GS}$     | Gate Threshold Voltage            | $V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$    | 2.8 | --   | 4.2 | V        |
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance | $V_{GS} = 10\ \text{V}, I_D = 3.5\ \text{A}$ | --  | 0.54 | 0.6 | $\Omega$ |

**Off Characteristics**

|            |                                 |                                                   |     |    |           |               |
|------------|---------------------------------|---------------------------------------------------|-----|----|-----------|---------------|
| $BV_{DSS}$ | Drain-Source Breakdown Voltage  | $V_{GS} = 0\ \text{V}, I_D = 250\ \mu\text{A}$    | 700 | -- | --        | V             |
| $I_{DSS}$  | Zero Gate Voltage Drain Current | $V_{DS} = 700\ \text{V}, V_{GS} = 0\ \text{V}$    | --  | -- | 10        | $\mu\text{A}$ |
|            |                                 | $V_{DS} = 560\ \text{V}, T_J = 125^\circ\text{C}$ | --  | -- | 100       | $\mu\text{A}$ |
| $I_{GSS}$  | Gate-Body Leakage Current       | $V_{GS} = \pm 20\ \text{V}, V_{DS} = 0\ \text{V}$ | --  | -- | $\pm 100$ | nA            |

**Dynamic Characteristics**

|           |                              |                                                                         |    |     |    |          |
|-----------|------------------------------|-------------------------------------------------------------------------|----|-----|----|----------|
| $C_{iss}$ | Input Capacitance            | $V_{DS} = 25\ \text{V}, V_{GS} = 0\ \text{V},$<br>$f = 1.0\ \text{MHz}$ | -- | 500 | -- | pF       |
| $C_{oss}$ | Output Capacitance           |                                                                         | -- | 250 | -- | pF       |
| $C_{rss}$ | Reverse Transfer Capacitance |                                                                         | -- | 7   | -- | pF       |
| $R_g$     | Gate Resistance              | $V_{GS} = 0\ \text{V}, V_{DS} = 0\ \text{V}, f = 1\text{MHz}$           | -- | 4   | -- | $\Omega$ |

**Switching Characteristics**

|              |                     |                                                                           |    |     |    |    |
|--------------|---------------------|---------------------------------------------------------------------------|----|-----|----|----|
| $t_{d(on)}$  | Turn-On Time        | $V_{DS} = 350\ \text{V}, I_D = 7.3\ \text{A},$<br>$R_G = 25\ \Omega$      | -- | 20  | -- | ns |
| $t_r$        | Turn-On Rise Time   |                                                                           | -- | 20  | -- | ns |
| $t_{d(off)}$ | Turn-Off Delay Time |                                                                           | -- | 60  | -- | ns |
| $t_f$        | Turn-Off Fall Time  |                                                                           | -- | 20  | -- | ns |
| $Q_g$        | Total Gate Charge   | $V_{DS} = 560\ \text{V}, I_D = 7.3\ \text{A},$<br>$V_{GS} = 10\ \text{V}$ | -- | 16  | -- | nC |
| $Q_{gs}$     | Gate-Source Charge  |                                                                           | -- | 4.0 | -- | nC |
| $Q_{gd}$     | Gate-Drain Charge   |                                                                           | -- | 5.5 | -- | nC |

**Source-Drain Diode Maximum Ratings and Characteristics**

|                 |                                               |                                                                                 |    |     |     |    |
|-----------------|-----------------------------------------------|---------------------------------------------------------------------------------|----|-----|-----|----|
| I <sub>S</sub>  | Continuous Source-Drain Diode Forward Current |                                                                                 | -- | --  | 7.3 | A  |
| I <sub>SM</sub> | Pulsed Source-Drain Diode Forward Current     |                                                                                 | -- | --  | 22  |    |
| V <sub>SD</sub> | Source-Drain Diode Forward Voltage            | I <sub>S</sub> = 7.3 A, V <sub>GS</sub> = 0 V                                   | -- | --  | 1.3 | V  |
| trr             | Reverse Recovery Time                         | I <sub>S</sub> = 3.5 A, V <sub>GS</sub> = 0 V<br>di <sub>F</sub> /dt = 100 A/μs | -- | 250 | --  | ns |
| Qrr             | Reverse Recovery Charge                       |                                                                                 | -- | 2   | --  | uC |

**Notes :**

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2.  $I_{AS}=2\text{A}, V_{DD}=50\text{V}, R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$

# Typical Characteristics

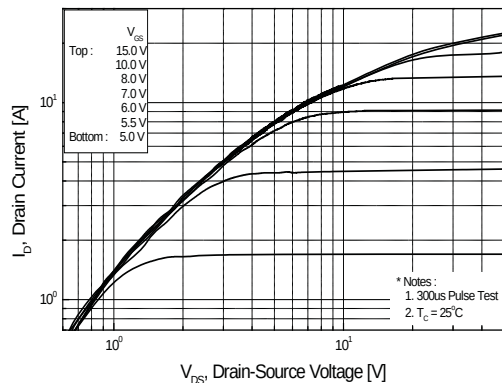


Figure 1. On Region Characteristics

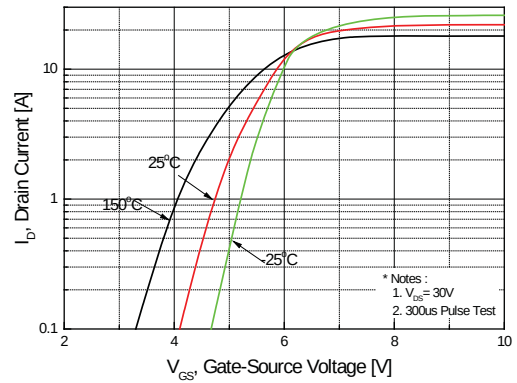


Figure 2. Transfer Characteristics

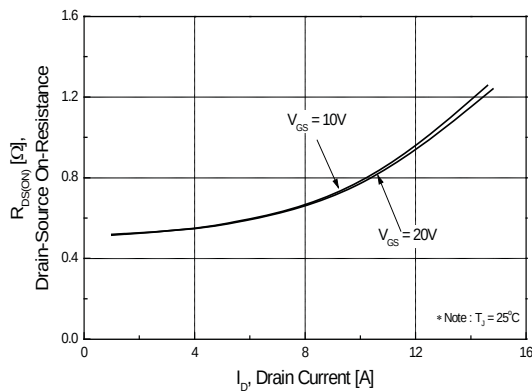


Figure 3. On Resistance Variation vs Drain Current and Gate Voltage

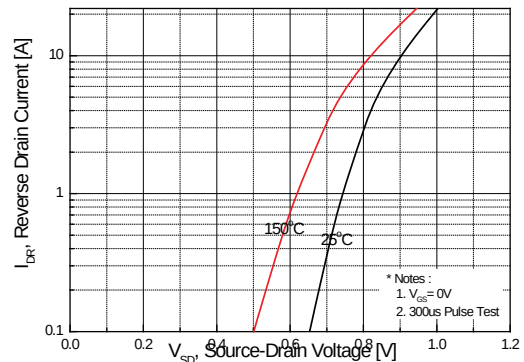


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

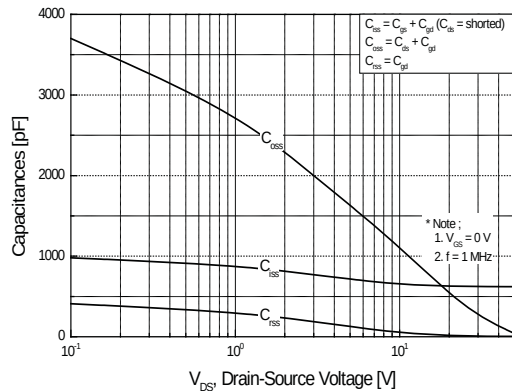


Figure 5. Capacitance Characteristics

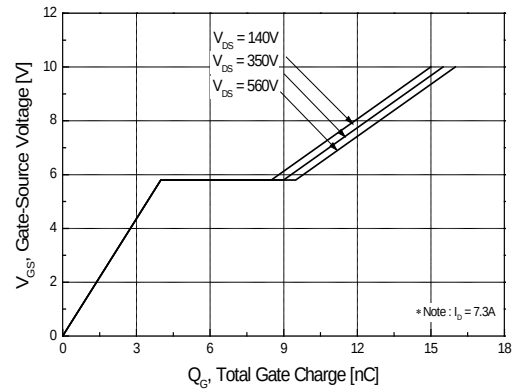
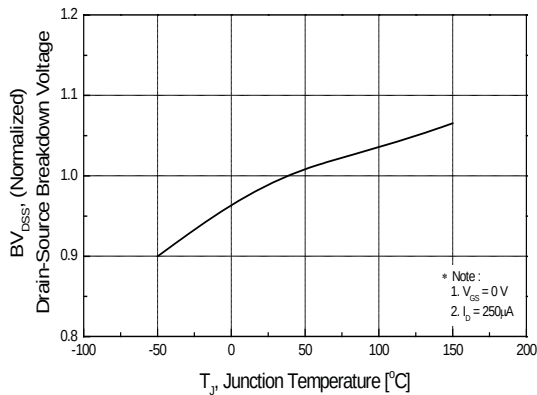
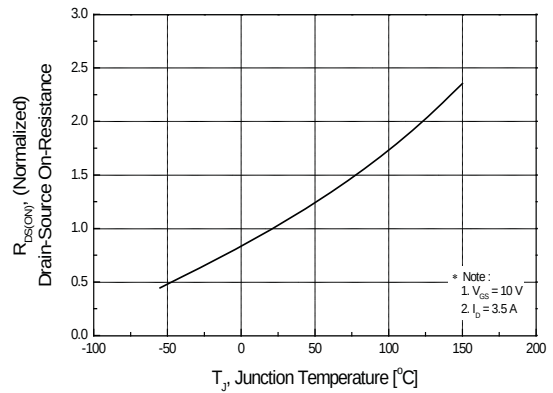


Figure 6. Gate Charge Characteristics

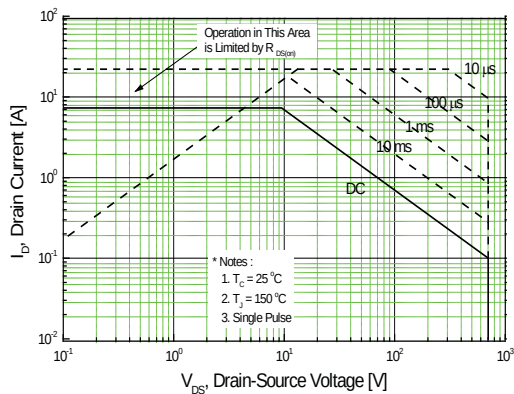
# Typical Characteristics (continued)



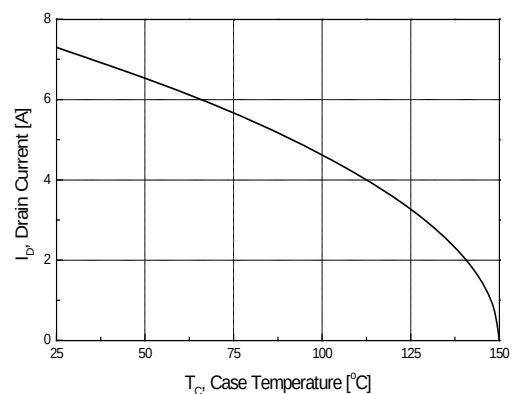
**Figure 7. Breakdown Voltage Variation vs Temperature**



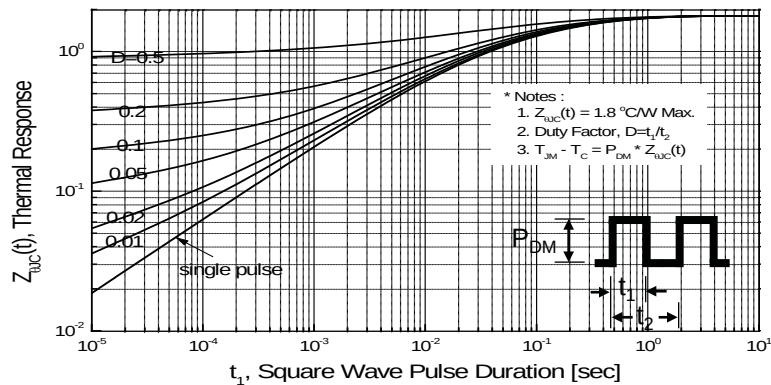
**Figure 8. On-Resistance Variation vs Temperature**



**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs Case Temperature**



**Figure 11. Transient Thermal Response Curve**

Fig 12. Gate Charge Test Circuit & Waveform

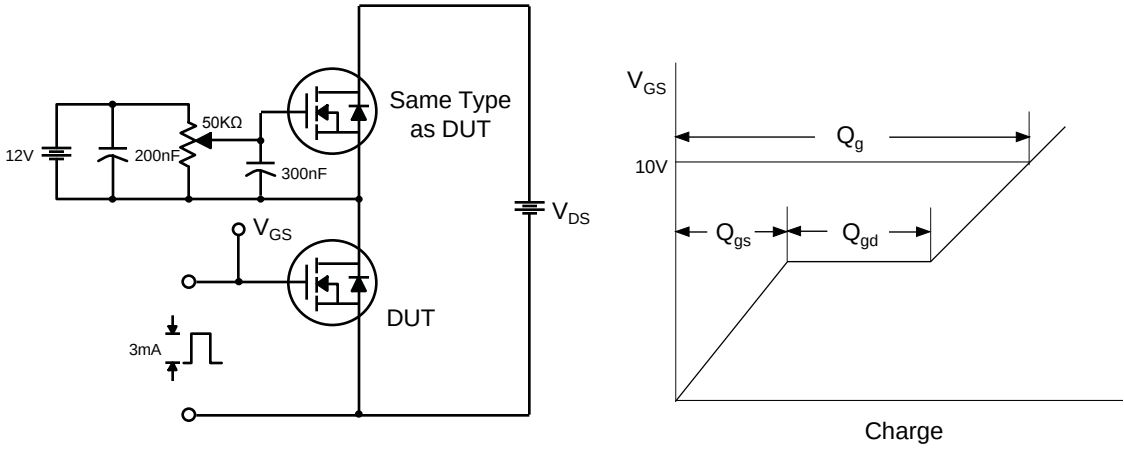


Fig 13. Resistive Switching Test Circuit & Waveforms

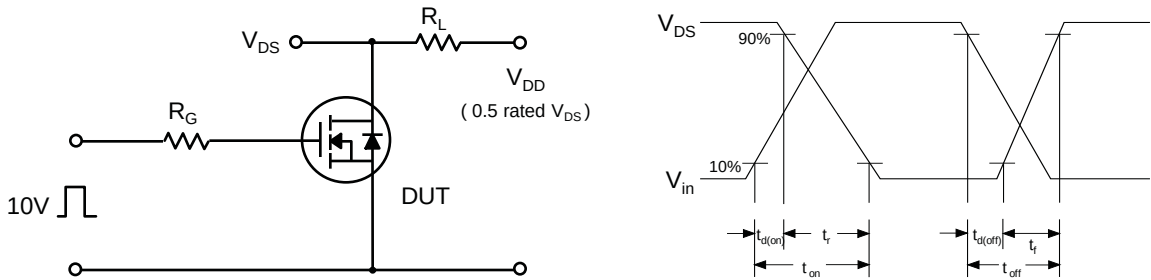


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms

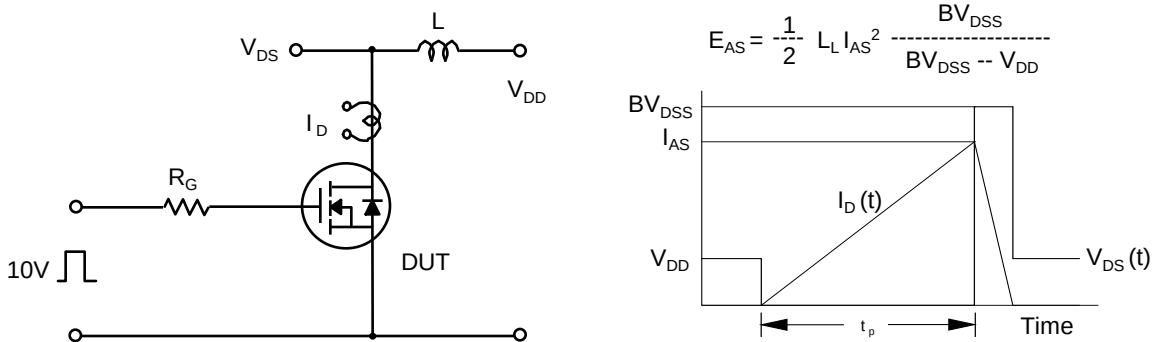
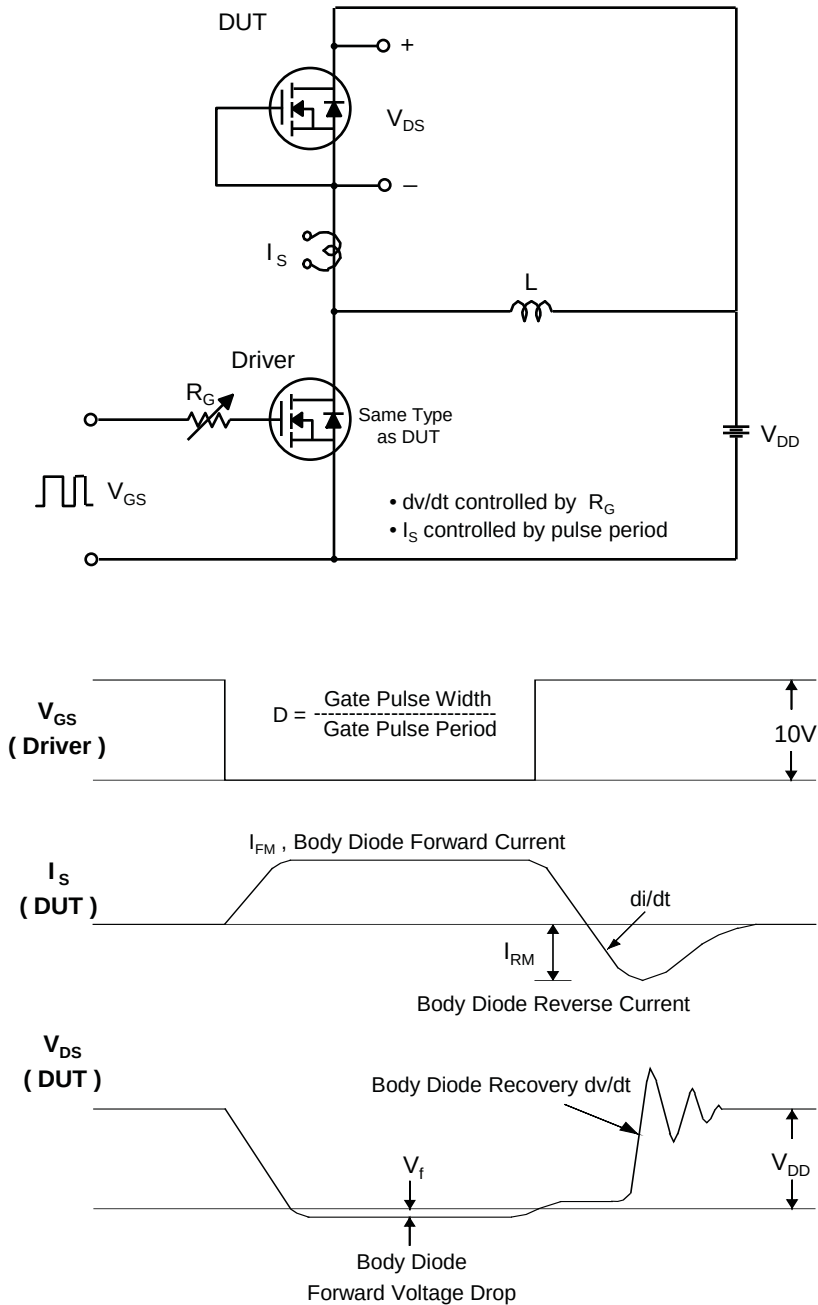


Fig 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



## Package Dimension

TO-252A  
(D-PAK)