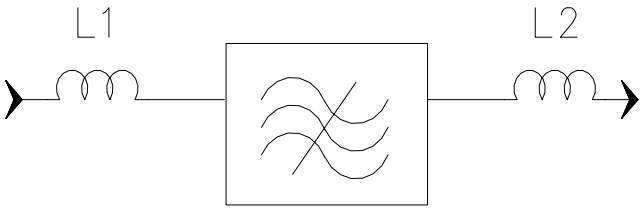
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	143.55	143.625	143.7
Insertion Loss	dB		13.6	16
1dB bandwidth	MHz	0.5	0.57	
40dB bandwidth	MHz		2	2.1
Ultimate Rejection	dB			
f0±2.5MHz		37	40	
f0±20MHz		40	45	
Passband Variation	dB		0.5	0.8
Absolute Delay	usec		1.71	
Subatrate Material		Quartz		
Ambient Temperature	°C	25		
Package Size	SF-26			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

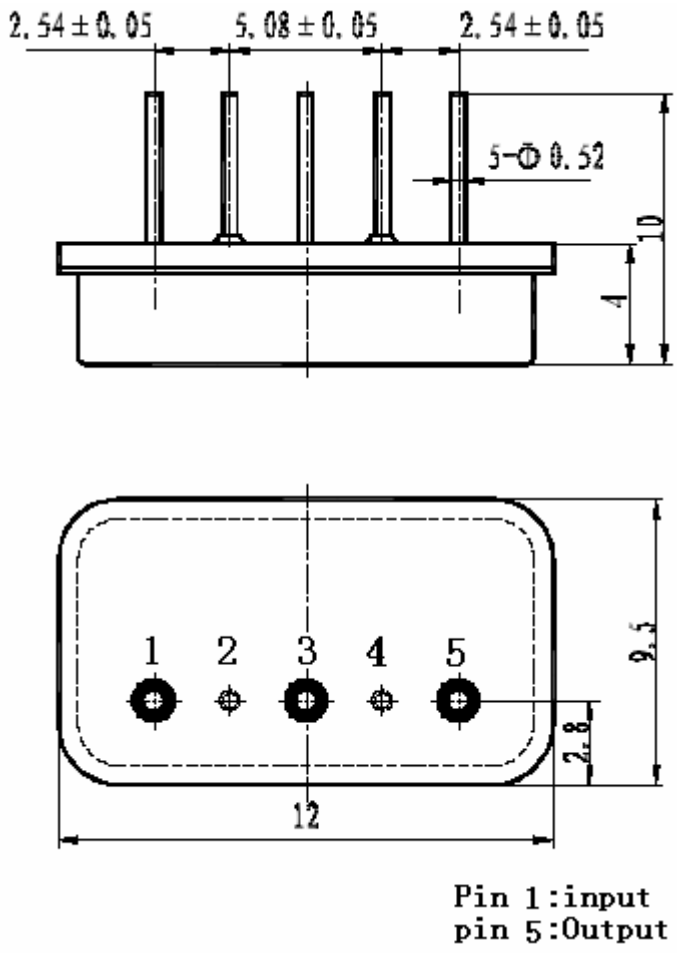


L1=82nH **L2=82nH**

Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

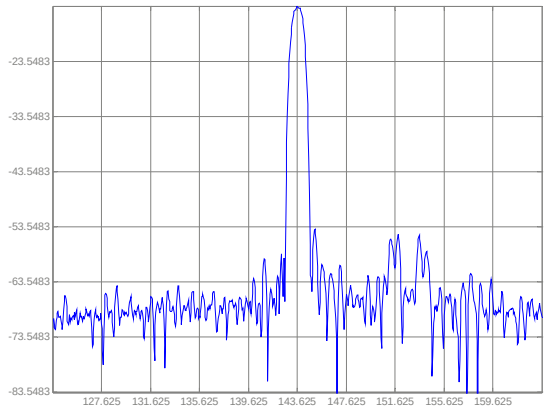
Package Dimension



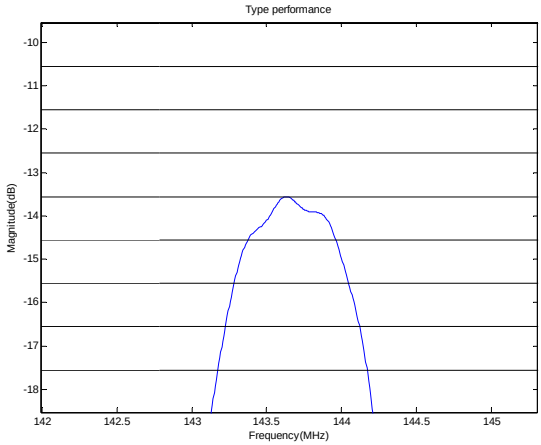
Pin 1:input
pin 5:Output

Typical Performance

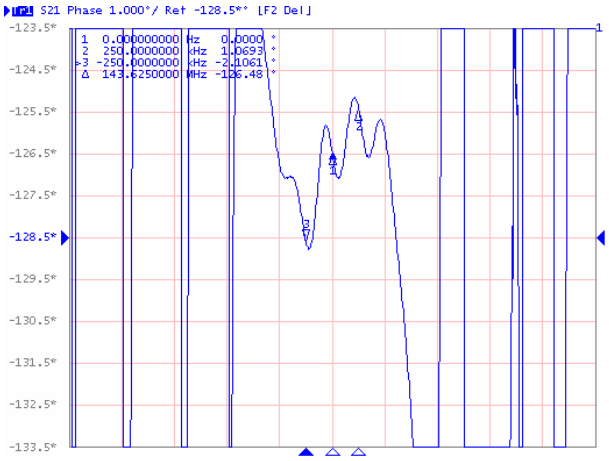
Frequency Respond



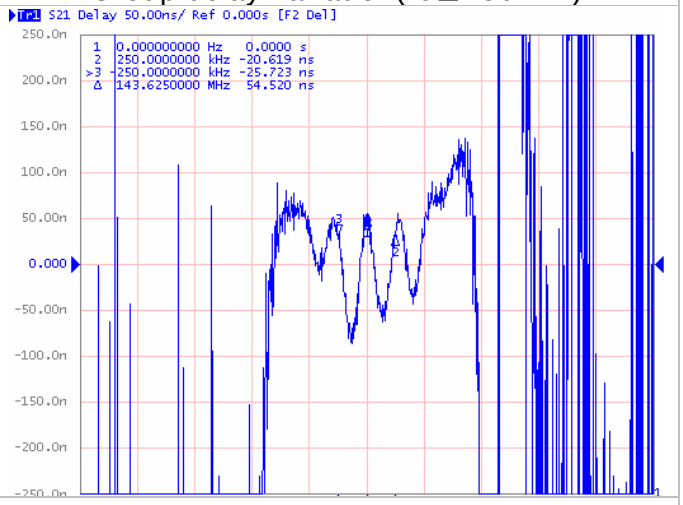
Passband Respond



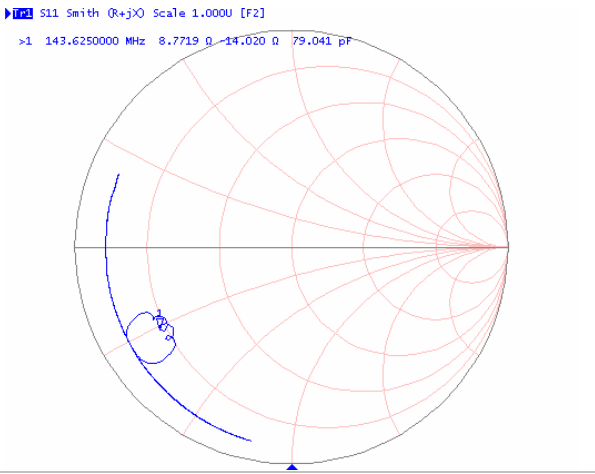
Phase Linearity (f0±250KHz)



Group delay variation(f0±250KHz)



Simth Chart S11



Simth Chart S22

