P3P18S19B

Notebook LCD Panel EMI Reduction IC

Features

- FCC approved method of EMI attenuation.
- Provides up to 15dB EMI reduction.
- Generates a low EMI Spread Spectrum clock and a non-spread reference clock of the input frequency.
- Optimized for Frequency range from 20 to 40MHz.
- Internal loop filter minimizes external components and board space.
- Low Inherent Cycle-to-Cycle jitter.
- Two spread % selections: -1.25% to -1.75%.
- 3.3V Operating Voltage.
- CMOS design.
- Supports notebook VGA and other LCD timing controller applications.
- Power Down function for mobile application.
- Available in 8-pin SOIC Package.
- RoHS Compliant

Product Description

The P3P18S19B is a Versatile Spread Spectrum Frequency Modulator designed specifically for input clock frequencies from 20 to 40MHz. (Refer to *Input Frequency and Modulation Rate* Table). The P3P18S19B reduces electromagnetic interference (EMI) at the clock source, allowing system wide reduction of EMI of downstream clock and data dependent signals. The P3P18S19B allows significant system cost savings by reducing the number of circuit board layers, ferrite beads, shielding, and other passive components that are traditionally required to pass EMI regulations.

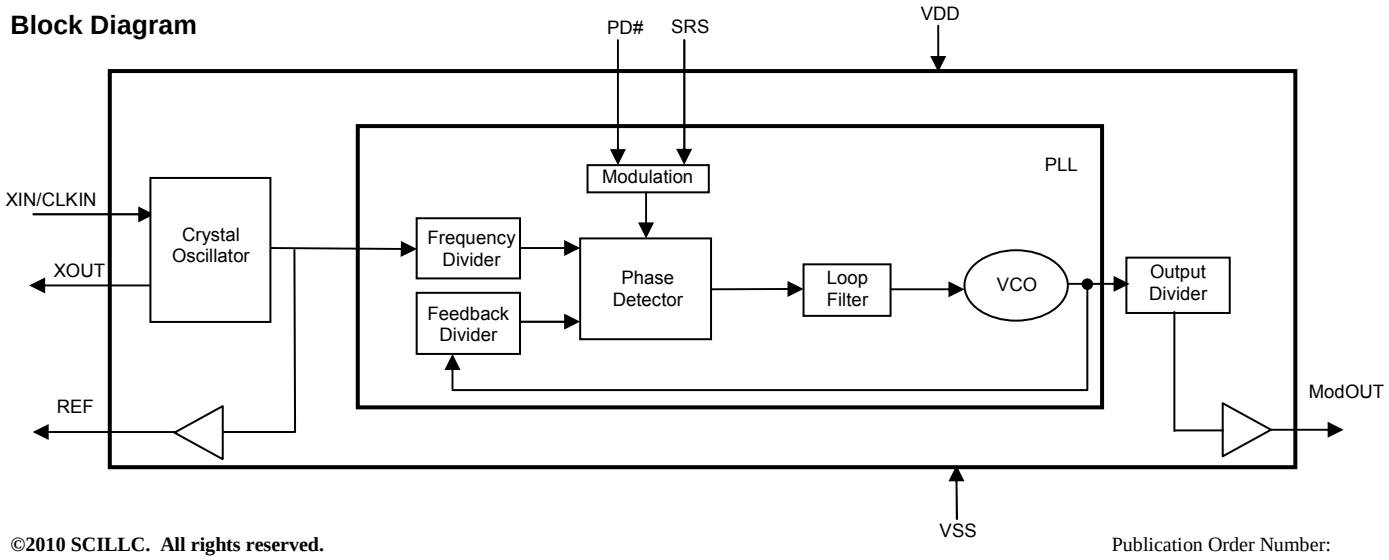
The P3P18S19B modulates the output of a single PLL in order to “spread” the bandwidth of a synthesized clock, and more importantly, decreases the peak amplitudes of its harmonics. This results in significantly lower system EMI compared to the typical narrow band signal produced by oscillators and most frequency generators. Lowering EMI by increasing a signal’s bandwidth is called ‘Spread Spectrum Clock Generation’.

The P3P18S19B uses the most efficient and optimized modulation profile approved by the FCC and is implemented in a proprietary all digital method.

Applications

The P3P18S19B is targeted towards EMI management for memory and LVDS interfaces in mobile graphic chipsets and high-speed digital applications such as PC peripheral devices, consumer electronics, and embedded controller systems.

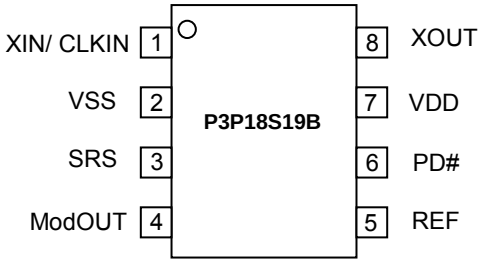
Block Diagram



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P3P18S19/D

Pin Configuration



Pin Description

Pin#	Pin Name	Type	Description
1	XIN / CLKIN	I	Crystal Connection or external frequency input. This pin has dual functions. It can be connected to either an external crystal or an external reference clock.
2	VSS	P	Ground Connection. Connect to system ground.
3	SRS	I	Spread range select. Digital logic input used to select frequency deviation *(*Refer to *Spread Deviation Selection* Table*).* This pin has an internal pull-up resistor.
4	ModOUT	O	Spread spectrum clock output. *(*Refer to *Input Frequency and Modulation Rate Table* and *Spread Deviation Selection* Table*)*
5	REF	O	Non-modulated Reference clock output of the input frequency.
6	PD#	I	Power down control pin. Pull LOW to enable Power-Down mode. This pin has an internal pull-up resistor.
7	VDD	P	Power Supply for the entire chip.
8	XOUT	O	Crystal Connection. Input connection for an external crystal. If using an external reference, this pin must be left unconnected.

Input Frequency and Modulation Rate

Part Number	Input Frequency Range	Output Frequency range	Modulation rate
P3P18S19B	20MHz to 40MHz	20MHz to 40MHz	Input Frequency / 512

Spread Deviation Selection

Part Number	SRS	Spread Deviation
P3P18S19B	0	-1.25% (DOWN)
	1	-1.75% (DOWN)

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
VDD, V_{IN}	Voltage on any pin with respect to Ground	-0.5 to +4.6	V
T_{STG}	Storage temperature	-65 to +125	℃
T_s	Max. Soldering Temperature (10 sec)	260	℃
T_J	Junction Temperature	150	℃
T_{DV}	Static Discharge Voltage (As per JEDEC STD22- A114-B)	2	KV

Note: These are stress ratings only and are not implied for functional use. Exposure to absolute maximum ratings for prolonged periods of time may affect device reliability.

DC Electrical Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
V_{IL}	Input Low voltage	VSS - 0.3		0.8	V
V_{IH}	Input High voltage	2.0		VDD + 0.3	V
I_{IL}	Input Low current (inputs PD#, SRS)	-60.0		-20.0	µA
I_{IH}	Input High current			1.0	µA
I_{XOL}	X_{OUT} Output low current @ 0.4V, VDD = 3.3V		3		mA
I_{XOH}	X_{OUT} Output high current @ 2.5V, VDD = 3.3V		3		mA
V_{OL}	Output Low voltage VDD = 3.3V, I_{OL} = 20mA			0.4	V
V_{OH}	Output High voltage VDD = 3.3V, I_{OH} = 20mA	2.5			V
I_{CC}	Dynamic supply current normal mode 3.3V and 25pF probe loading	7.1 $f_{IN - min}$		26.9 $f_{IN - max}$	mA
I_{DD}	Static supply current standby mode		4.5		mA
VDD	Operating Voltage		3.3		V
t_{ON}	Power up time (first locked clock cycle after power up)		0.18		mS
Z_{OUT}	Clock Output impedance		50		Ω

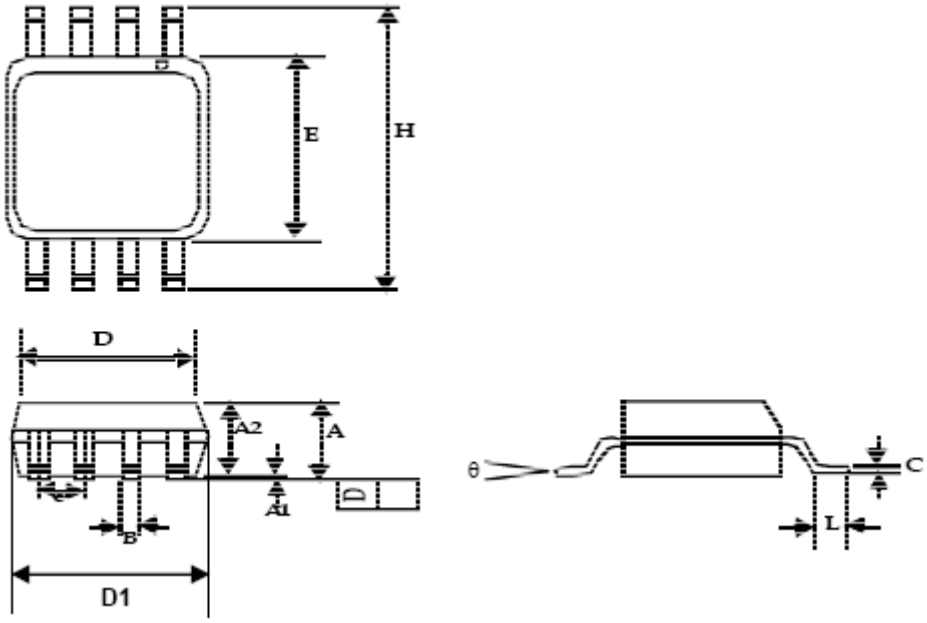
AC Electrical Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
f_{IN}	Input Frequency	20		40	MHz
f_{OUT}	Output Frequency	20		40	MHz
t_{LH}*	Output Rise time (Measured from 0.8V to 2.0V)		0.66		nS
t_{HL}*	Output Fall time (Measured from 0.8V to 2.0V)		0.65		nS
t_{JC}	Jitter (Cycle to cycle)	-200		200	pS
t_D	Output Duty cycle	45	50	55	%

*t_{LH} and t_{HL} are measured into a capacitive load of 15pF

Package Information

8-lead (150-mil) SOIC Package



Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A1	0.004	0.010	0.10	0.25
A	0.053	0.069	1.35	1.75
A2	0.049	0.059	1.25	1.50
B	0.012	0.020	0.31	0.51
C	0.007	0.010	0.18	0.25
D	0.193 BSC		4.90 BSC	
E	0.154 BSC		3.91 BSC	
e	0.050 BSC		1.27 BSC	
H	0.236 BSC		6.00 BSC	
L	0.016	0.050	0.41	1.27
θ	0°	8°	0°	8°

Package length (Excluding end flash) D - 188 ~ 193 mils.
Package length (Including end flash) D1 - 198 ~ 207 mils.

Ordering Information

Part Number	Marking	Package Type	Temperature
P3P18S19BF-08SR	ABS	8-Pin SOIC, TAPE & REEL , Pb Free	0℃ to +70℃

A “microdot” placed at the end of last row of marking or just below the last row toward the center of package indicates Pb-free

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