

High Voltage Positive Adjustable Regulators

FEATURES

- 60V Operation
- *Guaranteed* 1% Output Voltage Tolerance
- *Guaranteed* max. 0.01%/V Line Regulation
- *Guaranteed* max. 0.3% Load Regulation
- Min. 1.5A Output Current
- 100% Burn-in in Thermal Overload

APPLICATIONS

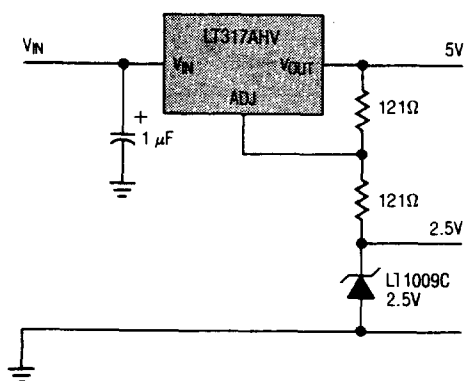
- Wide Range Power Supplies
- Constant Current Supplies
- Voltage Programmable Supplies

DESCRIPTION

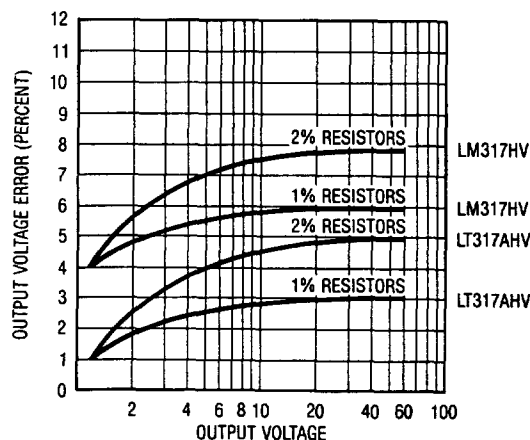
The LT117AHV Series are 3-terminal positive adjustable voltage regulators which offer improved performance over earlier devices. A major feature of the LT117AHV is the output voltage tolerance is guaranteed at a maximum of $\pm 1\%$, allowing an overall power supply tolerance to be better than 3% using inexpensive 1% resistors. Line and load regulation performance has been improved as well. Additionally, the LT117AHV reference voltage is guaranteed not to exceed 2% when operating over the full load, line and power dissipation conditions. The LT117AHV adjustable regulators offer an improved solution for all positive voltage regulator requirements with load currents up to 1.5 amps. For performance curves and applications circuits see the LT117A series data sheet.

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Regulator with Reference



Output Voltage Error



ABSOLUTE MAXIMUM RATINGS

Power Dissipation Internally Limited
Input to Output Voltage Differential 60V
Operating Junction Temperature Range
LT117AHV/LM117HV -55°C to 150°C
LT317AHV/LM317HV 0°C to 125°C
Storage Temperature Range
LT117AHV/LM117HV -65°C to 150°C
LT317AHV/LM317HV -65°C to 150°C
Lead Temperature (Soldering, 10 sec.) 300°C

PACKAGE/ORDER INFORMATION

BOTTOM VIEW	ORDER PART NO.	BOTTOM VIEW	ORDER PART NO.
	LT117AHVK LT317AHVK LM117HVК LM317HVК		LT117AHVH LT317AHVH LM117HVH LM317HVH

PRECONDITIONING:

100% THERMAL LIMIT BURN-IN

ELECTRICAL CHARACTERISTICS (See Note 1) LT117AHV/LM117HV

SYMBOL	PARAMETER	CONDITIONS	LT117AHV			LM117HV			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{REF}	Reference Voltage	$I_{\text{OUT}} = 10\text{mA}$ $T_J = 25^{\circ}\text{C}$	1.238	1.250	1.262				V
		$3\text{V} \leq (V_{\text{IN}} - V_{\text{OUT}}) \leq 60\text{V}$ $10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{max}}$ $P \leq P_{\text{max}}$	● 1.225	1.250	1.270	1.20	1.25	1.30	V
$\frac{\Delta V_{\text{OUT}}}{\Delta V_{\text{IN}}}$	Line Regulation	$3\text{V} \leq (V_{\text{IN}} - V_{\text{OUT}}) \leq 60\text{V}$ (See Note 2)		0.005	0.01		0.01	0.02	%/V
			● 0.01	0.02		0.02	0.05		%/V
$\frac{\Delta V_{\text{OUT}}}{\Delta I_{\text{OUT}}}$	Load Regulation	$10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{max}}$ (See Note 2) $V_{\text{OUT}} \leq 5\text{V}$ $V_{\text{OUT}} \geq 5\text{V}$		5 0.1	15 0.3		5 0.1	15 0.3	mV %
		$V_{\text{OUT}} \leq 5\text{V}$ $V_{\text{OUT}} \geq 5\text{V}$	● 20 0.3	50 1		20 0.3	50 1		mV %
	Thermal Regulation	$T_A = 25^{\circ}\text{C}$, 20msec Pulse		0.002	0.02		0.03	0.07	%/W
	Ripple Rejection	$V_{\text{OUT}} = 10\text{V}$, $f = 120\text{Hz}$ $C_{\text{ADJ}} = 0$	● 65			65			dB
		$C_{\text{ADJ}} = 10\mu\text{F}$	● 66	80		66	80		dB
I_{ADJ}	Adjust Pin Current		● 50	100		50	100		μA
ΔI_{ADJ}	Adjust Pin Current Change	$10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{max}}$ $2.5\text{V} \leq (V_{\text{IN}} - V_{\text{OUT}}) \leq 60\text{V}$	● 0.2	5		0.2	5		μA
I_{min}	Minimum Load Current	$(V_{\text{IN}} - V_{\text{OUT}}) = 60\text{V}$	● 3.5	7		3.5	7		mA
	Current Limit	$(V_{\text{IN}} - V_{\text{OUT}}) \leq 15\text{V}$ K Package H Package	● 1.5 0.5	2.2 0.8		1.5 0.5	2.2 0.8		A A
		$(V_{\text{IN}} - V_{\text{OUT}}) = 60\text{V}$, $T_J = 25^{\circ}\text{C}$ K Package H Package		0.1 0.03			0.1 0.03		A A
$\frac{\Delta V_{\text{OUT}}}{\Delta \text{Temp}}$	Temperature Stability	$-55^{\circ}\text{C} \leq T_J \leq +150^{\circ}\text{C}$		1	2		1		%
$\frac{\Delta V_{\text{OUT}}}{\Delta \text{Time}}$	Long Term Stability	$T_A = 125^{\circ}\text{C}$		0.3	1		0.3	1	%
e_n	RMS Output Noise (% of V_{OUT})	$T_A = 25^{\circ}\text{C}$, $10\text{Hz} \leq f \leq 10\text{kHz}$		0.001			0.001		%
θ_{jc}	Thermal Resistance Junction to Case	H Package K Package		12 2.3	15 3		12 2.3	15 3	$^{\circ}\text{C/W}$ $^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS (See Note 1) LT317AHV/LM317HV

SYMBOL	PARAMETER	CONDITIONS	LT317AHV			LM317HV			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{REF}	Reference Voltage	$I_{OUT} = 10\text{mA}$ $T_j = 25^\circ\text{C}$	1.238	1.250	1.262				V
		$3\text{V} \leq (V_{IN} - V_O) \leq 60\text{V}$ $10\text{mA} \leq I_{OUT} \leq I_{max}$, $P \leq P_{max}$	● 1.225	1.250	1.270	1.20	1.25	1.30	V
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	Line Regulation	$3\text{V} \leq (V_{IN} - V_{OUT}) \leq 60\text{V}$ (See Note 2)		0.005	0.01		0.01	0.04	%/V
			●	0.01	0.02		0.02	0.07	%/V
$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	Load Regulation	$10\text{mA} \leq I_{OUT} \leq I_{max}$, (See Note 2)							
		$V_O \leq 5\text{V}$		5	25		5	25	mV
		$V_O \geq 5\text{V}$		0.1	0.5		0.1	0.5	%
		$V_O \leq 5\text{V}$ $V_O \geq 5\text{V}$	● ●	20 0.3	50 1		20 0.3	70 1.5	mV %
	Thermal Regulation	$T_A = 25^\circ\text{C}$, 20msec Pulse		0.002	0.02		0.04	0.07	%/W
	Ripple Rejection	$V_O = 10\text{V}$, $f = 120\text{Hz}$ $C_{ADJ} = 0$	●	65			65		dB
		$C_{ADJ} = 10\mu\text{F}$	●	66	80	66	80		dB
I_{ADJ}	Adjust Pin Current		●	50	100		50	100	μA
ΔI_{ADJ}	Adjust Pin Current Change	$10\text{mA} \leq I_{OUT} \leq I_{max}$ $2.5\text{V} \leq (V_{IN} - V_{OUT}) \leq 60\text{V}$	●	0.2	5		0.2	5	μA
I_{min}	Minimum Load Current	$(V_{IN} - V_{OUT}) = 60\text{V}$	●	3.5	12		3.5	12	mA
		$(V_{IN} - V_{OUT}) \leq 15\text{V}$ K Package	●	1.5	2.2		1.5	2.2	A
		H Package	●	0.5	0.8		0.5	0.8	A
		$(V_{IN} - V_{OUT}) = 60\text{V}$, $T_j = 25^\circ\text{C}$ K Package H Package		0.1 0.03			0.1 0.03		A A
$\frac{\Delta V_{OUT}}{\Delta \text{Temp}}$	Temperature Stability	$0^\circ\text{C} \leq T_j \leq 125^\circ\text{C}$		1	2		1		%
$\frac{\Delta V_{OUT}}{\Delta \text{Time}}$	Long Term Stability	$T_A = 125^\circ\text{C}$		0.3	1		0.3	1	%
e_n	RMS Output Noise (% of V_{OUT})	$T_A = 25^\circ\text{C}$, $10\text{Hz} \leq f \leq 10\text{kHz}$		0.001			0.001		%
θ_{jc}	Thermal Resistance Junction to Case	H Package		12	15		12	15	$^\circ\text{C/W}$
		K Package		2.3	3		2.3	3	$^\circ\text{C/W}$

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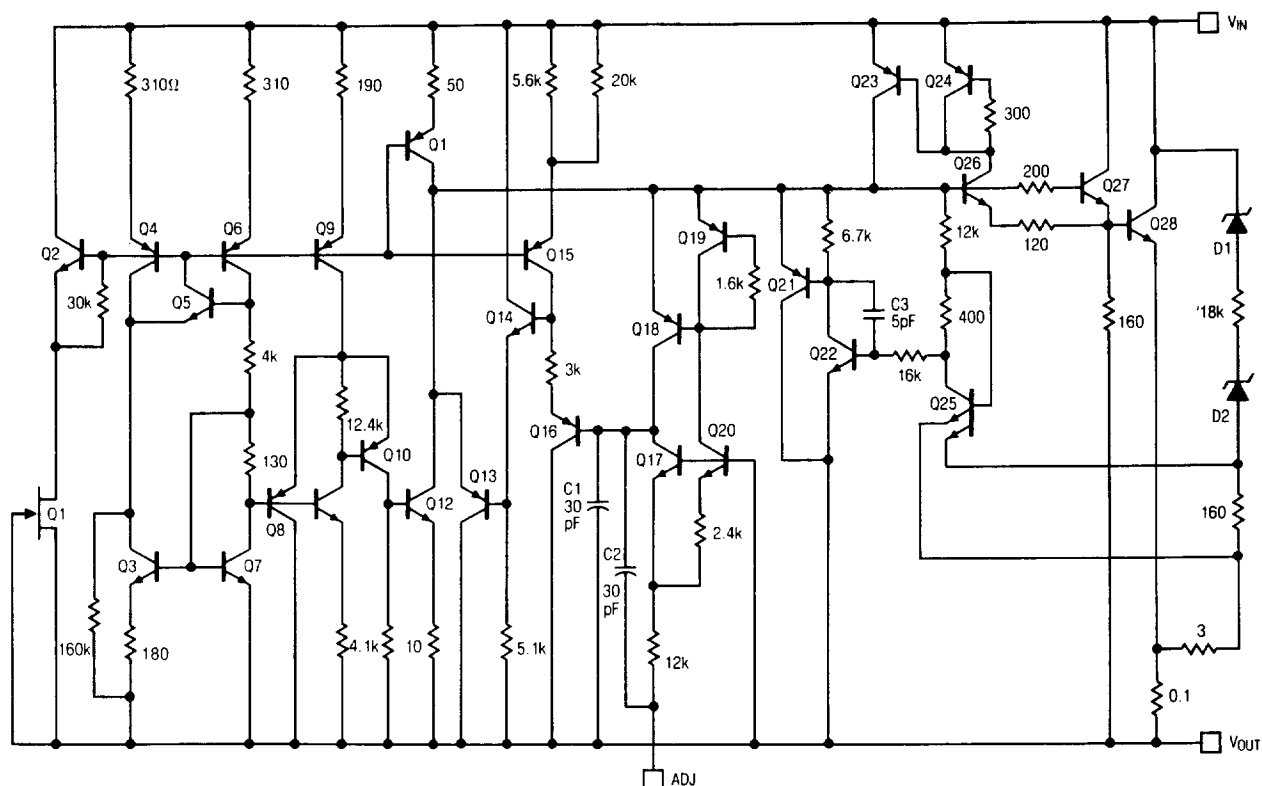
The ● denotes the specifications which apply over the full operating temperature range.

The shaded electrical specifications indicate those parameters which have been improved or guaranteed test limits provided for the first time.

Note 1: Unless otherwise specified, these specifications apply for $(V_{IN} - V_{OUT}) = 5\text{V}$; and $I_{OUT} = 0.1\text{A}$ for the TO-39 and $I_{OUT} = 0.5\text{A}$ for the TO-3 package. Although power dissipation is internally limited, these specifications are applicable for power dissipations of 2W for the TO-39, and 20W for the TO-3. I_{MAX} is 1.5A for the TO-3 package and 0.5A for the TO-39.

Note 2: Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.

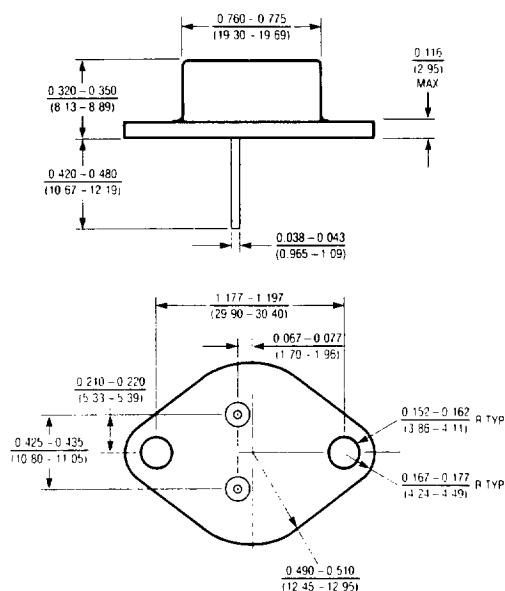
SCHEMATIC DIAGRAM



PACKAGE DESCRIPTION

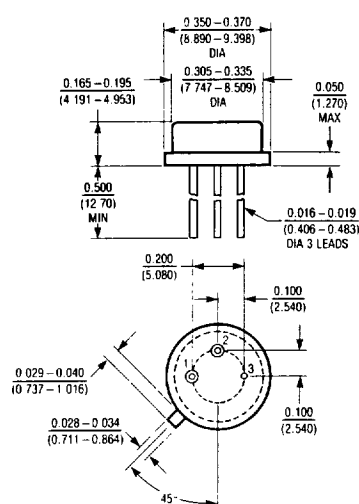
Dimensions in inches (millimeters) unless otherwise noted.

K Package TO-3 STEEL Metal Can



	T_J max.	θ_{JA}	θ_{JC}
117AHV 117HV	150°C	35°C/W	3°C/W
317AHV 317HV	125°C	35°C/W	3°C/W

H Package 3-Lead Metal Can



	T_J max.	θ_{JA}	θ_{JC}
117AHV 117HV	150°C	150°C/W	15°C/W
317AHV 317HV	125°C	150°C/W	15°C/W