

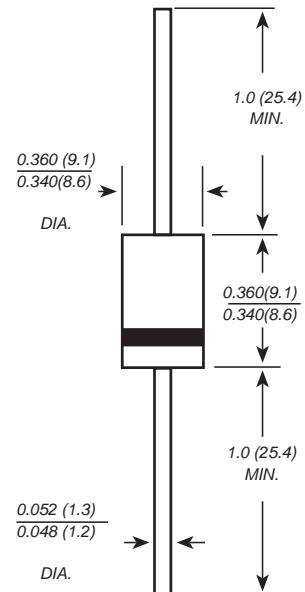


GENERAL PURPOSE SILICON RECTIFIER

Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ Construction utilizes void-free molded plastic technique
- ◆ Low reverse leakage
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed:
250°C/10 seconds, 0.375"(9.5mm) lead length, 5 lbs. (2.3kg) tension

R-6



Dimensions in inches and (millimeters)

Mechanical Data**Case :** JEDEC R-6 Molded plastic body**Terminals :** Solder plated, solderable per MIL-STD-750, Method 2026**Polarity :** Polarity symbol marking on body**Mounting Position :** Any**Weight :** 0.072 ounce, 2.05 gramsMaximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	SYMBOLS	8A05	8A1	8A2	8A4	8A6	8A8	8A10	UNITS
Marking Code		MDD 8A05	MDD 8A1	MDD 8A2	MDD 8A4	MDD 8A6	MDD 8A8	MDD 8A10	
Maximum repetitive peak reverse voltage	V _{RPM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current 0.375"(9.5mm) lead length at T _A =60°C	I _(AV)							8.0	A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}							600	A
Maximum instantaneous forward voltage at 8.0A	V _F							1.0	V
Maximum DC reverse current T _A =25°C at rated DC blocking voltage T _A =100°C	I _R							10.0 100	µA
Typical junction capacitance (NOTE 1)	C _J							150	pF
Typical thermal resistance (NOTE 2)	R _{θ JA}							10.0	°C/W
Operating junction and storage temperature range	T _J , T _{STG}							-50 to +150	°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

2. Thermal resistance from junction to ambient at 0.375"(9.5mm) lead length, P.C.B. mounted



Ratings And Characteristic Curves

FIG. 1 -- TYPICAL FORWARD CHARACTERISTIC

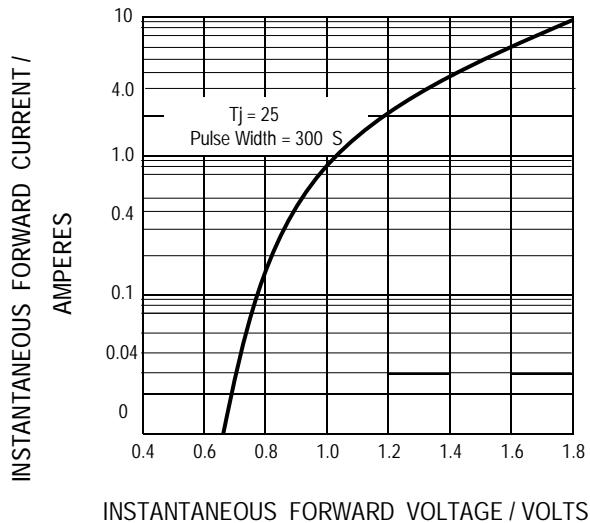


FIG. 2 -- TYPICAL JUNCTION CAPACITANCE

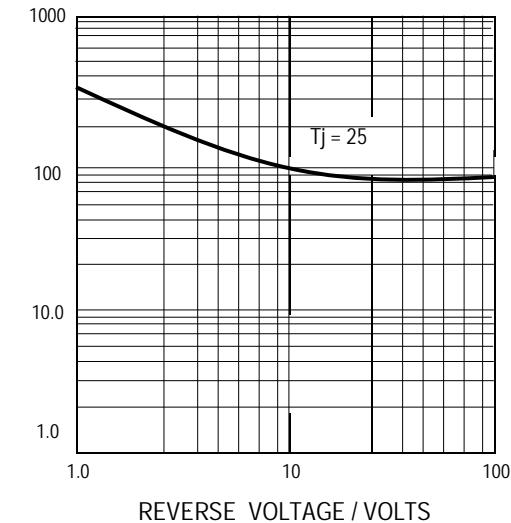


FIG. 3 -- FORWARD CURRENT DERATING CURVE

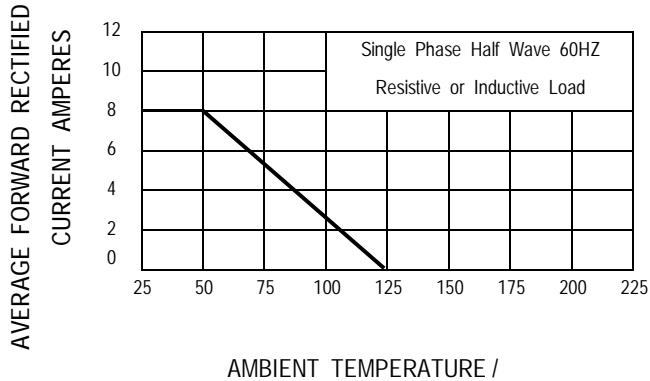
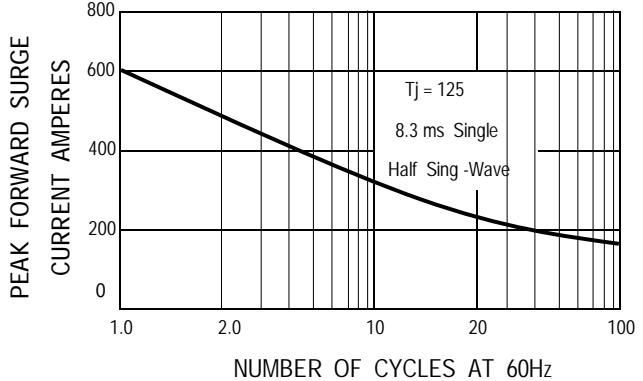


FIG. 4 -- PEAK FORWARD SURGE CURRENT



The curve above is for reference only.