

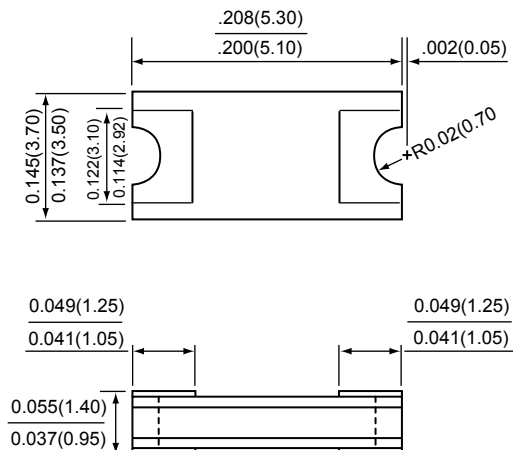
A suffix of "-C" specifies halogen & lead-free



2114

FEATURES

- * RoHS Compliant Product
- * Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- * Low profile package
- * Built-in strain relief
- * Metal to silicon rectifier , majority carrier conduction
- * Low power loss , High efficiency
- * High current capability
- * High surge capacity
- * For using in low voltage high frequency switching power supply, inverters , free wheeling , and polarity protection applications



MECHANICAL DATA

- * Case : Packed with FRP substrate and epoxy underfilled
- * Terminals : Solder Tin plated (Lead-Free) , solderable per MIL-STD-750, Method 2026
- * Polarity : Laser marking

* Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25 °C ambient temperature unless otherwise specified. Single phase half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOLS	SCD52	SCD54	SCD56	SCD5100	UNITS
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	20	40	60	100	V
Working Peak Reverse Voltage	V _{RMS}	20	40	60	100	V
Maximum DC Blocking Voltage	V _{DC}	20	40	60	100	V
Maximum Average Forward Rectified Current (See FIG. 1)	I _(AV)	5.0				A
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	120				A
Maximum Instantaneous Forward Voltage at 5.0A (Note1)	V _F	0.55		0.70	0.85	V
Maximum DC Reverse Current (Note1) Ta=25°C at Rated DC Blocking Voltage Ta=100°C	I _R	0.5				mA
		20		10		
Typical Thermal Resistance (Note 2)	RθJA	55				°C / W
	RθJL	17				
Operating Temperature Range	T _J	-50 ~ +125				°C
Storage Temperature Range	T _{STG}	-65 ~ +150				°C

NOTES:

1. Pulse test width PW=300 usec, 1% duty cycle.
2. Mounted on P.C. board with 0.2 x 0.2" (5.0 x 5.0mm) copper pad areas.

FIG.1 - FORWARD CURRENT DERATING CURVE

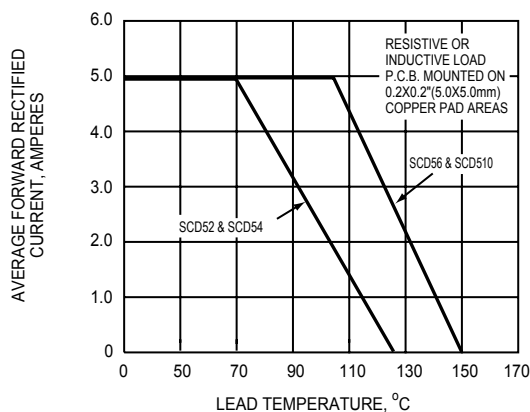


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

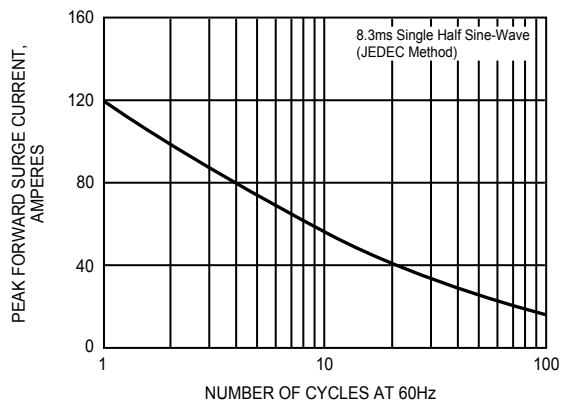


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

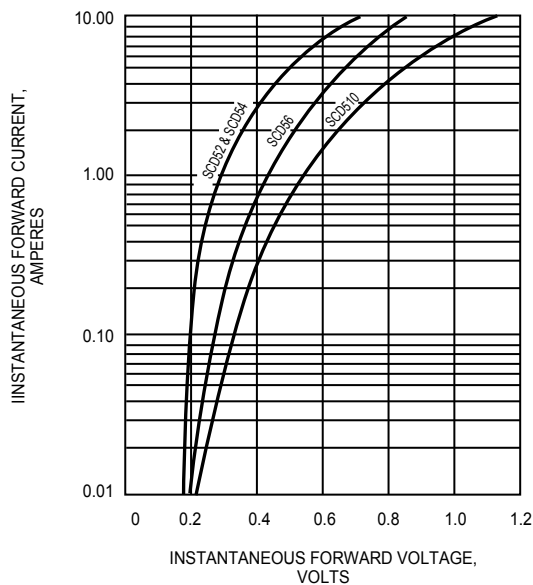


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

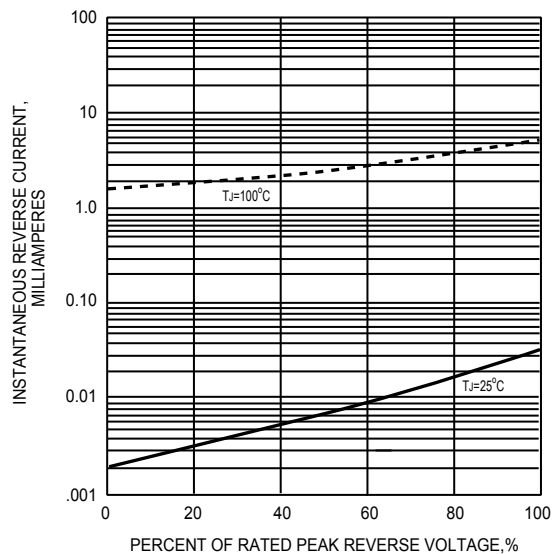


FIG.5 - TYPICAL JUNCTION CAPACITANCE

